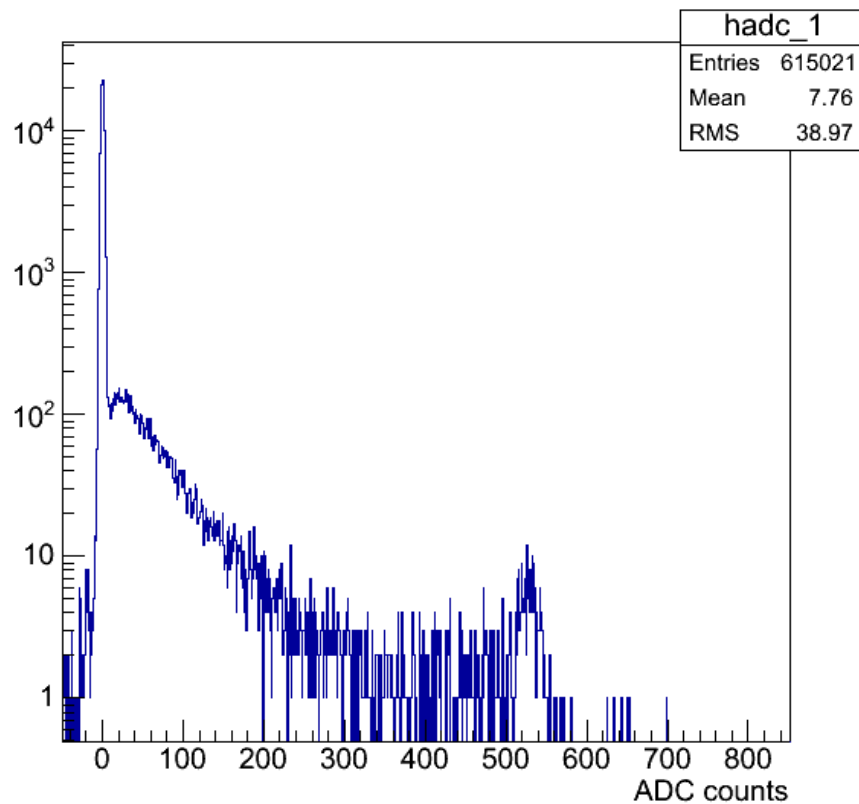


Analogue readout after interDIF modification

2012 testbeam iDIF

Standard 1 – ASU 40

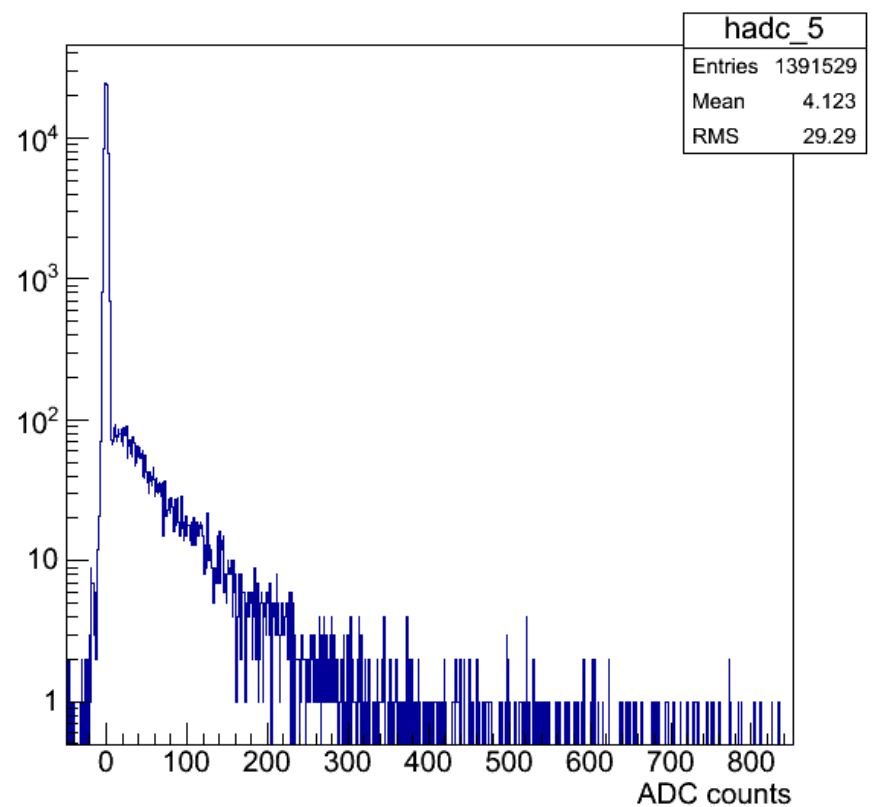
ADC distribution in chip in beam after pedestal sub in chamber 1



IDIF without multiplexer

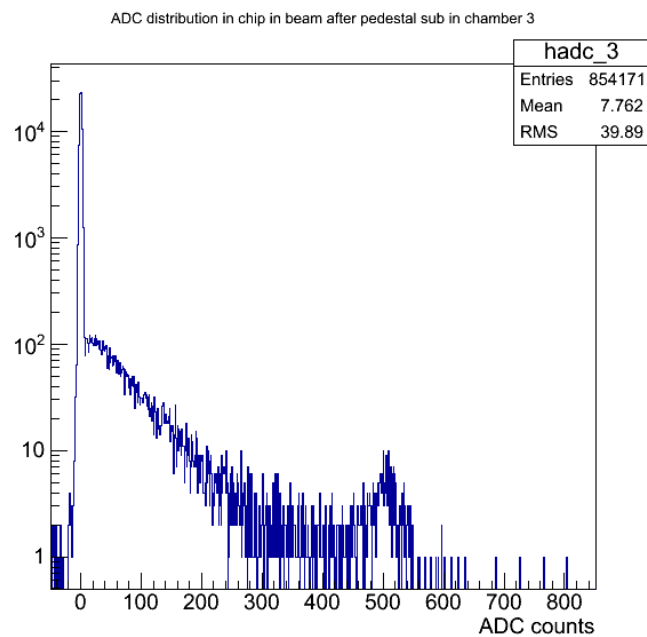
Standard 3 – ASU 42

ADC distribution in chip in beam after pedestal sub in chamber 5

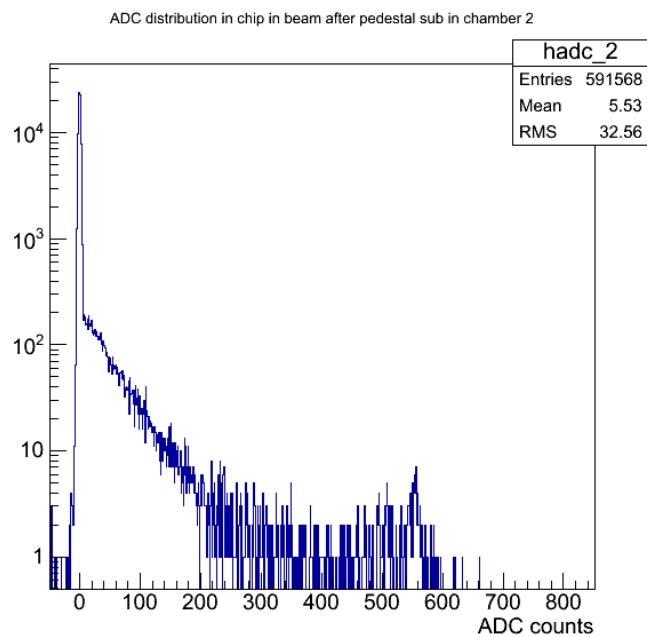


Analogue readout after interDIF modification

Standard 3 – ASU 42



Resistive 1 – ASU 43



Resistive 2 – ASU 44

