ANR SPLAM

Réunion technique
LAPP

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Outline

Summary of previous meetings

PCB review

Resistive prototypes

Other points

Outline

Summary of previous meetings

We all agree on the following points:

- build 16×16 cm^2 chambers;
- build 3 "reference" detectors, which will provide tracking;
- build 2 (3) resistive detectors.

Outline

PCB review

Alex, Cyril and Renaud have performed the "standard" PCB review on monday the 11^{th}:

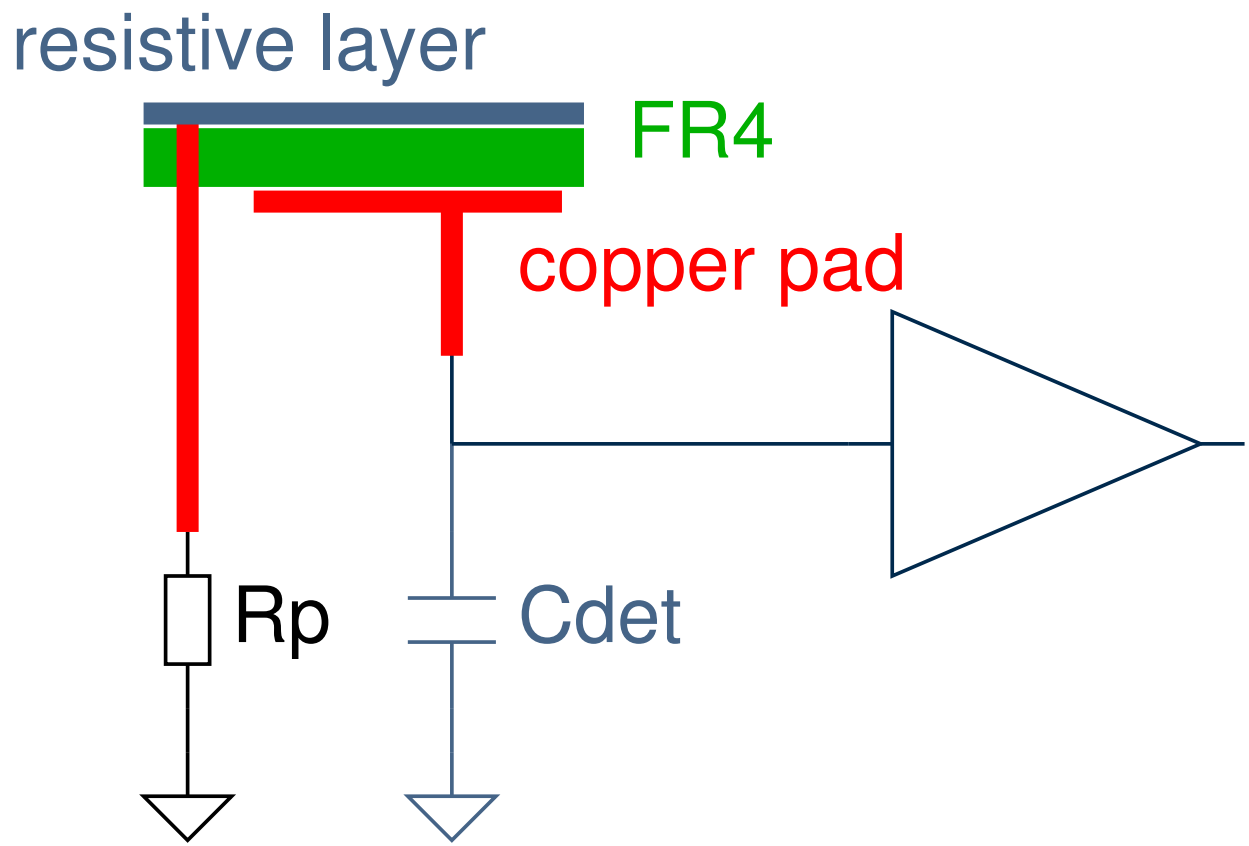
- Near finished, only few modification;
- Alex succeed in improving PCB wrt 32×48 ASU!
- Bypass modified;
- Add extra pull-up for I2C;
- Add 4 connection points for the mesh (ok for Rui);
- Add guard ring on the pad side.

The PCB with "resistive via" is well advanced too, and will profit of the "standard" review.

Outline

Resistive via

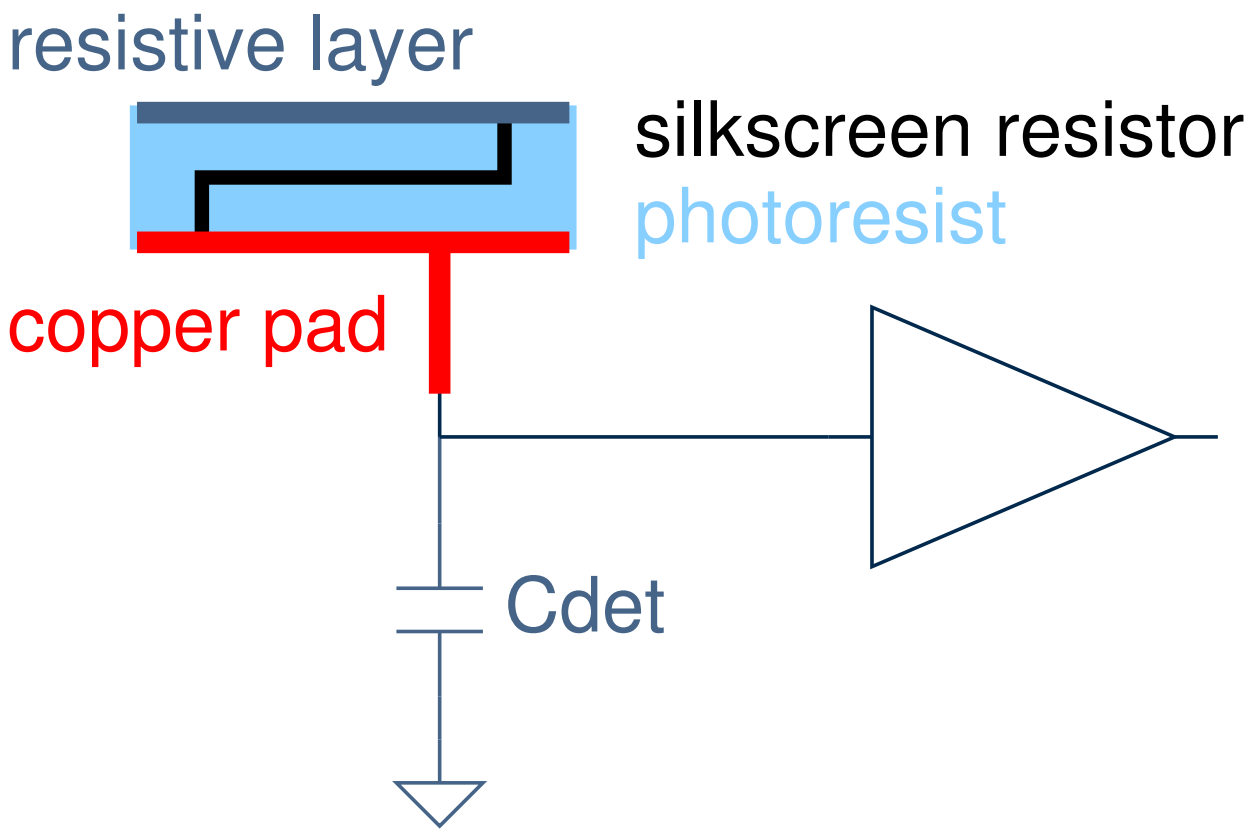
Proposed by LAPP:



Non standard PCB, allow pad to HV.

Buried resistor

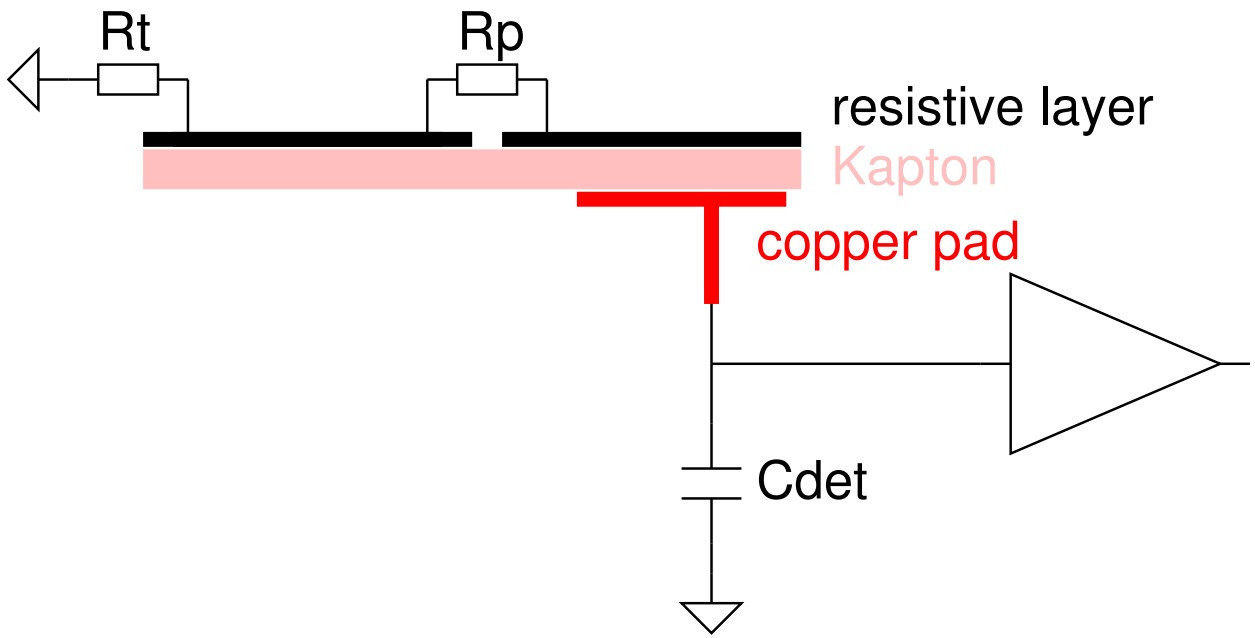
Inspired from COMPASS, proposed by Rui:



Standard PCB, but does not allow pad to HV.

Resistive pads

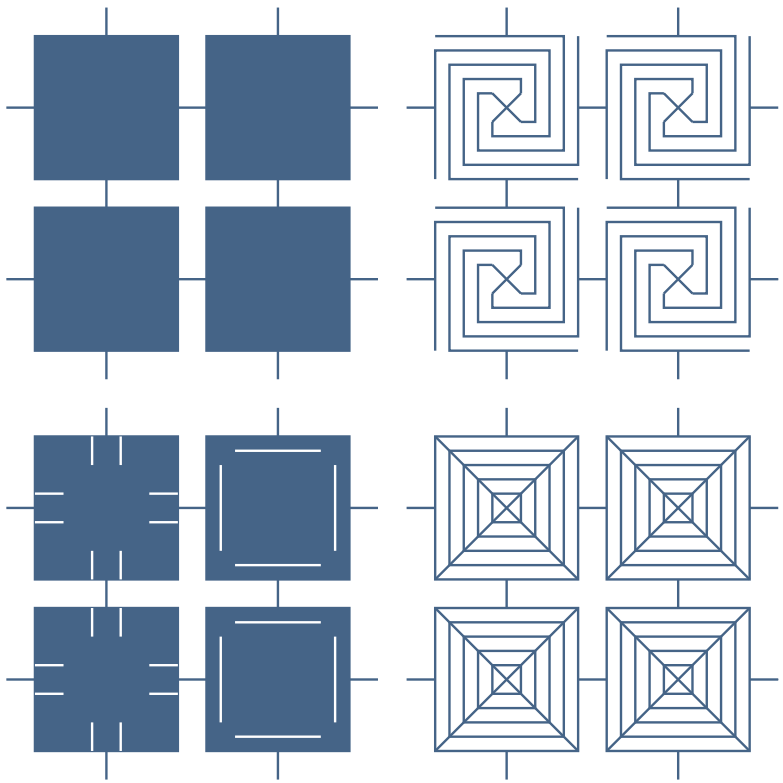
Proposed lask week, discussed with Rui



Standard PCB, inexpensive, allow HV on pad, but limited rate.

Proposed patterns

Proposed lask week, discussed with Rui:



Many options, may mix 4 pattern on 1 ASU!

Outline

Checks

- Interdifs?
- Cathode 9 µm instead of 17 µm?
- Check terminating boards.