

Session Program

18–22 May 2026



XIII FRONT-END ELECTRONICS WORKSHOP PARIS France

session 7 : HEP

LPNHE, Amphithéâtre Charpak
Campus Pierre et Marie Curie Patio 22-33 Rez-de-chaussée (RC) - Niveau Saint Bernard (SB) - Porte 02 4 place Jussieu
75005 Paris

Tuesday 19 May

16:45

session 7 : HEP

Session |

Location: LPNHE, Amphithéâtre Charpak, Campus Pierre et Marie Curie Patio 22-33 Rez-de-chaussée (RC) - Niveau Saint Bernard (SB)
- Porte 02 4 place Jussieu 75005 Paris

16:45-17:15 **Frontend chip development for the CEPC electronic system**

Speaker

Wei Wei

17:15-17:45 **SALSA: a new versatile readout chip for MPGD**

Speaker

Christophe Flouzat

17:45-18:15 **"High performance TDC and ADC blocks at ultra-low power"**

Speaker

Marek Idzik

18:15