

Topics

- Planar sensors
- 3D Sensors
- CMOS MAPS
- Electronics
- LGAD
- Technology
- Applications
- Simulations
- System Issues
- Wide bandgap

semiconductor detectors

The goal of is to bring together experts on sensor technology, front-end electronics, system issues, detector applications, etc. for discussions of the present state of the art, establishment of requirements of the fields and future programs.



The workshop will consist of invited talks and contributed talks and posters, with ample time for discussions.

Abstract submission deadline: December 1st 2025

Venue



Perugia is the capital city of the Umbria region in central Italy, near the Tiber river.





The workshop will be held in Palazzo dei Priori, Sala dei Notari, downtown Perugia.



Proceedings

The Workshop Proceedings will published on JINST.





More information at:

https://indico.cern.ch/event/1586892/overview