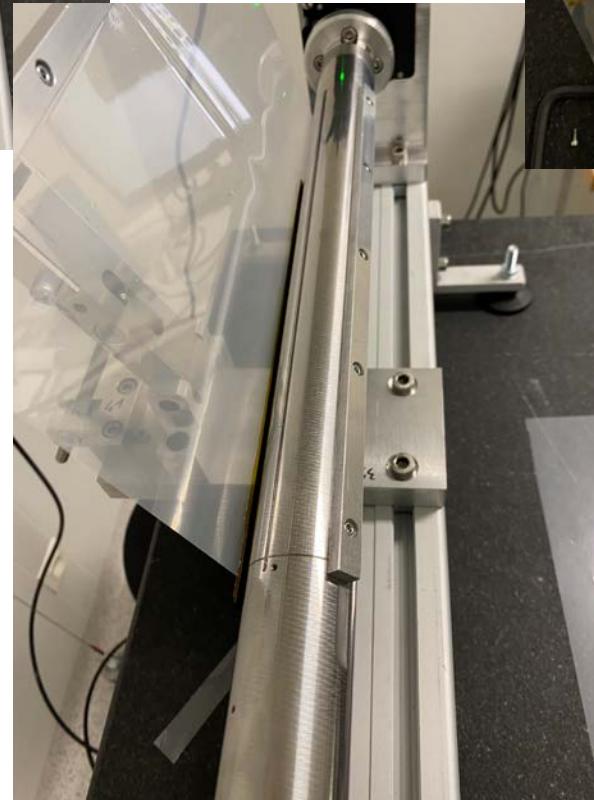
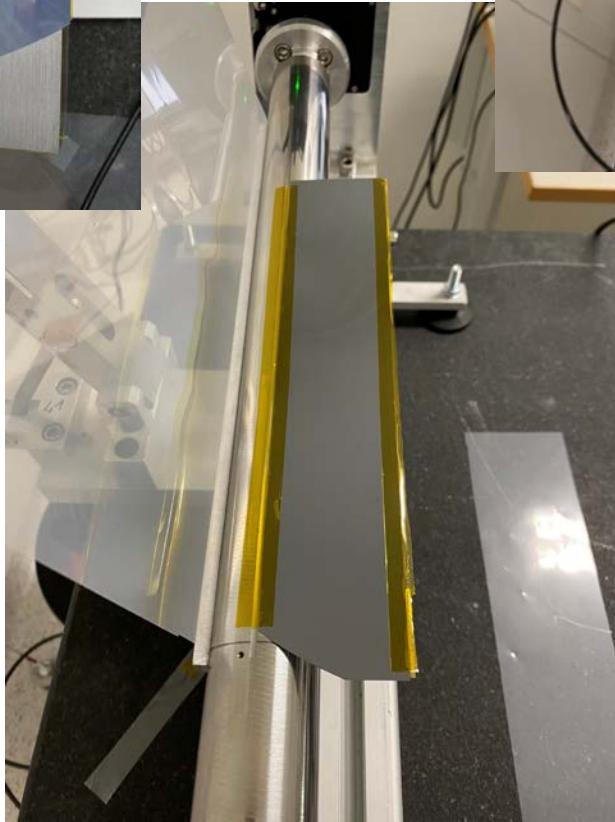
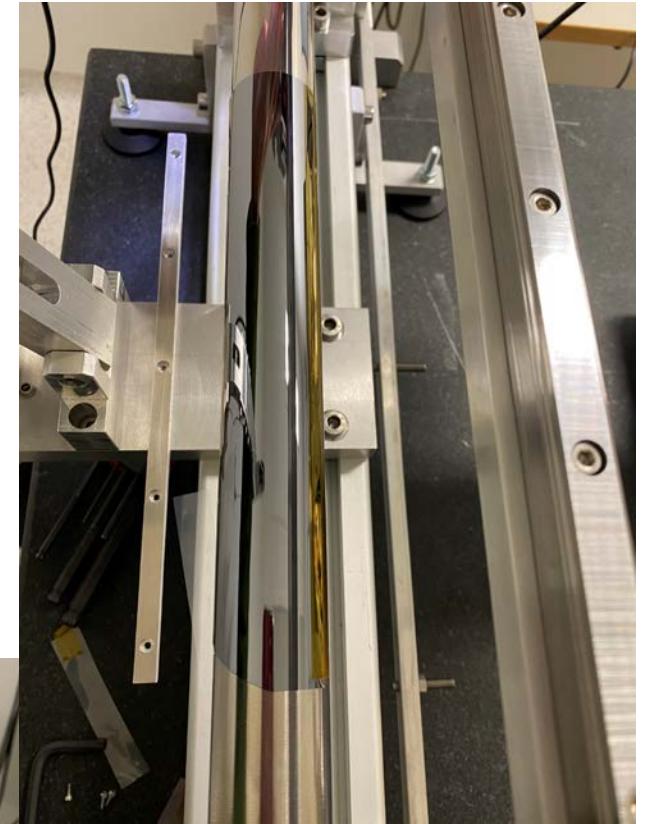
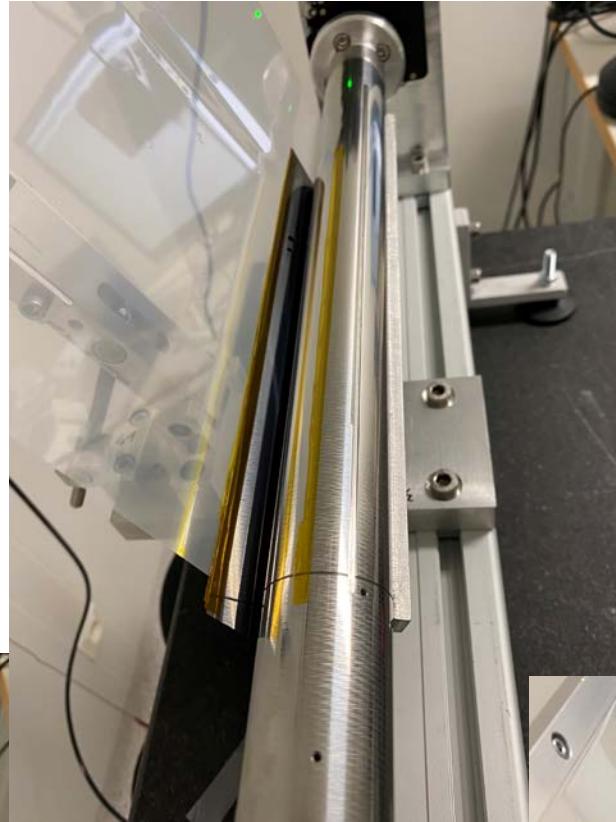


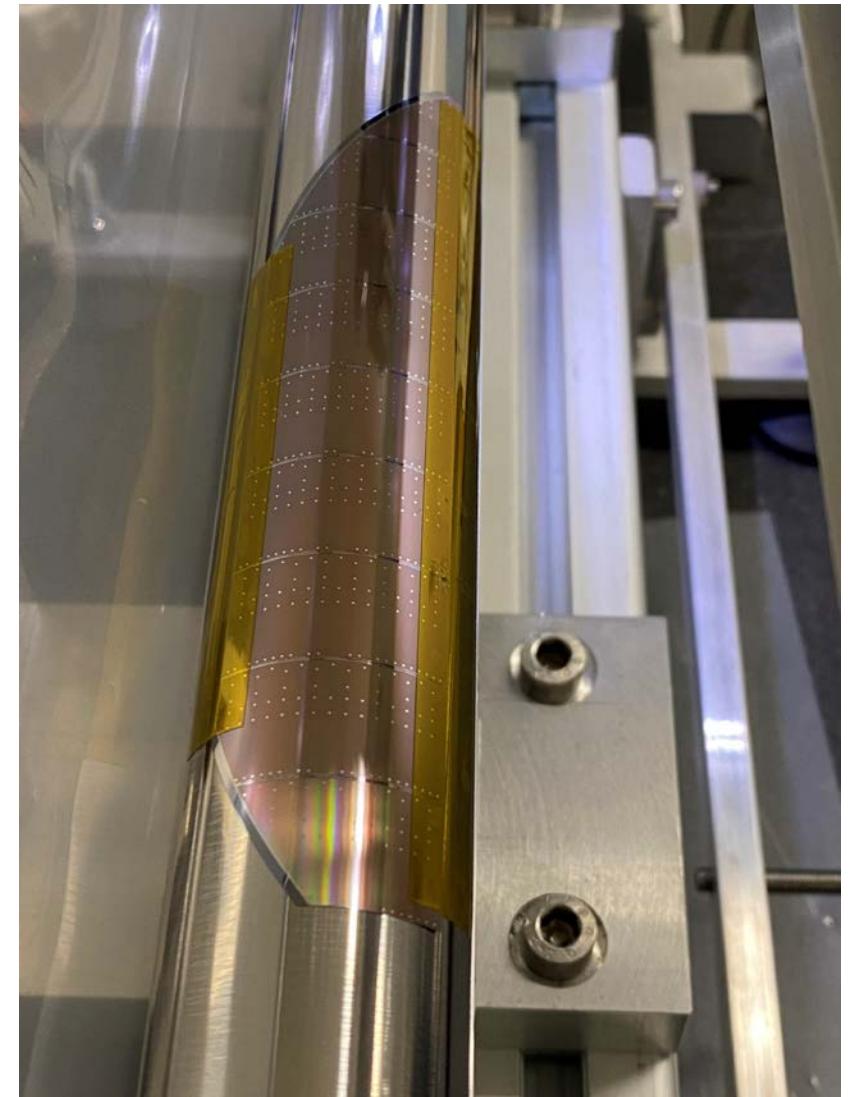
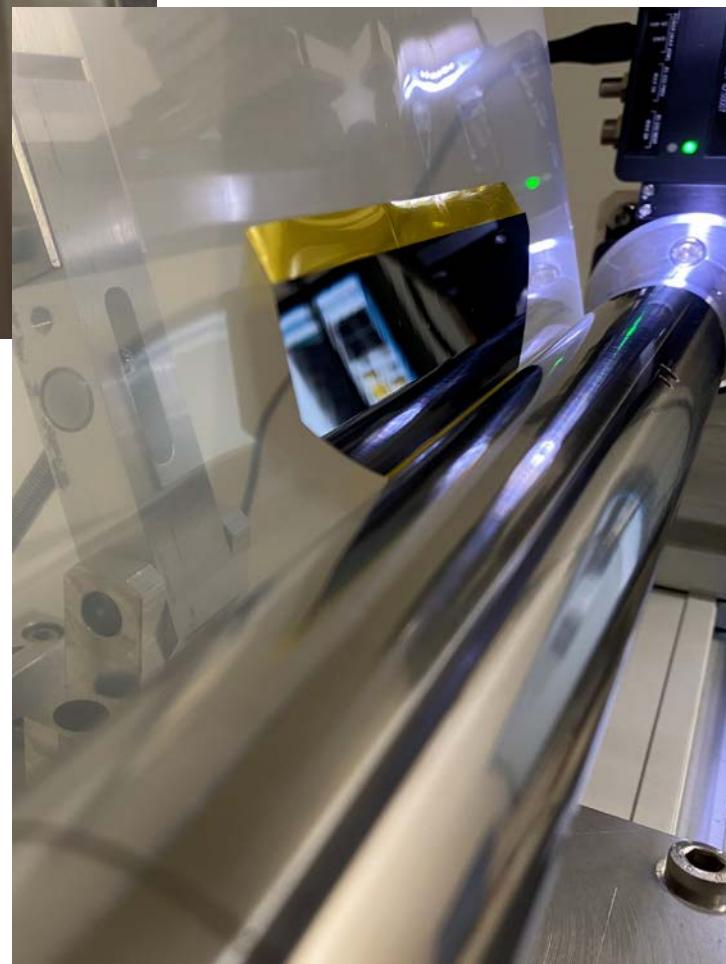
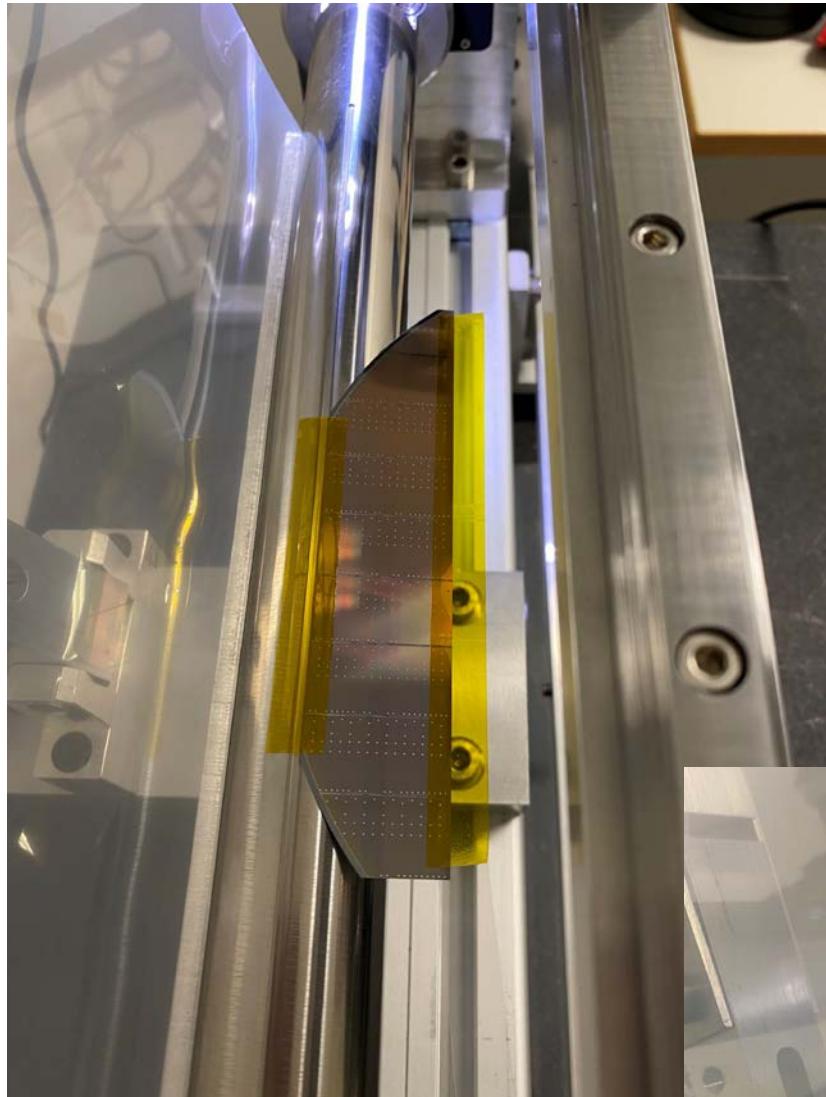
# Cintrage demi-lune de silicium , mandrel Ra=0,6 , $\phi$ 36mm



Silicium sans process,  
dimension 200x50mm  
épaisseur 50 $\mu$ m,  
10 Itérations de pliage- dépliage  
et maintien en position 3jours.

Pas de casse du silicium.

# Cintrage demi-lune pad-Alpide, mandrel Ra=0,6 , $\phi 36\text{mm}$

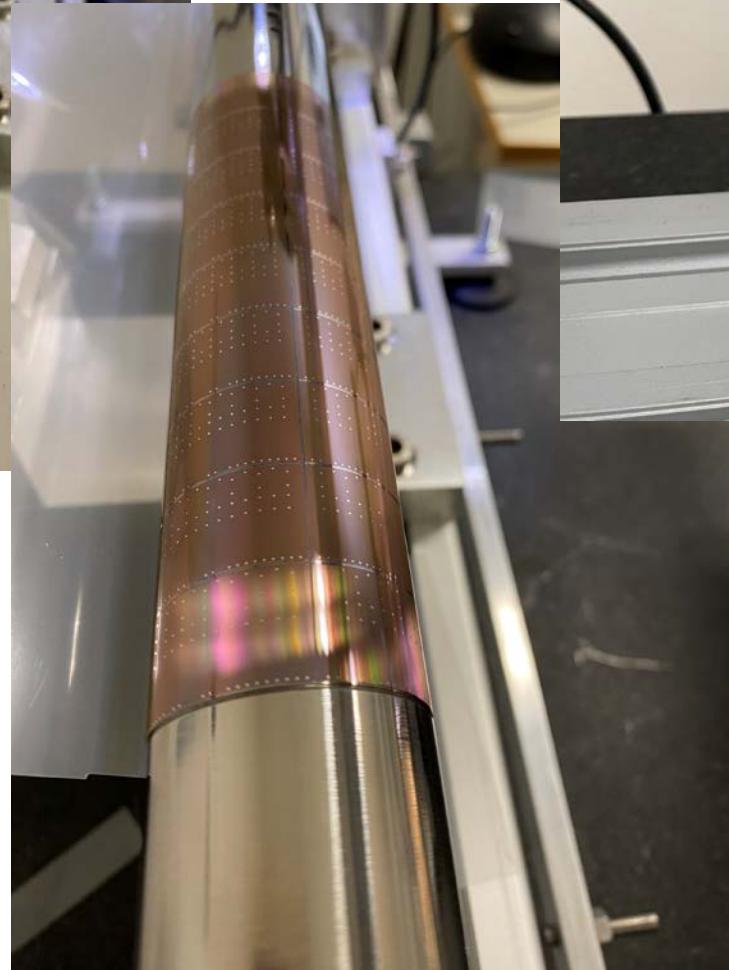
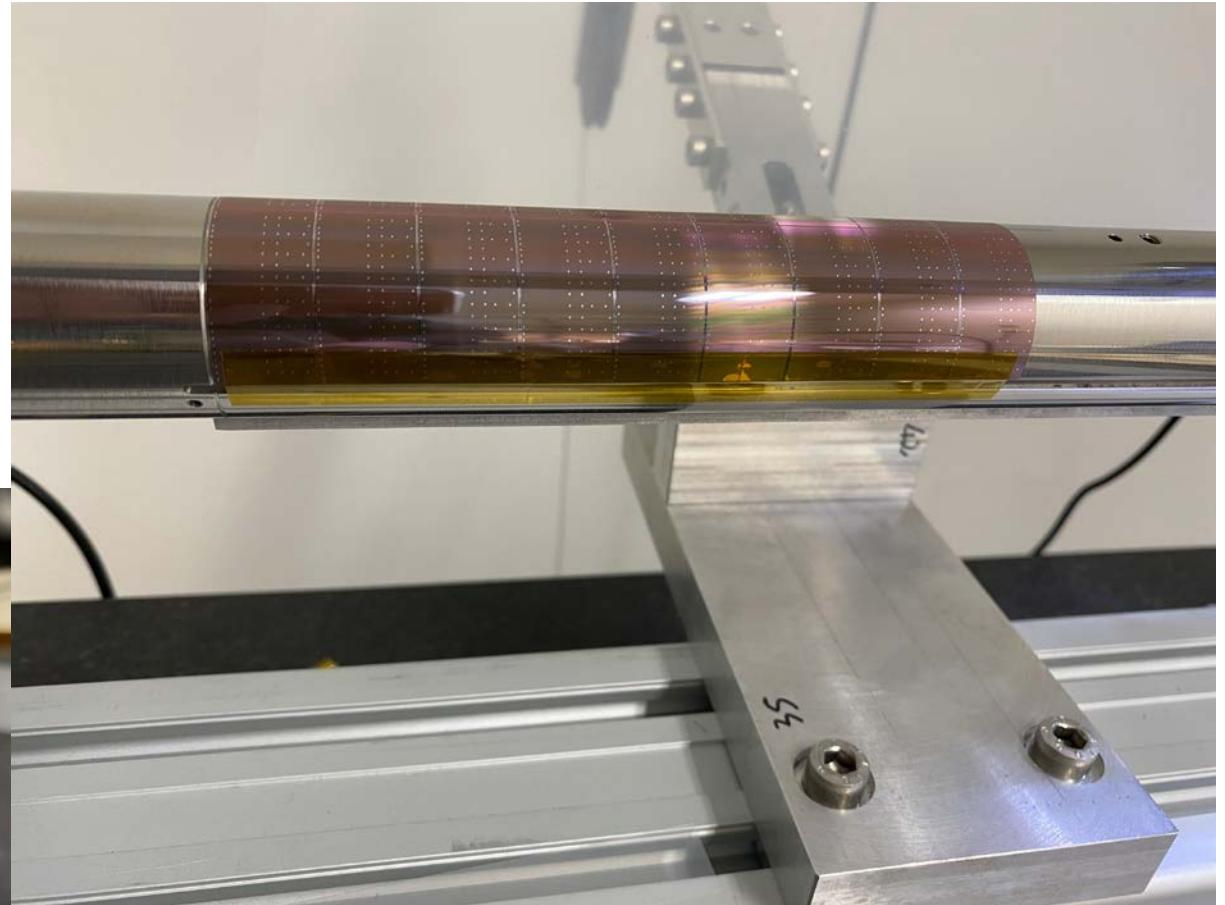
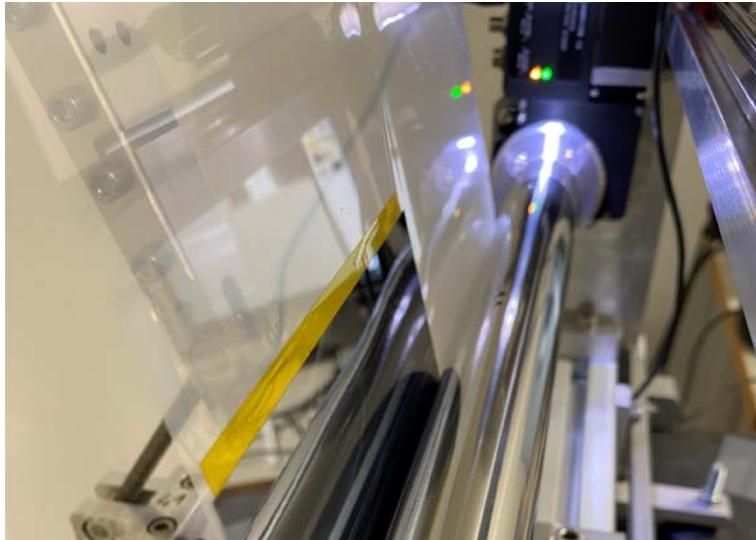


Structure de test Pad super-  
alpide, dimension 130x50mm  
épaisseur 40 $\mu\text{m}$ ,

Maintient en position 3jours,  
puis dépliage.

Pas de casse du silicium.

# Cintrage pad-Alpide, mandrel Ra=0,6 , $\phi 36\text{mm}$

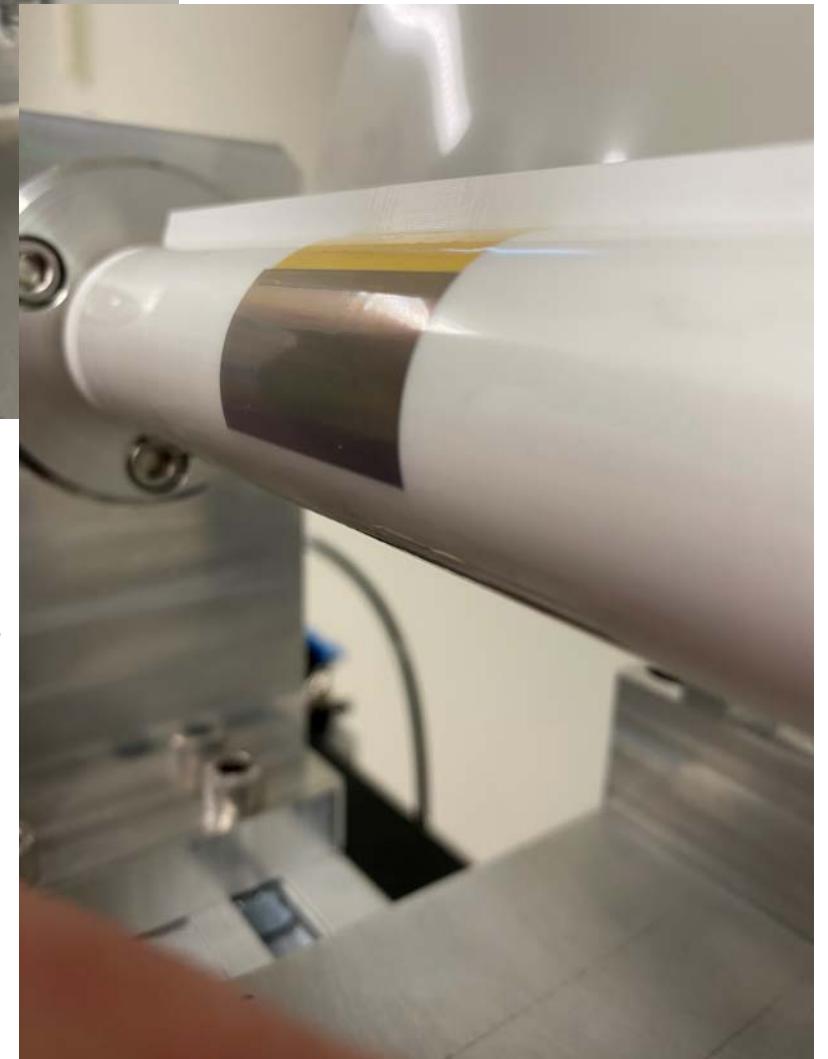
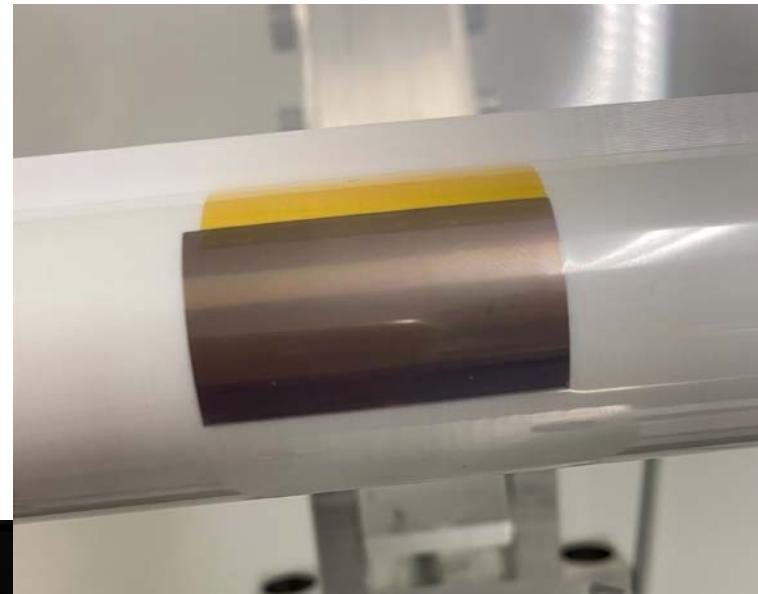
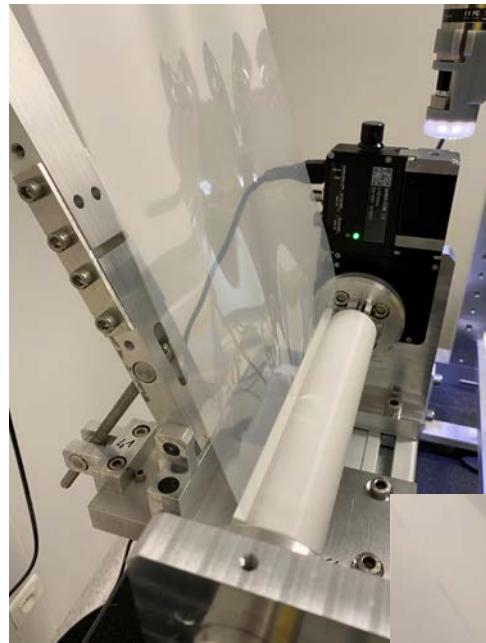


Pad super-alpide, dimension  
130x50mm épaisseur 40 $\mu\text{m}$ ,

Pliage puis dépliage.

Pas de casse du silicium.

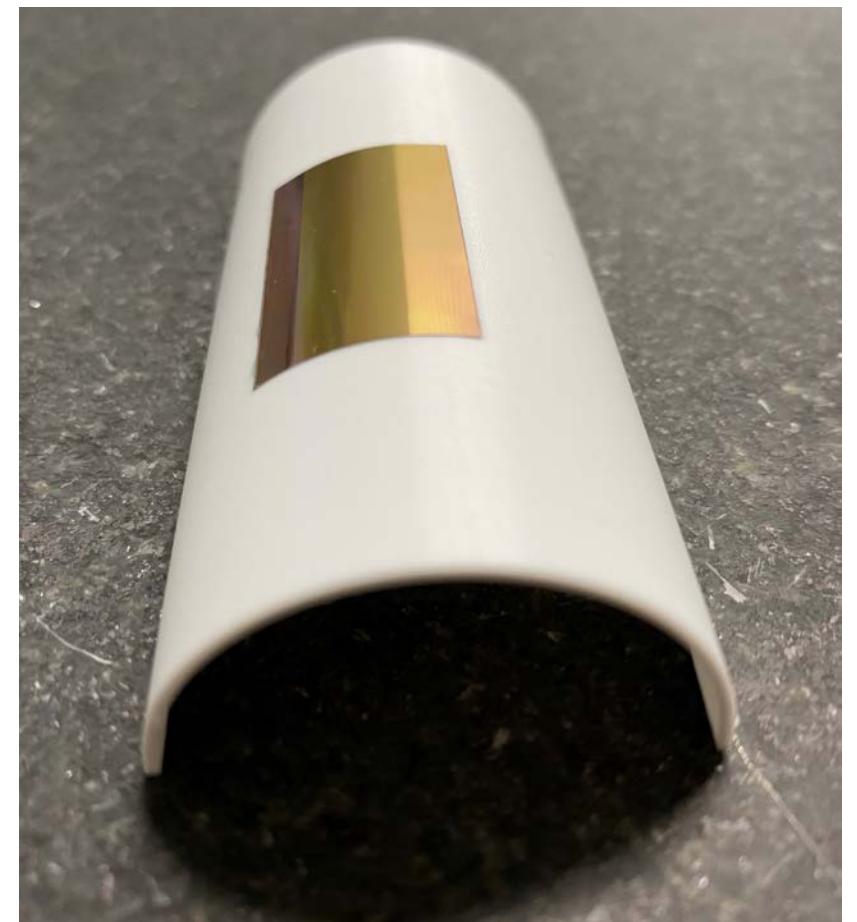
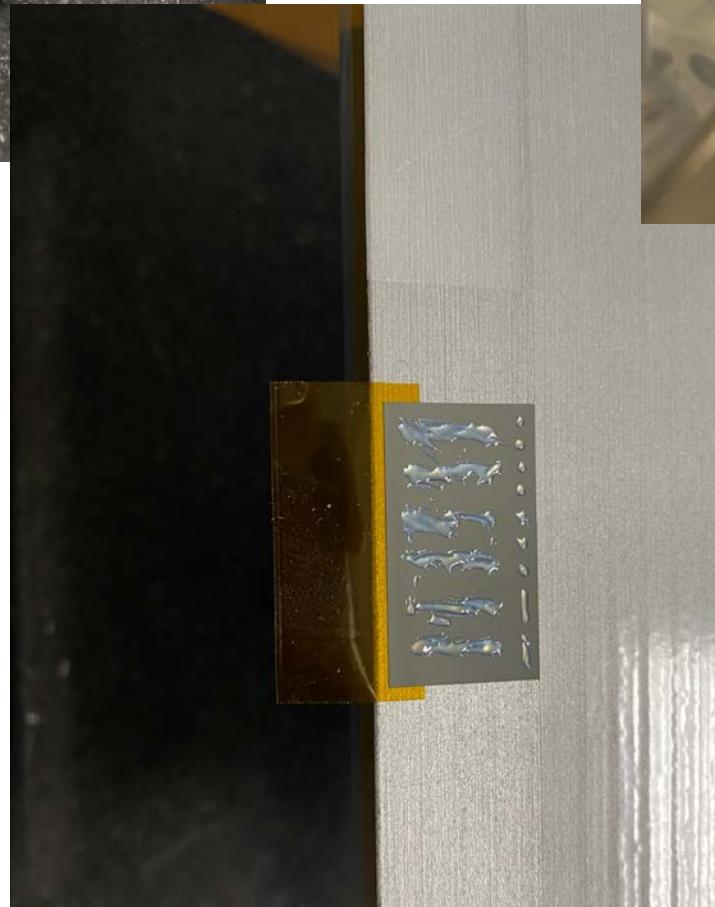
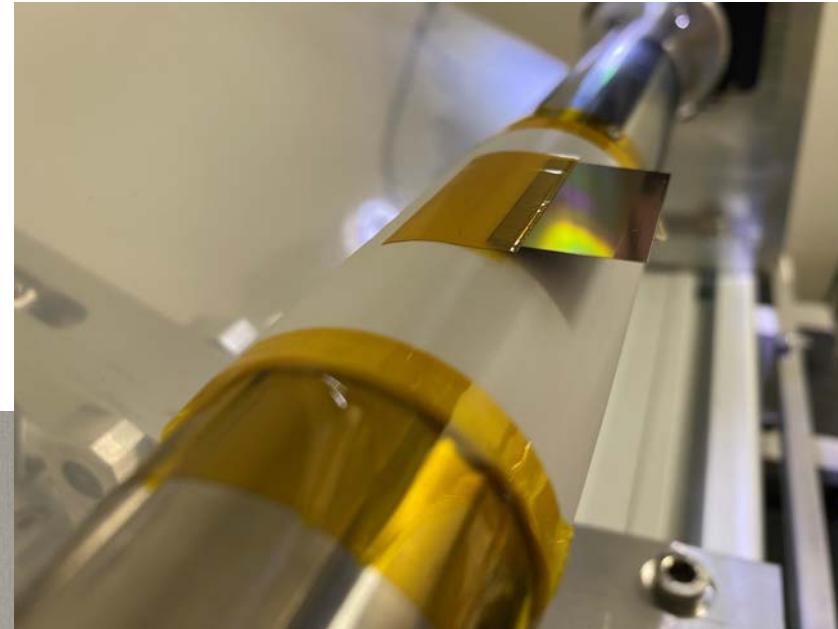
# Cintrage Mimosis, mandrel impression 3D , Ra=1,5 , $\phi$ 30mm



Mimosis2.1 , dimension  
31x17mm épaisseur 50 $\mu$ m,  
Maintient en position 3jours,  
puis dépliage.

Pas de casse du silicium.

# Cintrage Mimosis, mandrel impression 3D , Ra=1,5 , $\phi$ 37mm

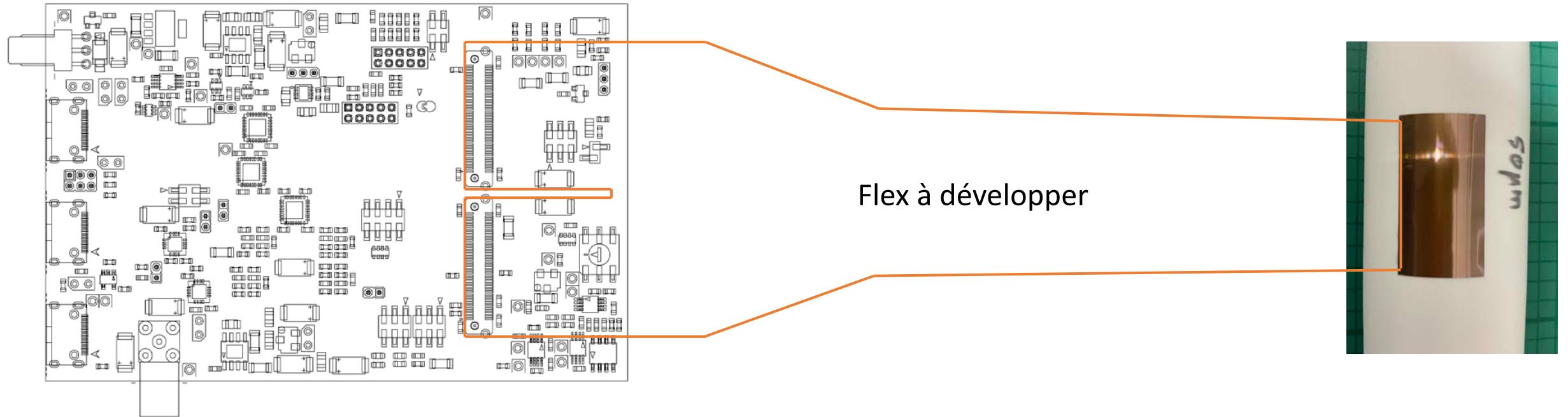


Mimosis2.1 , dimension  
31x17mm épaisseur 50 $\mu$ m,  
Collé avec Araldite sur demi-  
coquille

Pas de casse du silicium.

# Projet de test des chip mimosis courbé

Carte Msis\_prox



Pour le test d'ensemble de 2 ou 4 capteurs il suffit de multiplier le nombre de carte msis\_prox et de flex.