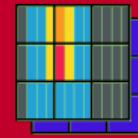


# Phase-1 Upgrade of the ATLAS Level-1 Calorimeter Trigger EPS-HEP 2025

Marseille, 08.07.2025

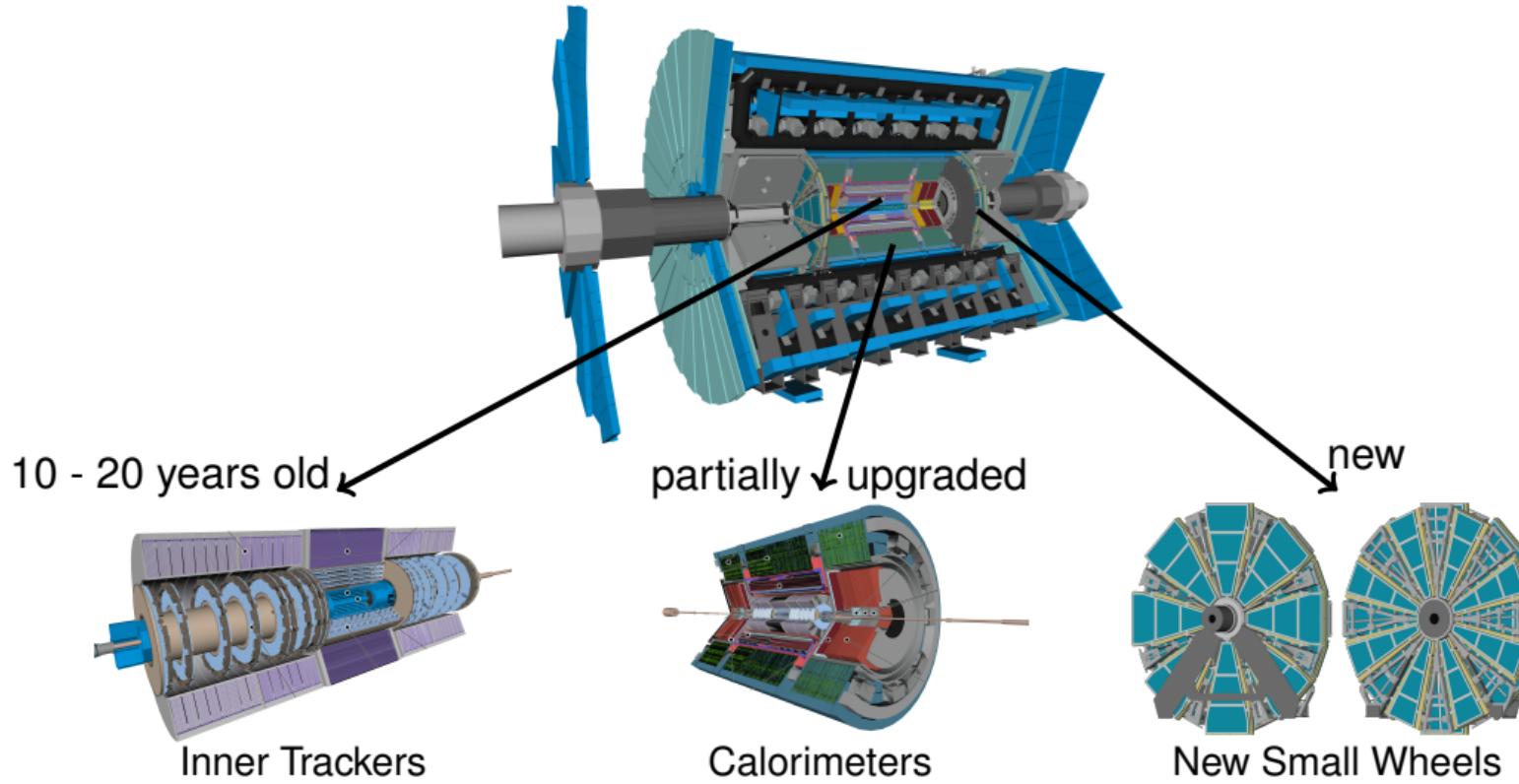
Ralf Gugel (JGU Mainz)  
on behalf of the ATLAS Collaboration

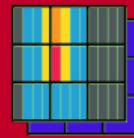




# ATLAS in Run 3

Spanning decades of evolving technology,

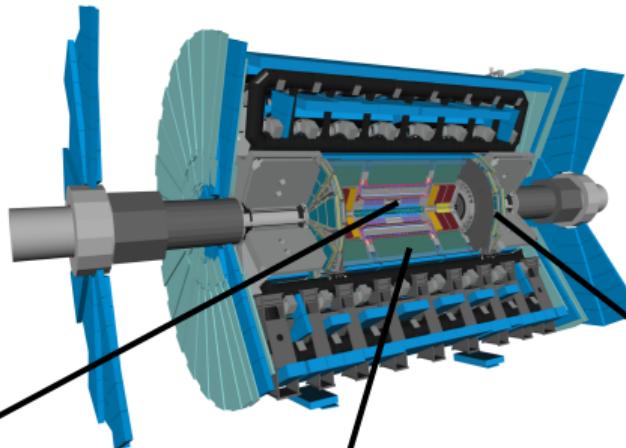
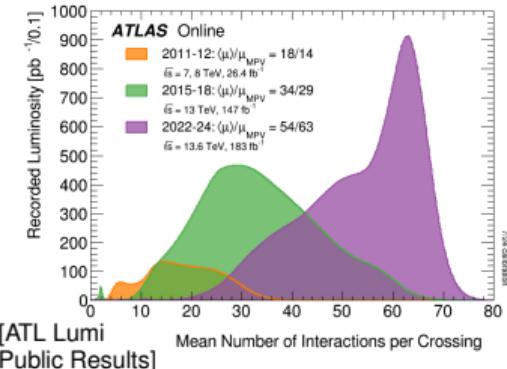




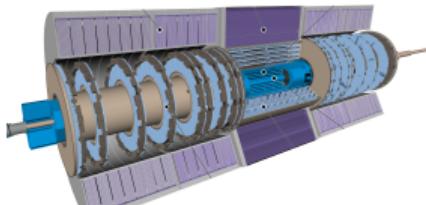
# ATLAS in Run 3

Spanning decades of evolving technology,

experimental conditions,

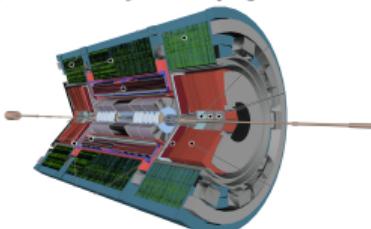


10 - 20 years old



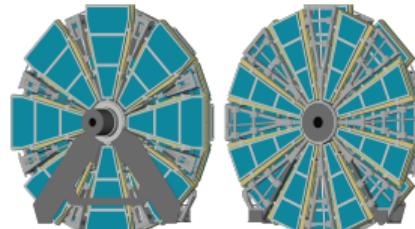
Inner Trackers

partially upgraded



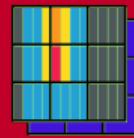
Calorimeters

new



New Small Wheels

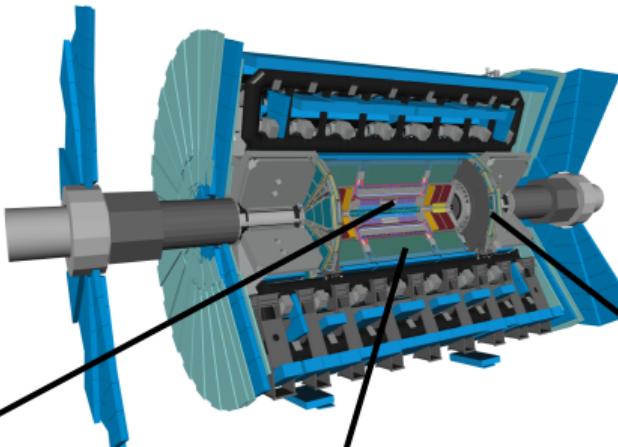
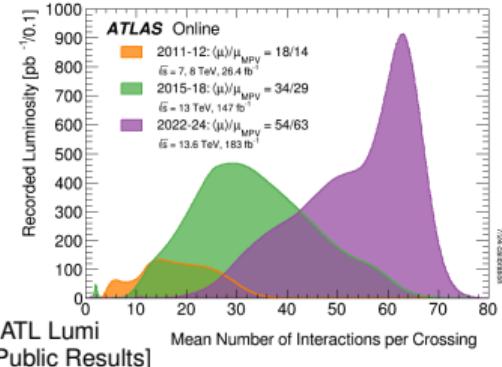
[JINST 19 P05063]



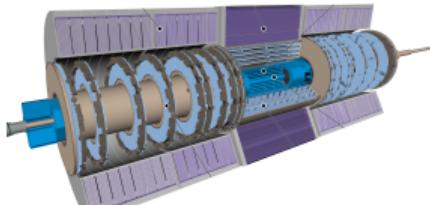
# ATLAS in Run 3

Spanning decades of evolving technology,

experimental conditions,

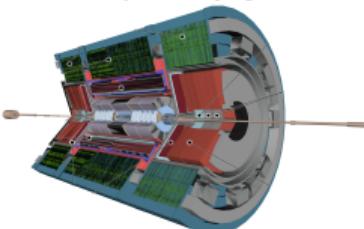


10 - 20 years old



Inner Trackers

partially upgraded



Calorimeters

opportunities & challenges

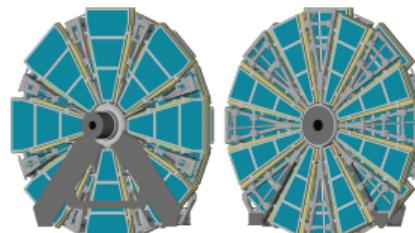
+ signal events

! background events

! event complexity

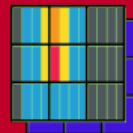
! legacy systems

new



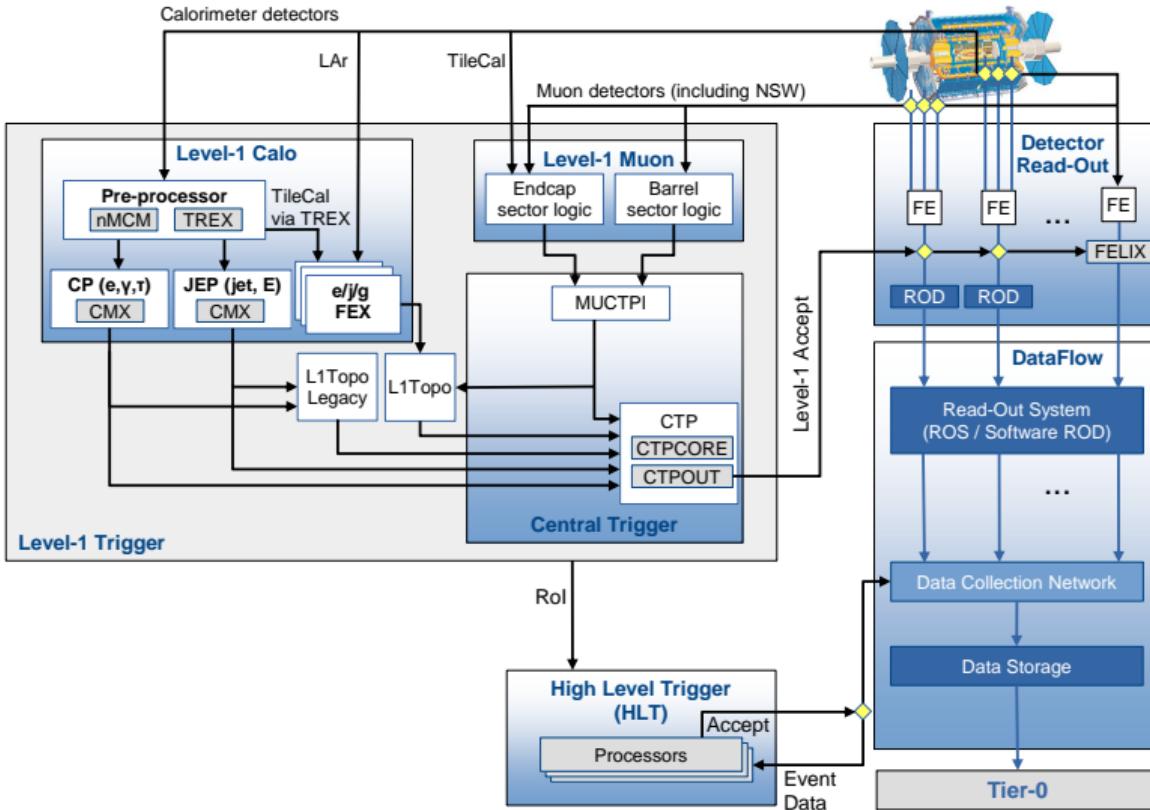
New Small Wheels

[JINST 19 P05063]



# ATLAS TDAQ in Run 3

Calorimeter detectors



collisions @ 40 MHz

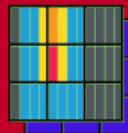
**Level-1**  
limiting: FE buffers  
→ hard real-time  
latency:  $\lesssim 2.5 \mu\text{s}$

**L1Accept @ 100 kHz**

**HLT**  
limiting: CPU cores &  
readout buffers  
→ soft real-time  
avg. latency:  $\mathcal{O}(1 \text{ s})$

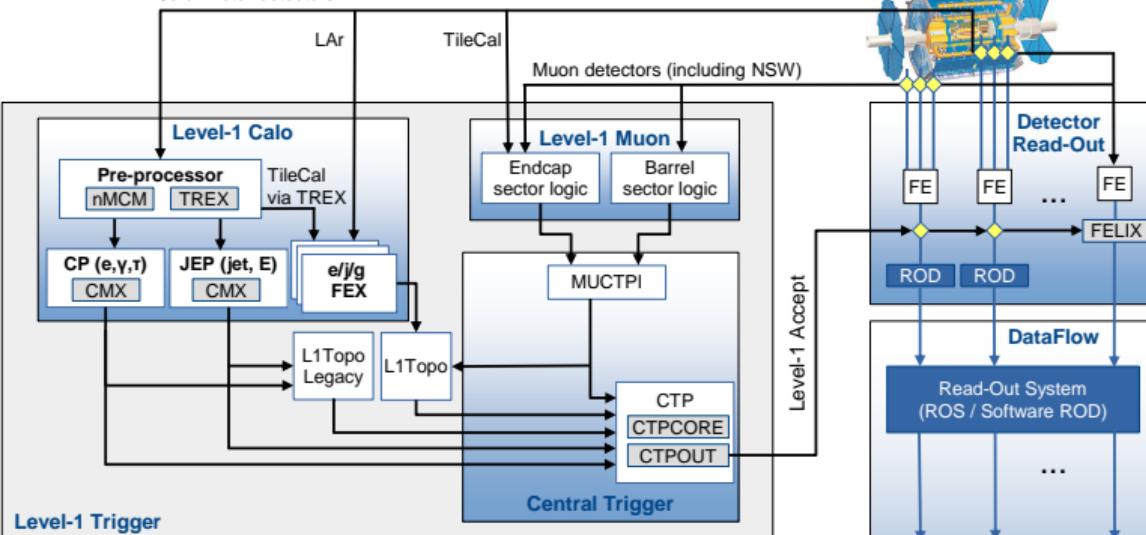
**recording**  $\sim 3 \text{ kHz}$

[JINST 19 P06029]

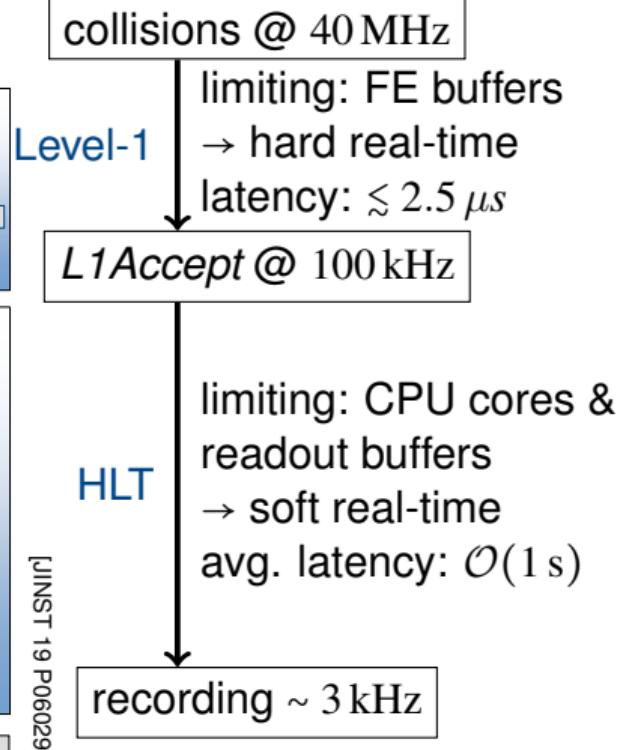


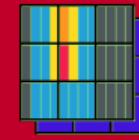
# ATLAS TDAQ in Run 3

Calorimeter detectors



"Regions of Interest":  
HLT speed up via  
seeded reconstruction

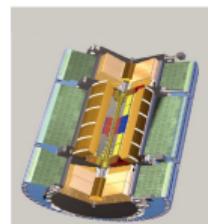




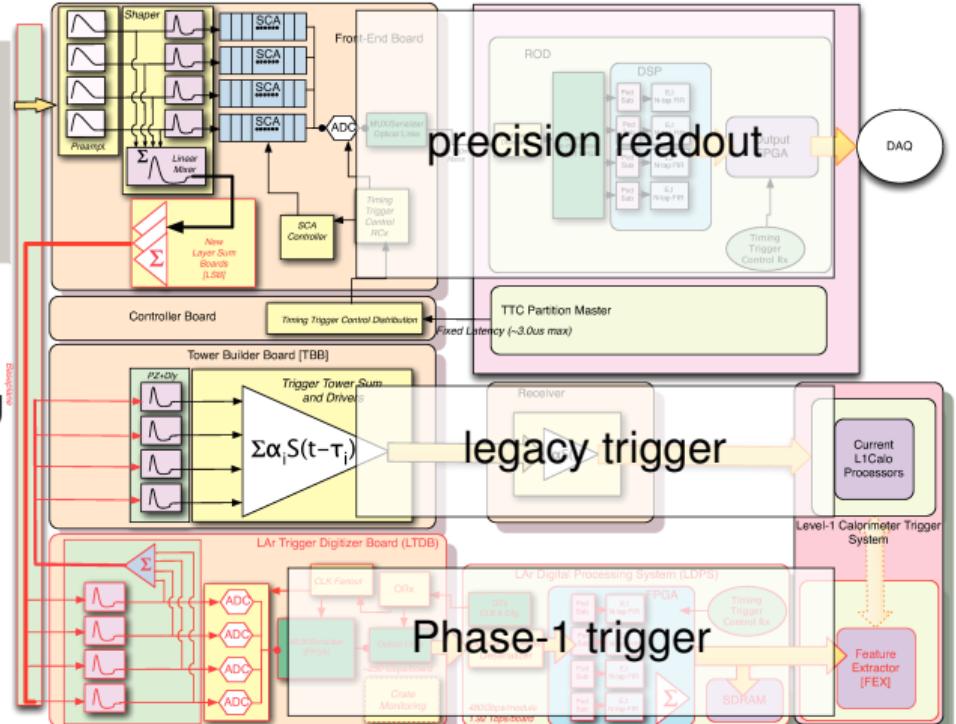
# Calorimeter-side Upgrades

## Liquid Argon calorimeter:

- ▶ EM calorimetry ( $|\eta| \lesssim 1.6$ )
- ▶ EM+had ( $|\eta| \gtrsim 1.6$ )
- ▶ Legacy: analog electrical  
→ L1Calo trigger (off detector)
- ▶ Phase 1: on-detector digitization  
→ off-detector processing (energy, timing)
- ▶ Simultaneous operation during commissioning



## on detector

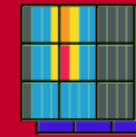


optical fibers  
~ 25 Tbps

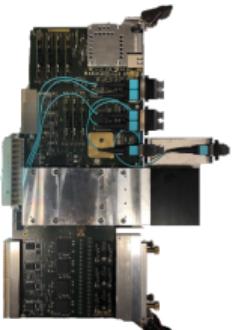


LAr trig. processing board  
(off detector)

LAr trig. digitizer board  
(on detector)  
[photos courtesy of LAr team]



# Level-1 Calorimeter Trigger: Inputs

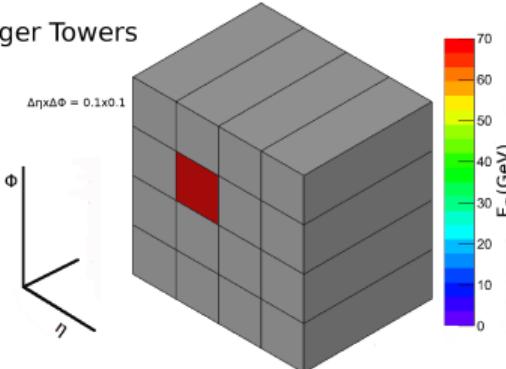


- ▶ Tile Rear EXtension for legacy pre-processors  
→ fiber-optical inputs for Ph-1 Feature EXtractors
- ▶ Digital inputs from LAr  
→ 10× increased granularity



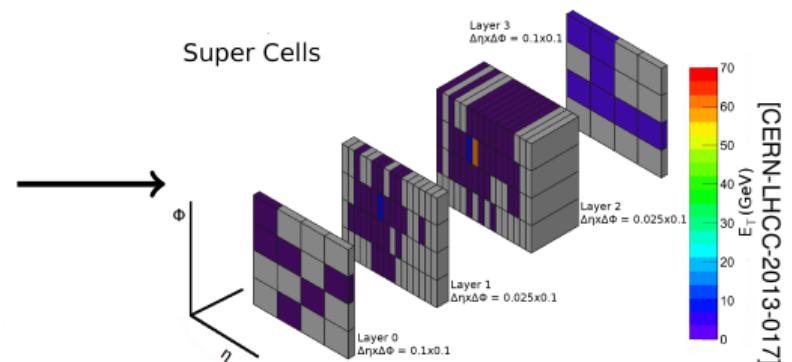
Tile / legacy LAr granularity  
 $\Delta\eta \times \Delta\phi = 0.1 \times 0.1$  Trigger Towers

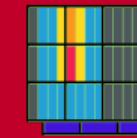
Trigger Towers



Phase-1 LAr (EM) granularity  
10 Super Cells per Trigger Tower

Super Cells



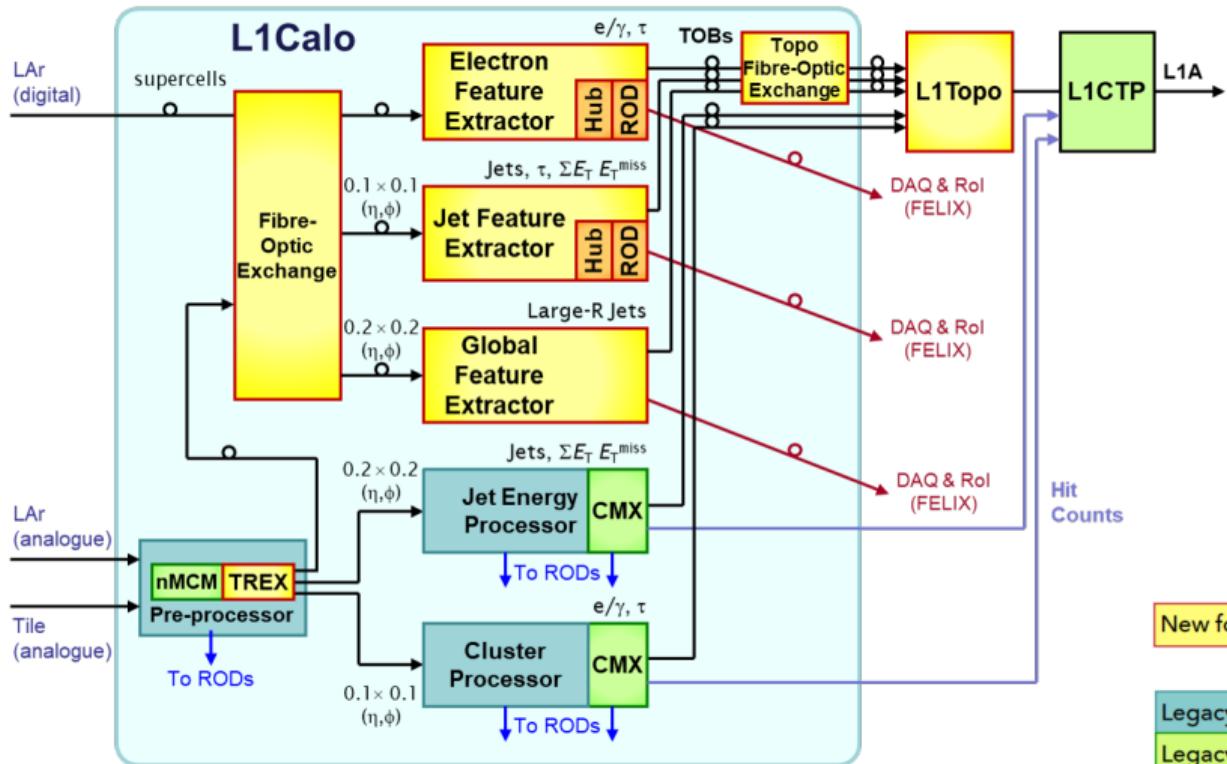


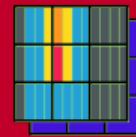
# L1Calo Phase-1 Upgrade

Feature  
EXtractors  
(FEXes)

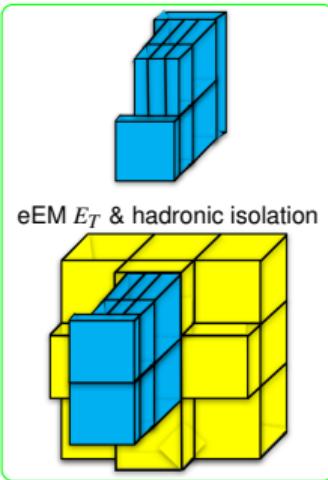
replacing

Jet & Cluster  
Processors





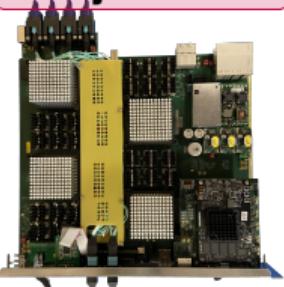
# L1Calo Feature EXtractors



*electromagnetic*  
**eFEX**



*jet*  
**jFEX**



*global*  
**gFEX**



higher granularity

larger coverage &  
algorithmic windows

$\Delta\eta \times \Delta\phi$  (EM cal.)  
max.  $|\eta|$  (core)

$0.025 \times 0.1$   
 $\leq 2.5$

$0.1 \times 0.1$   
 $\leq 4.9$

$0.2 \times 0.2$   
 $\leq 4.9$

trigger objects (TOB)

$e/\gamma, \tau_{\text{had}}$

jets,  $\tau_{\text{had}}, E_T^{\text{miss}}, \sum E_T, e/\gamma_{|\eta|>2.5}$

large jets,  
 $E_T^{\text{miss}}, \sum E_T$

main FPGAs  $\text{XC7VX550T} \times 96$

#LUT/FPGA 346k

input bw/board  $\sim 1.4 \text{ Tbps } (\times 24)$

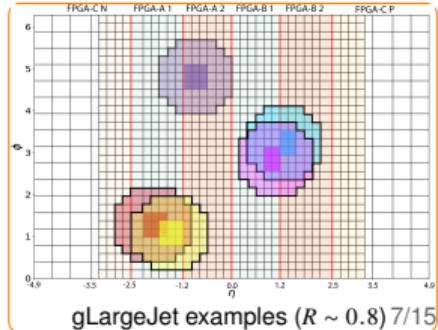
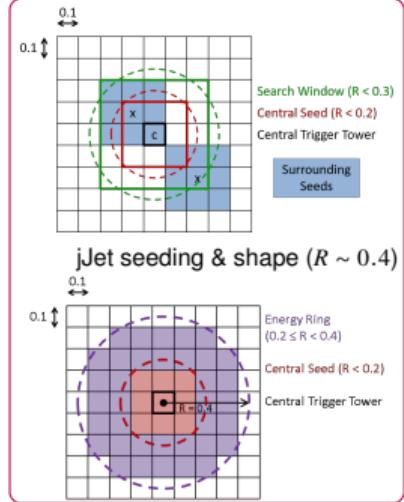
1182k

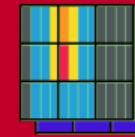
$\sim 2.2 \text{ Tbps } (\times 6)$

$\text{XCVU9P} \times 3$

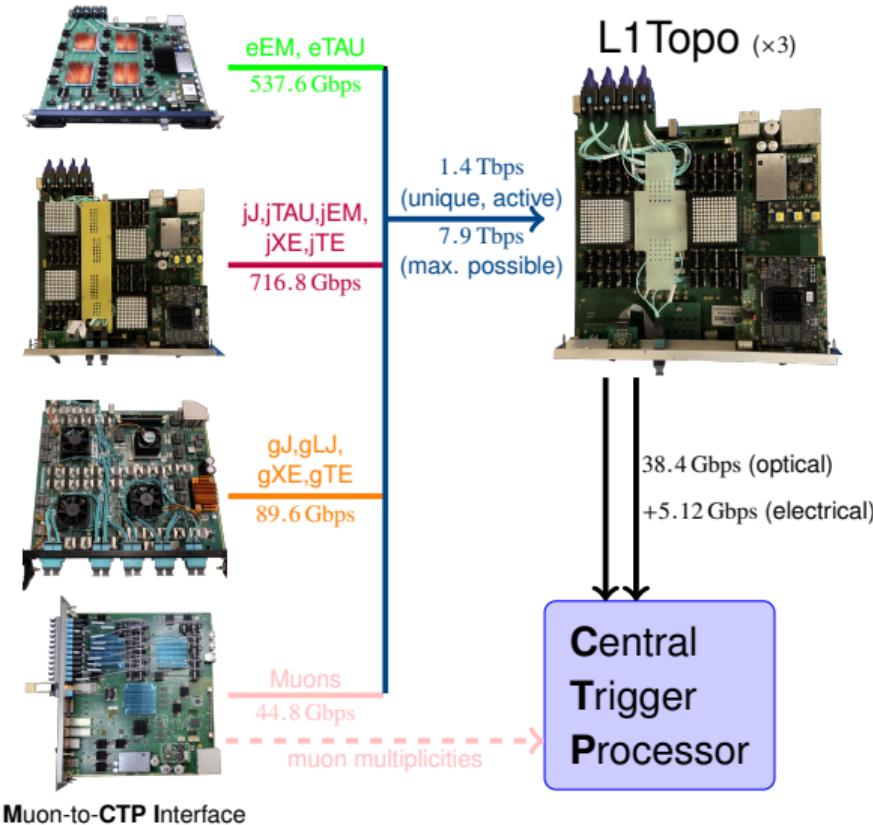
1182k

$\sim 3.4 \text{ Tbps } (\times 1)$

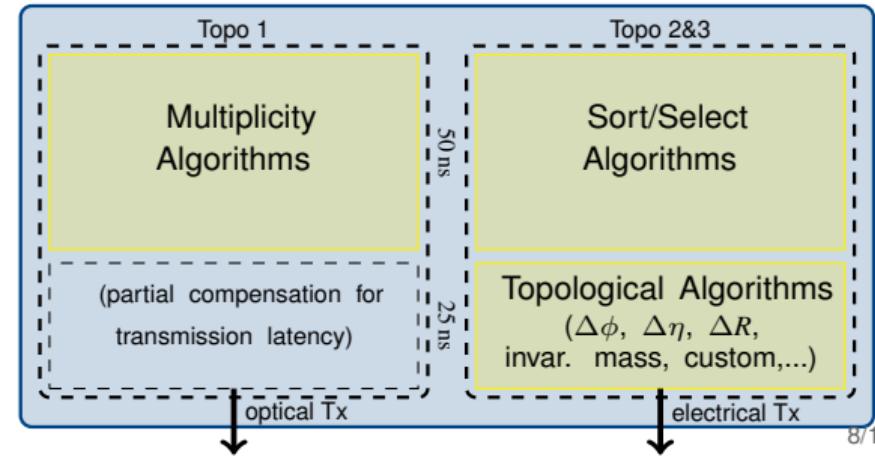


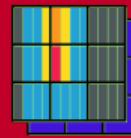


# L1Topo



- ▶ jFEX-derived HW design, 3x2 FPGAs
- ▶ Different input composition per FPGA  
→ maximize possible combinations
- ▶ Algorithmic firmware composition derived from (L1) Trigger Menu  
"Topo Converter": JSON → VHDL
- ▶ ~ 200 results/event

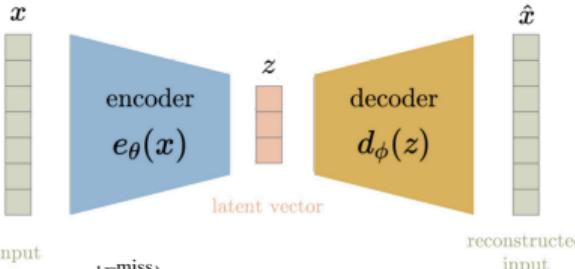




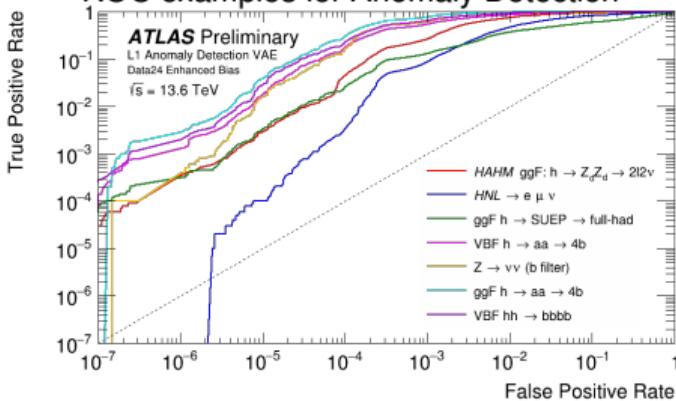
# Algorithmic Examples: ML/AI

## Real-time ML inference in ultra-low latency systems

- ▶ L1Topo: Anomaly detection w/ variational auto encoder
- ▶ [HLS4ML] + Vitis™ HLS
- ▶ Latent space based discriminant → inference w/o decoder part
- ▶ Inputs:  $(E_T, \eta, \phi) \times 15$  TOBs (jJets, eTaus, muons,  $\text{j}_T^{\text{miss}}$ )  
→ 3 layer MLP &  $\sum \mu_{\text{latent}}^2$  in  $\leq 25$  ns

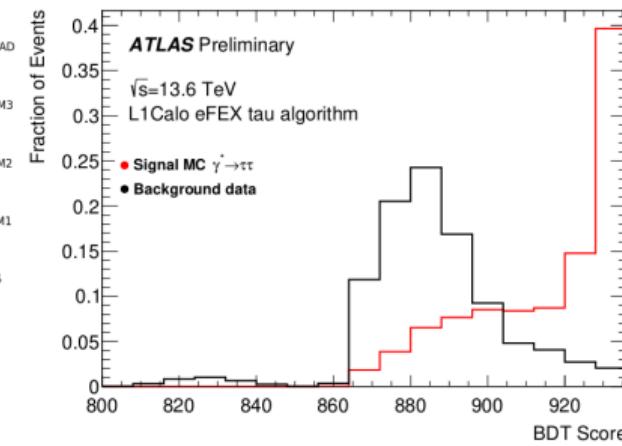
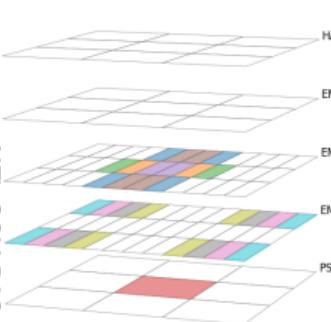


### ROC examples for Anomaly Detection



[ATL-COM-DAQ-2025-039]

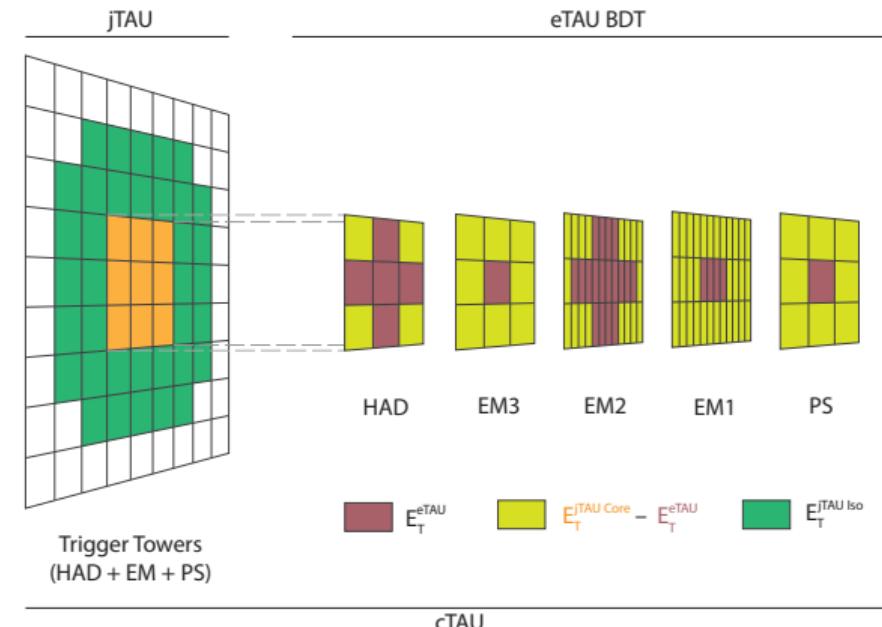
Cells summed for  
eTAU BDT inputs





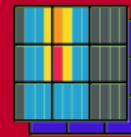
# Algorithmic Examples: "combined" TAU

- ▶ eFEX:  
high granularity, small windows  
 $\rightarrow E_T$  & BDT working point (0/L/M/T)
- ▶ jFEX:  
moderate granularity, large windows  
 $\rightarrow E_T$  &  $E_T^{iso}$
- ▶ L1Topo: combine eTAU + jTAU = cTAU  
 $\rightarrow$  coordinate-based matching
- ▶ wide-area isolation & eTAU BDT  
for cTAU multiplicities



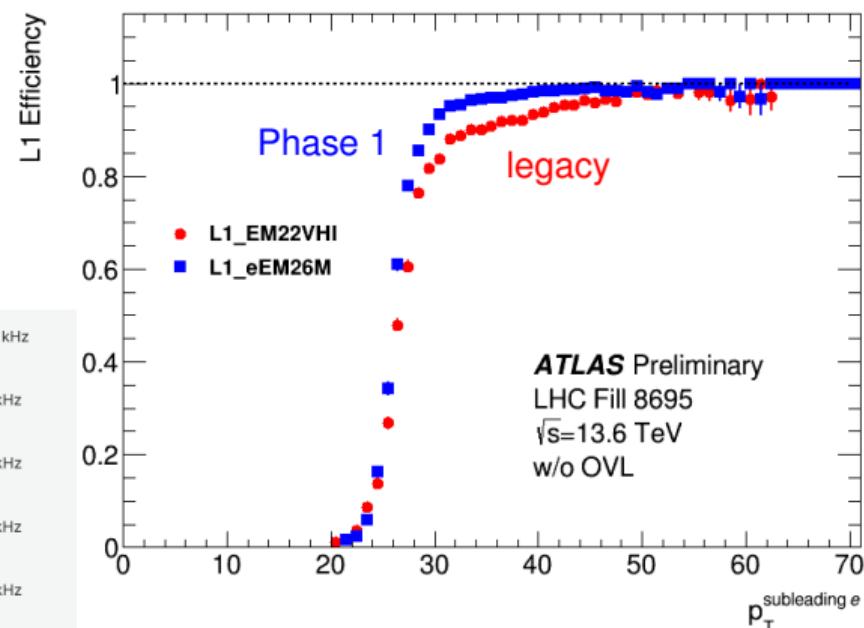
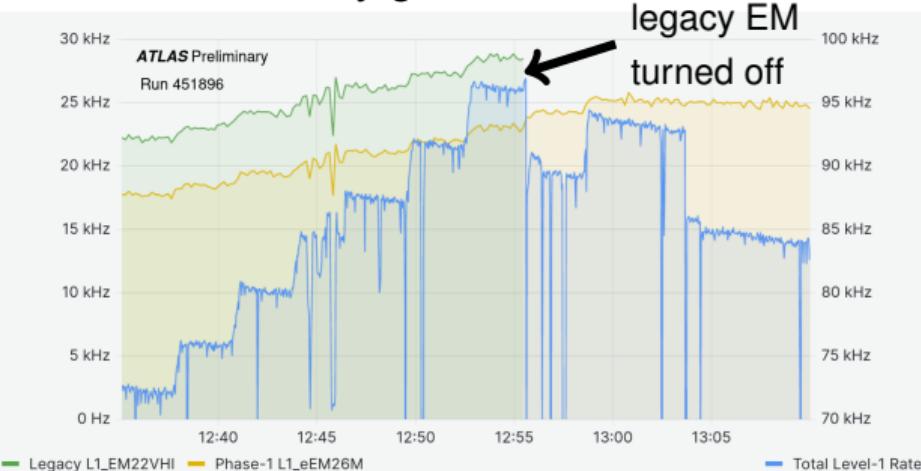
Isolation condition:  
(if matched)

$$\frac{E_T^{jTAU \text{ Iso}} + \alpha (E_T^{jTAU \text{ Core}} - E_T^{eTAU})}{E_T^{eTAU}} < r$$

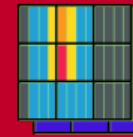


# Performance: eFEX

- ▶ June'23: LHC intensity ramp-up, ATLAS nearing 100 kHz L1 rate
- ▶ eEM (Ph-1) replacing EM (legacy)
- ▶ ~ 5 kHz saving at L1
- ▶ Sizable efficiency gains!

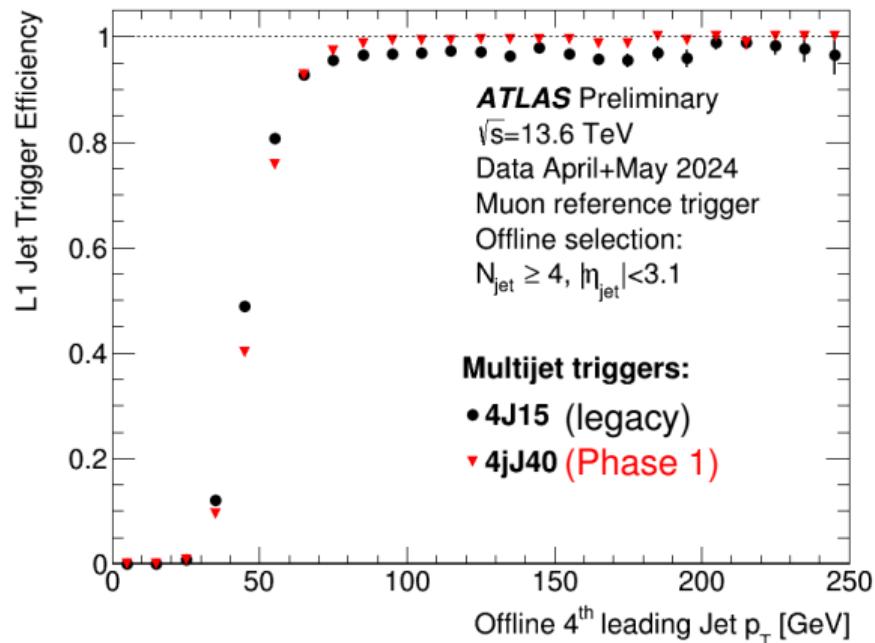


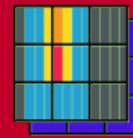
N.B.: Run 3 & further refinements still ongoing,  
results here = snapshots



# Performance: jFEX

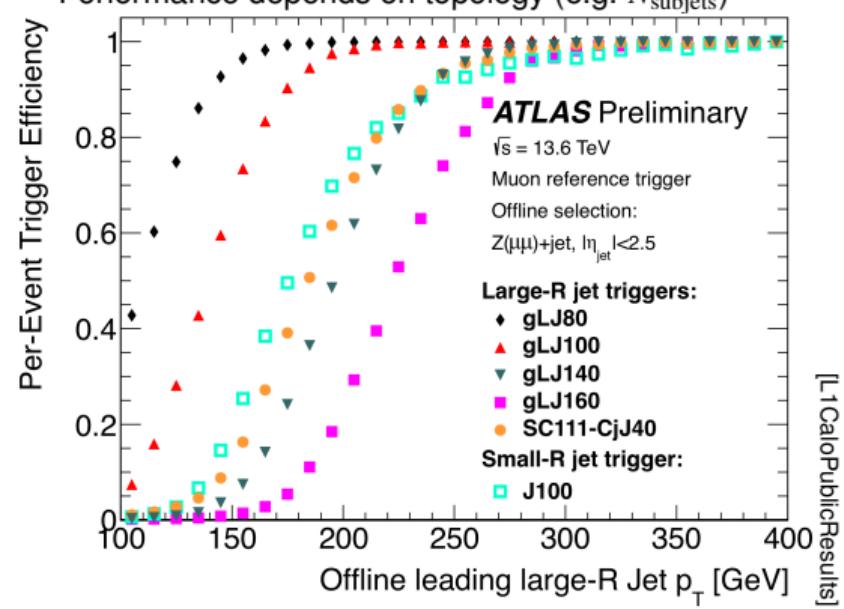
- ▶ jets:  $\eta$ -dependent calibration  
→ sharpened turn-ons
- ▶ Multijet: resolving nearby jets  
→ same rate, better plateau efficiency
- ▶  $E_T^{\text{miss}}$ : per-event pile-up density estimate & correction



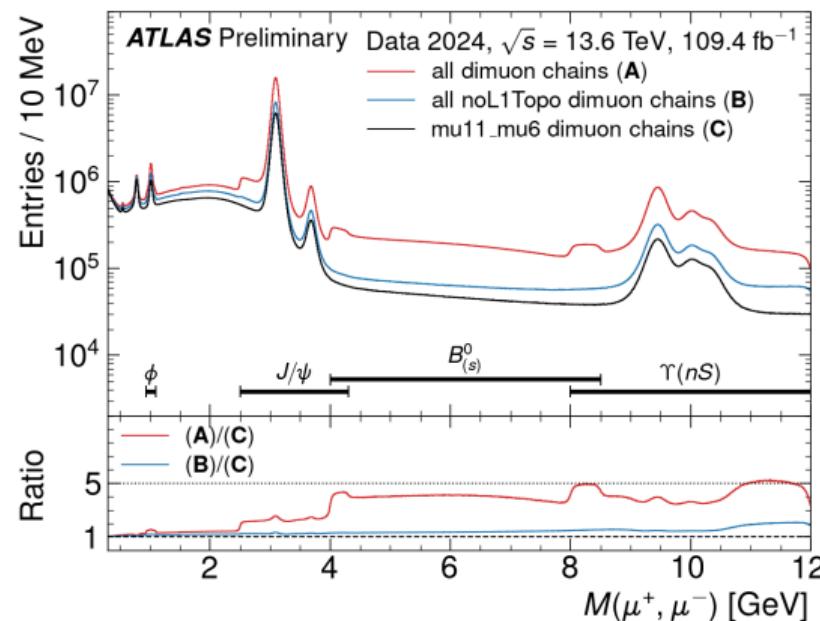


# Performance: gFEX, jFEX, L1Topo

- ▶ HLT large radius jet chains seeded from
  - J100, jJ160 (leg./Ph-1 single jet)
  - gLJ140 (gFEX large-R jets)
  - SC111 (jJets reclustered @ L1Topo)
- ▶ Dedicated large-R triggers  
→ earlier plateau, small additional rate
- ▶ Performance depends on topology (e.g.  $N_{\text{subjets}}$ )



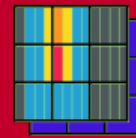
- ▶ Lowering (di)muon thresholds  
→ rapidly increasing trigger rate
- ▶ Mitigation: L1Topo based selections using  $M_{\mu\mu}$ ,  $\Delta R_{\mu\mu}$
- ▶ Greatly enhanced dataset!





# Conclusions

- ▶ Overview of ATLAS Phase-1 L1 Calorimeter trigger system & preliminary performance
- ▶ Crucial improvements to fully exploit LHC Run 3
- ▶ Important milestone towards Phase-2 / HL-LHC



# Thanks for your Attention!

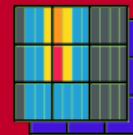
Part of a larger Phase-1 Upgrade family

The image displays a collection of ATLAS detector modules and components, arranged in a grid-like fashion against a blue background. In the center is the ATLAS Experiment logo. The components are labeled as follows:

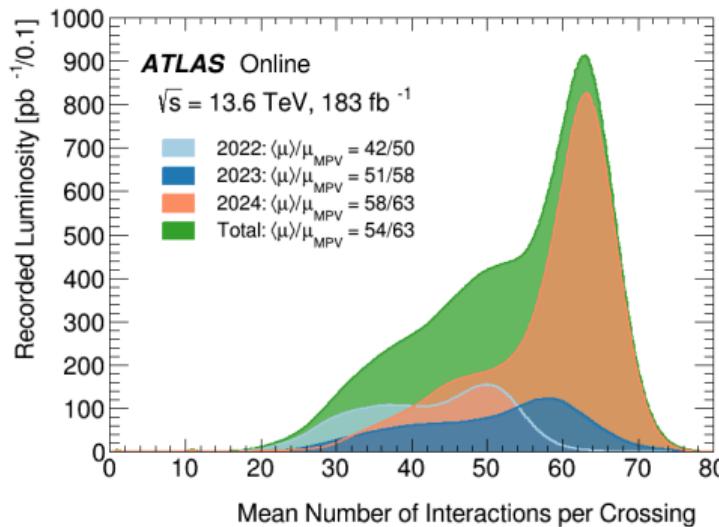
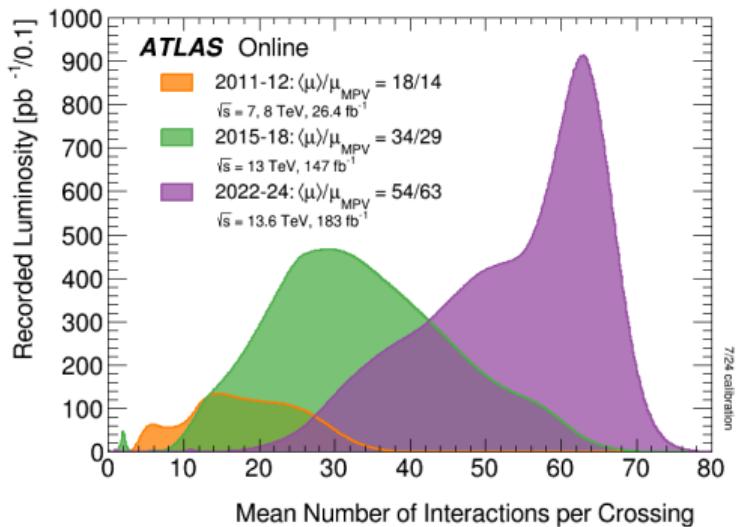
- Latome** (LAr Trigger Output Mezzanine)
- TREX** (Tile Rear Extension)
- LDPB** (LAr Digital Processing Board)
- eFEX** (electromagnetic Feature EXtractor)
- jFEX** (jet Feature EXtractor)
- gFEX** (global Feature EXtractor)
- HUB & ROD** (auxiliary modules for eFEX, jFEX & L1Topo)
- BIS78 Pad** (Barrel Inner Small - trigger & readout board)
- new SL** (Muon Endcap Sector Logic)
- MUCTPI** (MUon-to-CTP Interface)
- L1Topo** (Topological trigger processor)
- FELIX 712** (FrontEnd Link exchange)
- NSW** (New Small Wheel)

**Run 3 - Trigger & DAQ**

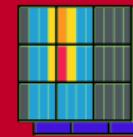




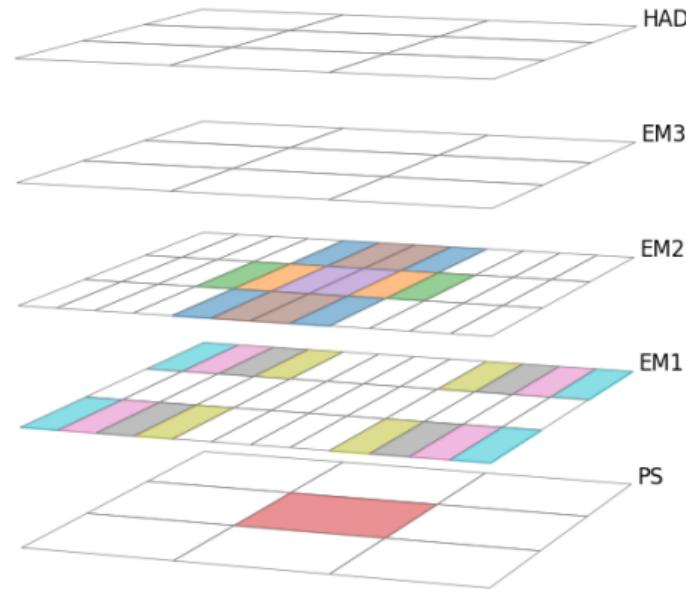
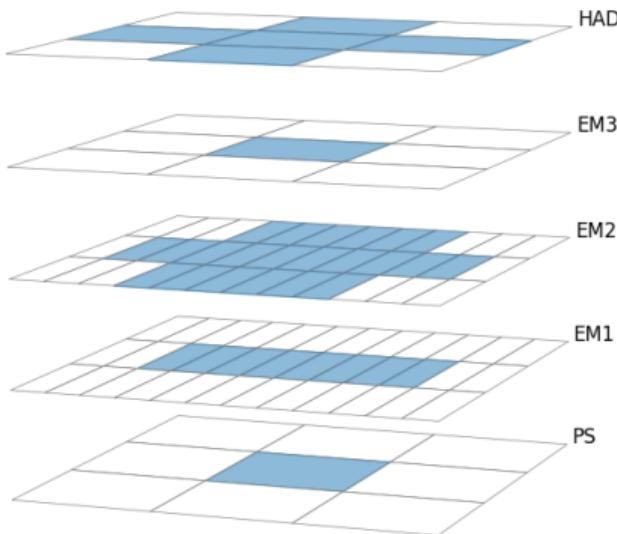
# Pileup Evolution Runs 1-3



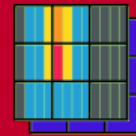
[ATL Lumi Public Results



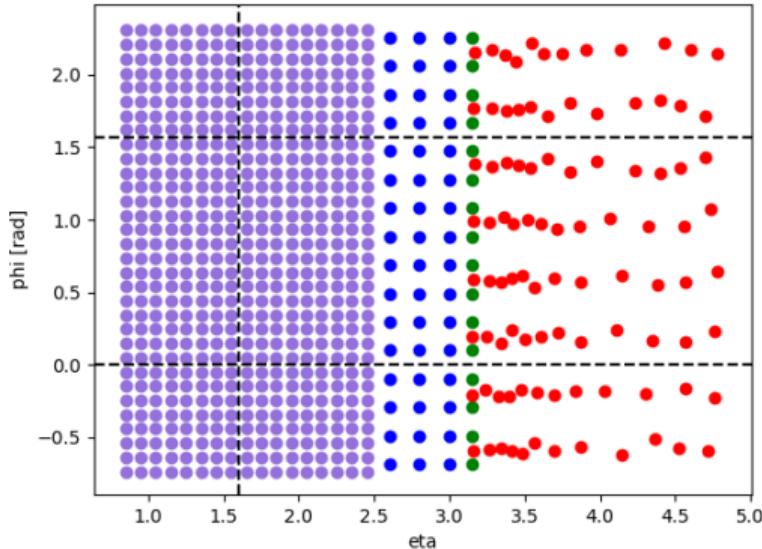
# eFEX eTAU $E_T$ and BDT Input Sums



[L1CaloPublicPlots]



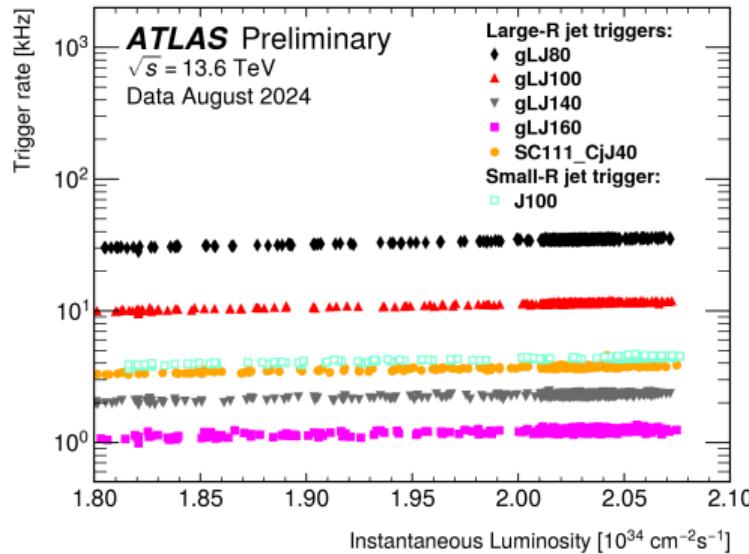
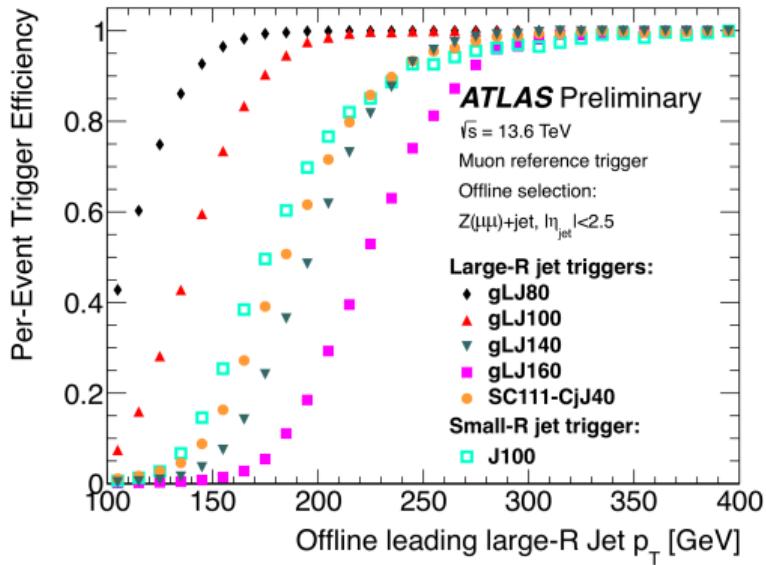
# jFEX Input Granularity



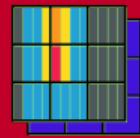
[JINST 19 P05063]



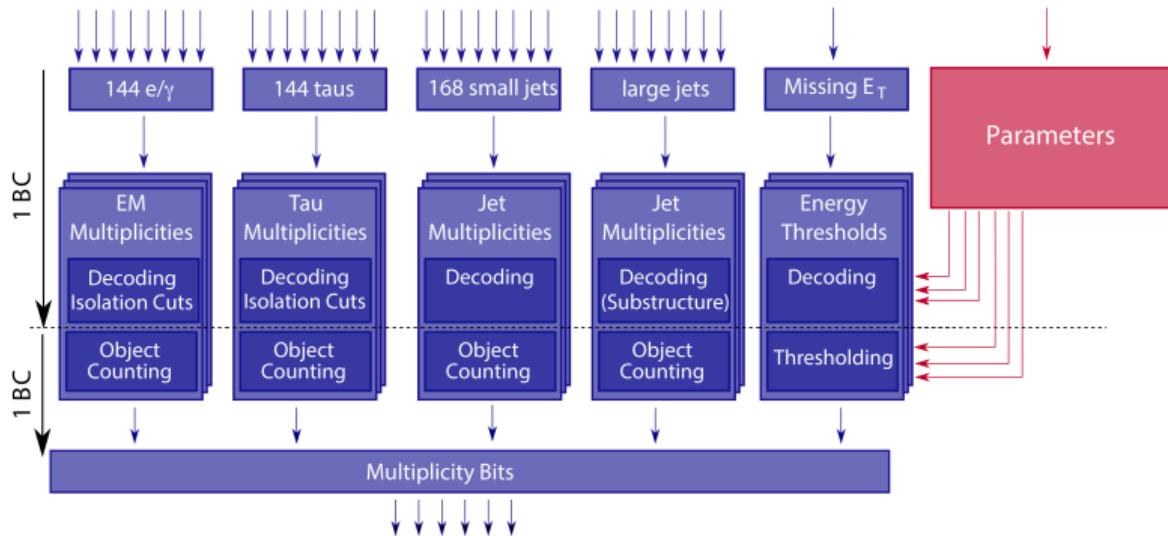
# Large-R Jet Efficiency & Rate vs Luminosity



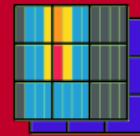
[L1CaloPublicPlots]



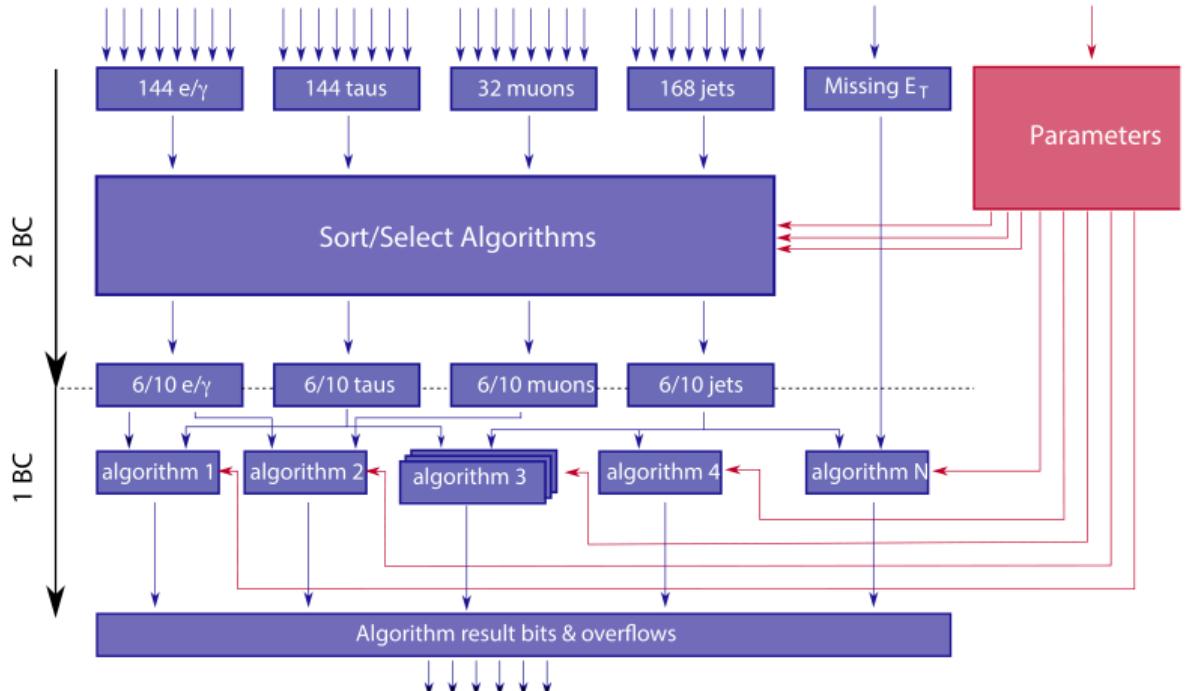
# L1 Topo Multiplicity FW Structure



[JINST 19 P05063]



# L1Topo Topological FW Structure



[INSI19 P05063]