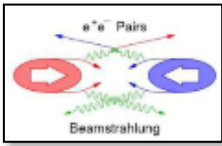


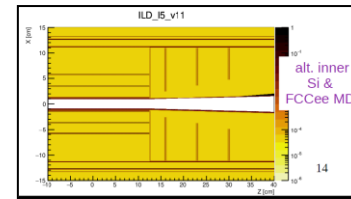
# Projet de détecteur de vertex pour FCCee

## Articulations

R&D TPSCo 65nm - DRD3 – FCCee – Stratégie in2p3/CEA  
Synergies avec les autres projets



# Requirements reminder



Physics

- ⇒ Flavour tagging
- ⇒ Low pT tracks
- ⇒ Vertex/Jet charge determination

Material Budget

- ⇒ ~ 0.1-0.2 %  $X_0$  / layer
- ⇒ < 1%  $X_0$  for the whole VTX
- ~ 900  $\mu\text{m}$  Si
- + ~0.14%  $X_0$  for the beam pipe (ILC)
- + ~0.6 %  $X_0$  for the beam pipe (FCC)

500  $\mu\text{m}$  Be ~ 0.15 %  $X_0$   
 100  $\mu\text{m}$  Si ~ 0.1 %  $X_0$

Radiation hardness  
 $O(100\text{kRad/yr})$  &  $O(10^{11})n_{\text{eq/yr}}$

Cooling  
 Stiffness / Alignment

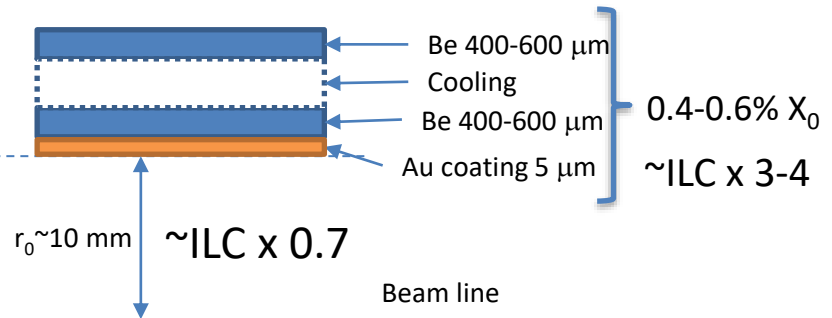
- Geometry
  - ✓ 5-6 layers in the inner radius ( $\sim < 6-10$  cm) + disks
- Spatial resolution
  - ✓ ( $\sigma_{\text{sp}} \sim 3 \mu\text{m}$ ) ⇒ Pitch  $\sim 14-17 \mu\text{m}$  (Binary output)
- Material budget
  - ✓ ~ 0.1-0.2 %  $X_0$  / layer
- Time resolution:  $O(100$  ns)
  - ✓ BX time = 20 ns (Z); 1  $\mu\text{s}$  (ZH); 3  $\mu\text{s}$  (tt)
  - ✓ Integrate over  $O(10)$  BX is probably affordable
- Particle flux
  - ✓ Occupancy mainly coming from beam background
  - ✓ Dead time < per mil
  - ✓ Up to 100-400 MHz/cm<sup>2</sup> (with safety factors)
- Power (without Power pulsing)
  - ✓  $\sim < 50\text{mW/cm}^2$  (assuming air flow cooling)
  - ✓  $\sim < O(100$  W) in total

FCCee

$$\sigma_{d_0} = a \oplus \frac{b}{p \sin^{3/2} \theta}$$

$a \simeq 5 \mu\text{m}; \quad b \simeq 15 \mu\text{m GeV}$

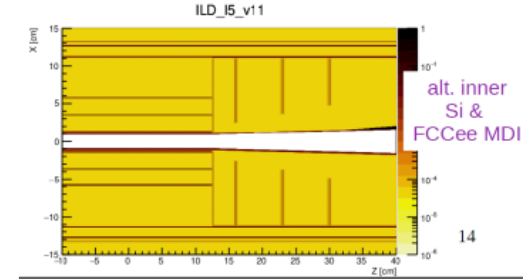
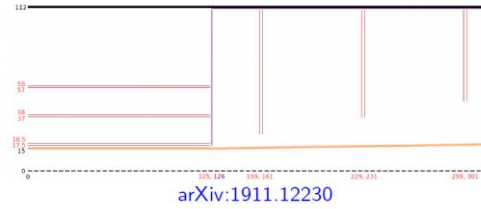
$b \sim r_0 \sqrt{\text{material}}$   
 $a \sim \sqrt{r_0}$



# Design concepts

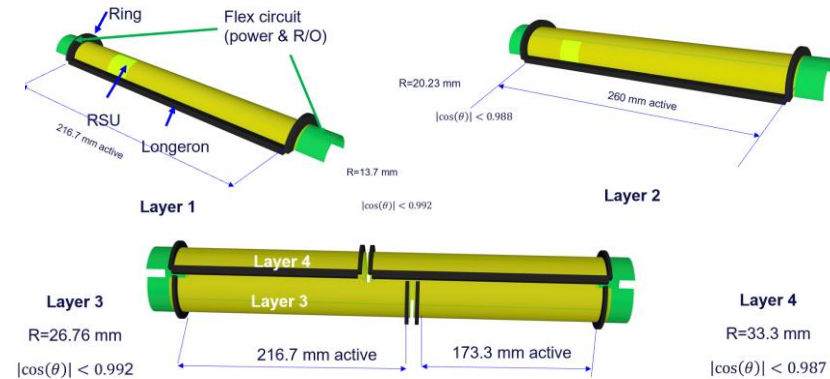
## À la CLD/ILD:

- ✓ 3 double ladders + discs
- ✓ Layer 1:  $|z_{max}| \sim 12\text{cm}$
- ✓  $R_{in} \sim 12\text{ mm}$



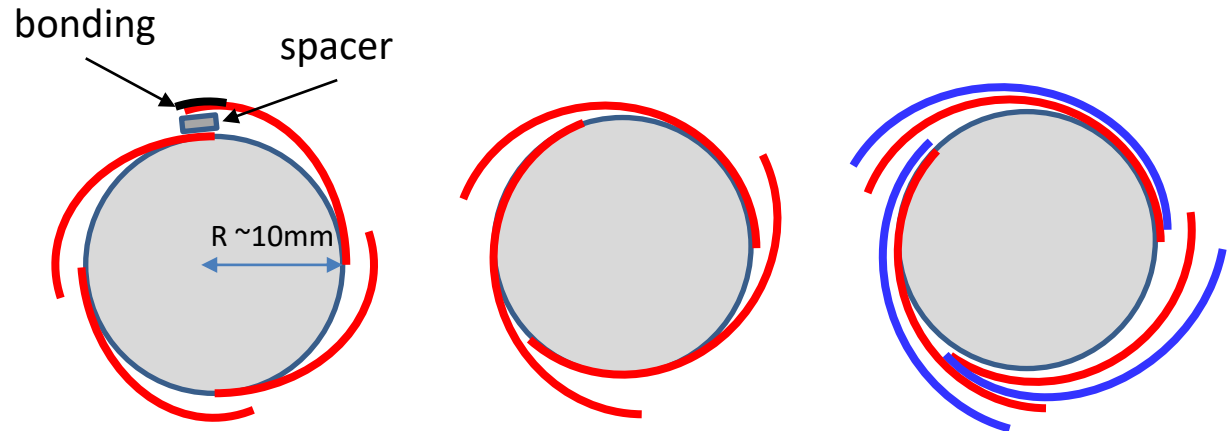
## À la ALICE ITS-3

- ✓ 3/4 layers with stitched half cylinders
- ✓ Fill factor not 100% per layer
- ✓ Stitching mandatory
  - Pitch ? Power ? Yield ? Fill factor ? Bent radius ?



## Concept Schnecke

- ✓ Stitching or not stitching
- ✓ Full acceptance in  $\phi / z$
- ✓ Double sided can considered
- ✓ Competitive for mat. Budget.



# Roadmap (to be discussed)

- Goal: reach the requirements before ~ 2030

- ESPPU: (2025-26)

- ✓ In2p3/CEA: submit VTX contribution to GTS in oct. 2024

- DRD3/7 and submissions

- ✓ “Fine pitch” project

- Determine the most suitable architecture
- Target 3  $\mu\text{m}$  resolution
- Improve time resolution  $O(100\text{ns})$
- Relax Power constraints

- ✓ Step 1: explore architectures & spatial resolution (use previous MLR1/ER1/ER2 results)

- ✓ Step 2: large size demonstrator usable for beam telescopes

- Strategy:

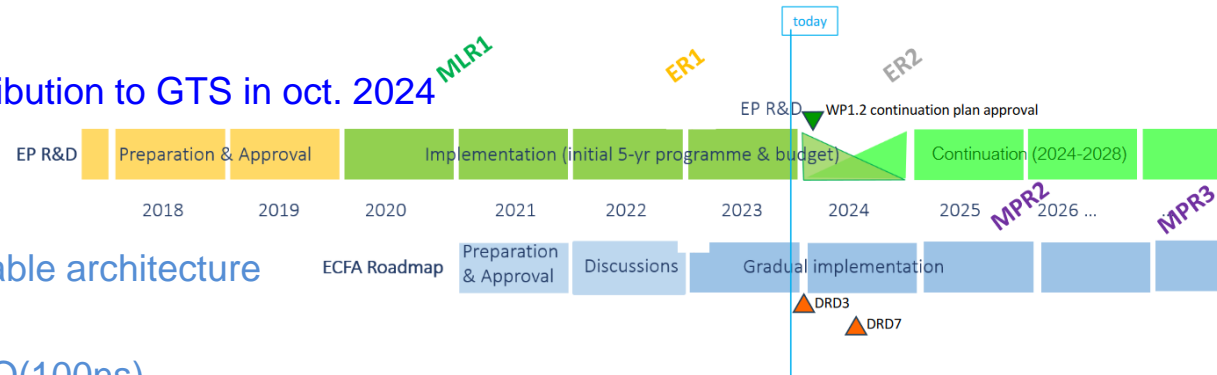
- ✓ Maintain the know with short/mid term projects (MIMOSIS, OBELIX, MOSAIX, etc.)

- ✓ Priority on the chip design (spatial resolution) but integration and simulations can not be neglected

- FCCee: EoI/LOI for detector concepts

- ✓ Submit a realistic/robust vertex detector concept independent from the whole detector concepts

- Backbone: chip design fulfilling the requirements based on an architecture developed @ C4PI.
- Global design: versatile enough to cope with stitched / unstitched approaches.
- Simulation  $\Rightarrow$  quantify precisely figures of merit (resolution down to tagging capabilities)
- Integration  $\Rightarrow$  Bent sensors and ladders to demonstrate the feasibility of concept



# Challenges & discussion

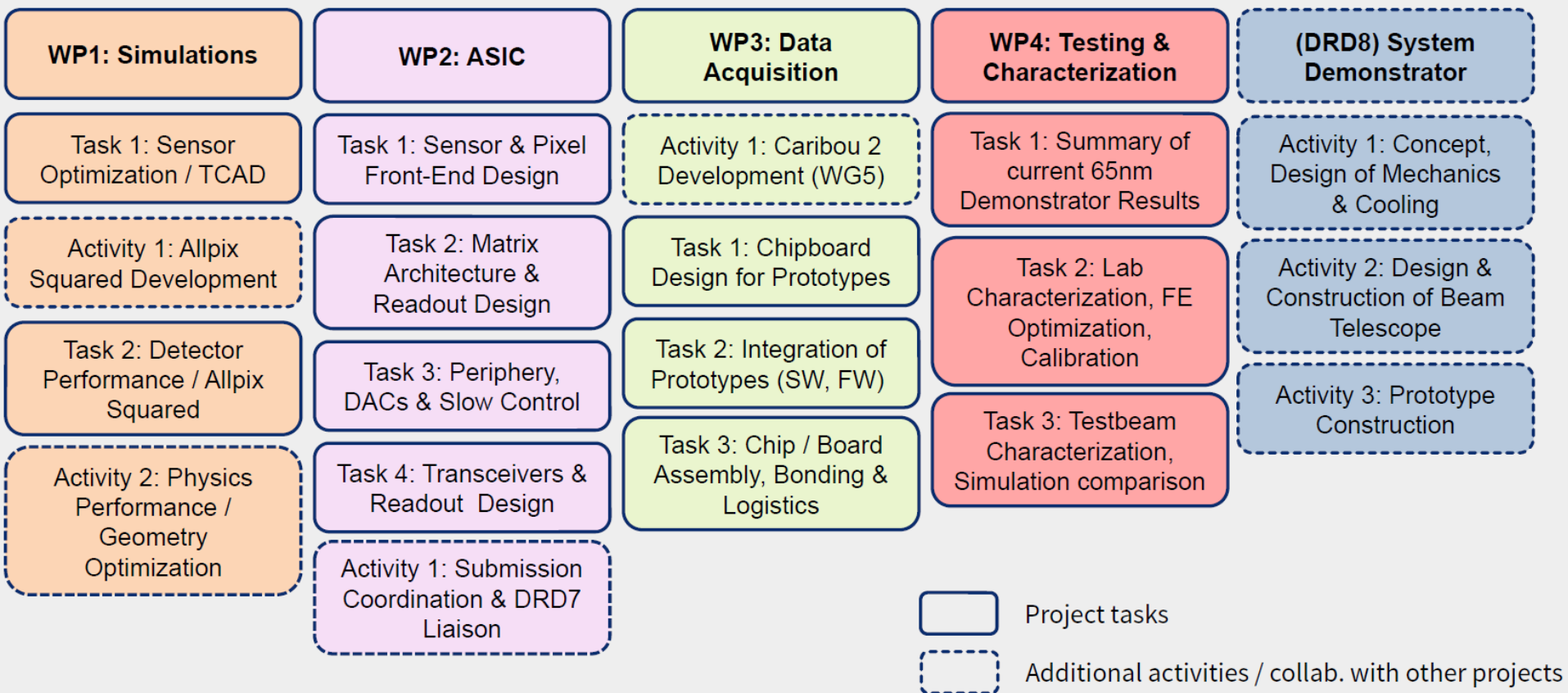
- FTE (in particular designers & integration)
  - ✓ C4PI resources
  - ✓ Tests FTE is demanding but easier to find
- French strategy with respect to other projects ? (Belle-2, ALICE 3, other LHC upgrades, etc.)
- Relationship with ALICE
  - ✓ Strong push from ALICE-3 @ CERN to find synergies with FCCee
  - ✓ How to find a way to not be absorbed by ALICE ?
- Relationship with other countries (Germany, Italy, etc.)
- DRD3
  - ✓ “Fine pitch” project on track and structured
  - ✓ Identified list of partners (IPHC, APC, DESY, CERN, Zurich, Prague, etc.)
- Outer tracker (larger pitch) synergies ?
  - ✓ Common architectures blocks ?
- Submission and funding strategy (MPR2 ~2026 ; MPR3 ~ 2027-2028)
  - ✓ Reticle:  $>6 \text{ cm}^2$  ; 650 000 keuros per Ing. Run; half of it payed by CERN
  - ✓ Master project GRAM / DEPHY ? Others ?
  - ✓ Spread the cost over years: 30-50 keuros / year ? + costs for tests, etc.
- Integration activities
  - ✓ Not independent from the design
  - ✓ Crucial to propose a global concept & for the visibility
  - ✓ Services, mechanics, Power/read-out/driving scheme, Cooling, interconnexion, alignment, acceptance, etc.
- Simulations & performances (Full simulation, tagging performances, charge transport, digitization, etc.)
  - ✓ Crucial for the visibility
  - ✓ Crucial to perform the right choices
  - ✓ Cf. PhD G. Sadowski  $\Rightarrow$  what's next ?

Back up

# DRD3 WP1 project: Fine-pitch CMOS pixel sensors with precision timing for vertex detectors at future Lepton-Collider experiments



## Fine-Pitch CMOS Sensors with Precision Timing for LC Experiments



# Milestones/deliverables

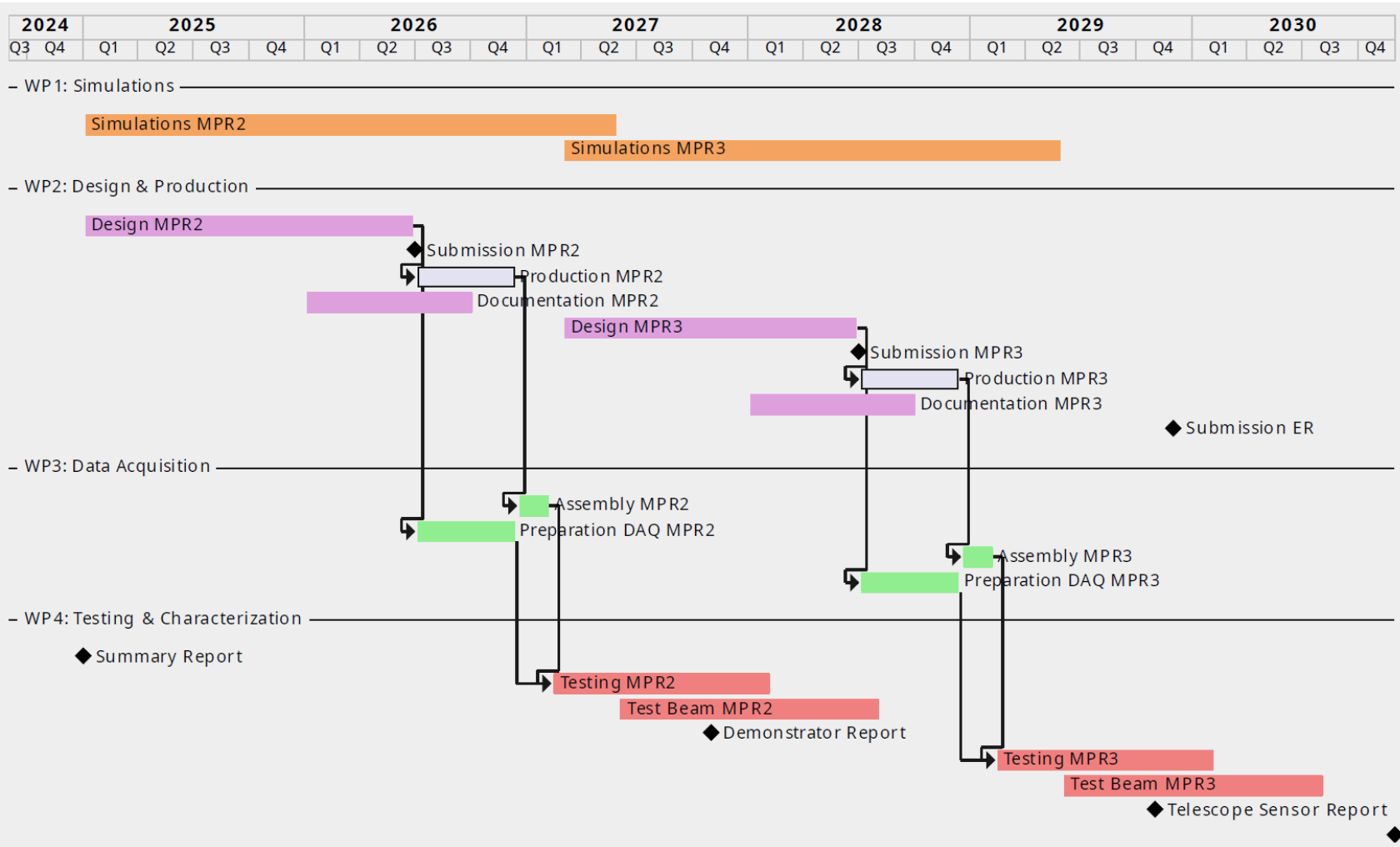
Number	Deliverable/Milestone Title	WP project #	Lead	Type	Dissemination Level	Due Date
M1	Report on Demonstrators	4	DESY	Report	DRD3 report	Month 9 (Q1 2025)
D1 <b>MPR2</b>	Beam Telescope Demonstrator Matrix Submission <b>3 <math>\mu</math>m</b>	1, 2	IPHC	Prototype	Manual / Presentation	Month 24 (Q2 2026)
M2	Report on Demonstrator Matrix Characterization	3, 4	DESY	Report	Publication	Month 36 (Q2 2027)
D2 <b>MPR3</b>	Full Beam Telescope Sensor Submission	2, 3	IPHC	Prototype	Manual / Presentation	Month 48 (Q2 2028)
M3	Report on Beam Telescope Sensor Performance	3, 4	DESY	Report	Publication	Month 60 (Q2 2029)
D3 <b>ER</b>	LC Vertex Sensor Demonstrator Submission	1, 2	IPHC	Prototype	Manual / Presentation	Month 66 (Q4 2029)
M4	Report on LC Vertex Sensor Demonstrator Performance	3, 4	DESY	Report	Publication	Month 78 (Q4 2030)

Full column height

$\geq 2\text{cm}^2$  sensor



# Gantt plan



Institute	Contact	Main areas of contribution
APC Paris	M. Bomben	Simulations, testing
Bonn University	J. Dingfelder	ASIC design, testing
CERN	D. Dannheim	Testing, DAQ, ASIC design support (through DRD7)
DESY	S. Spannagel	ASIC design, testing, DAQ, simulations
ETH Zurich	M. Backhaus	ASIC design, testing
FNSPE Prague	P. Svihra	ASIC design, DAQ, testing
GSI	M. Deveaux	Simulations, testing
HEPHY Vienna	T. Bergauer	DAQ, testing, ASIC design
IPHC Strasbourg	A. Besson	ASIC design, testing
Zurich University	A. Macchiolo	Testing, DAQ, simulations

# DRD3 WP1 project: Fine-pitch CMOS pixel sensors with precision timing for vertex detectors at future Lepton-Collider experiments

## Abstract

This project concerns the simulation, development and evaluation of monolithic fine-pitch pixel sensors implemented in the TPScO65 process, targeting the vertex-detector requirements of future Lepton Colliders as outlined in the ECFA detector roadmap. Key development goals include  $\sim 3 \mu\text{m}$  single-point resolution, down to  $\sim 5 \text{ ns}$  time resolution as required for some of the LC proposals, thinning to  $50 \mu\text{m}$ , an average power consumption below  $50 \text{ mW/cm}^2$ , a minimal inactive periphery area, and a sensor architecture scalable to a large-area detector system. The development of new high-resolution sensors for beam telescopes at DESY and CERN is foreseen as an intermediate target, with relaxed power-consumption and timing requirements. This staged approach allows for a further refinement of the performance targets, following the conclusions of the next update of the European Strategy for Particle Physics.

## DRD technology areas and working groups

The project targets the technology area DRDT 3.1 - *Achieve full integration of sensing and microelectronics in monolithic CMOS pixel sensors*. It will therefore mainly be performed within DRD3 WG 1 - *Monolithic CMOS sensors*. Simulations will be performed within DRD3 WG 4 - *Simulations*. Support for access to the chosen imaging technology and the related design and testing tools, as well as ASIC design, validation and submission support is expected to be provided by DRD7 WG 7.6 (*Complex imaging ASICs and technologies*) and DRD7 WG 7.7 (*Tools and technologies*). The design expertise and IP blocks developed within the project will be made available to the community in the context of DRD7 WG 7.6.

## Development targets and strategy

The physics goals and experimental conditions at high-energy Lepton Colliders (LC) result in stringent requirements for the silicon vertex detectors. High spatial and temporal measurement accuracy needs to be combined with very low mass and power consumption, and the readout scheme needs to be optimized for the expected duty cycle and background particle rates at the different accelerators. This proposal concerns the simulation, development and evaluation of monolithic fine-pitch pixel sensors implemented in advanced CMOS imaging processes, targeting the LC requirements as outlined in the ECFA detector roadmap. Key development targets include  $\sim 3 \mu\text{m}$  single-point resolution, down to  $\sim 5 \text{ ns}$  time resolution as required for some of the LC proposals, thinning to  $50 \mu\text{m}$ , an average power consumption below  $50 \text{ mW/cm}^2$ , a minimal inactive periphery area, and a sensor architecture scalable to a large-area detector system.

A new generation of low-mass high-resolution beam-telescope sensors is needed to support the various ECFA detector-roadmap developments and to provide accurate reference measurements. The precision requirements for these instruments are similar to the ones for lepton-collider vertex detectors, while the constraints on the power budget, timing precision and periphery area can be relaxed. It is therefore foreseen to develop high-resolution beam-telescope sensors as an intermediate target in a first R&D phase. In a second phase, the developed sensor architecture will be adapted and further optimized in terms of power consumption, time-resolution and periphery area towards the LC requirements.

The proposed staged approach allows for a further refinement of the development targets for the later LC-focused stages, following the conclusions of the next update of the European Strategy for Particle Physics (2027/28). In particular, the choice of the accelerator technology for the lepton collider (linear or circular, with a significant difference in duty cycle) will affect the sensor power-reduction strategy and the trade-off between low power consumption and high timing precision for the rejection of beam-induced background particles.

## Foreseen activities, milestones and deliverables

The following activities, milestones and deliverables with indicative completion dates are foreseen towards the final project goals:

- Characterisation and TCAD + Monte-Carlo simulations of [recently produced](#) monolithic pixel sensor demonstrators with small collection electrodes implemented in the modified TPScO65 CMOS imaging process with  $65 \text{ nm}$  feature size (APTS, DPTS, H2M, CE65v2, DFE) in laboratory and test-beam measurements. Comparison of the observed performance to simulations as well as the requirements of beam-telescope sensors and lepton collider vertex detectors.  
*Milestone 1: report on characterisation and simulation results for each of the demonstrators. [end 2024]. Responsible lead institute: DESY?*
- Design of a full-column demonstrator pixel matrix ( $\sim 1\text{-}2 \text{ mm}$  width,  $10 \text{ mm}$  height) in TPScO65, targeting the requirements of beam-telescope sensors. Main features: pixel dimensions and sensor process variant compatible with position resolution of  $3 \mu\text{m}$  in both directions; per-pixel arrival-time measurement at the  $100 \text{ ns}$  level; readout of digitized hits with an architecture that is scalable towards instantaneous particle-hit rates of up to approximately  $50 \text{ MHz/cm}^2$ . Alternative pixel geometries, such as hexagons, will be investigated in simulation studies in terms of time and position-resolution benefits. Sensor process and design variants to be based on the results obtained with the previously characterized demonstrators (Milestone 1) and optimized mainly for position resolution (charge sharing), using TCAD and Allpix-Squared Monte Carlo simulations. More advanced technologies under study within DRDT 3.4 (Develop full 3D-interconnection technologies for solid state devices in particle physics) will also be considered, in case they become available as cost-effective Multi-Project-Wafer (MPW) submissions. Submission for production in a shared run, targeting the EP R&D WP 1.2 MPR2 submission scheduled for mid 2026. Design and production of readout printed-circuit boards compatible with the Caribou modular DAQ system.  
*Deliverable 1: beam-telescope demonstrator matrix submitted for production. [mid 2026]. Responsible lead institute: IPHC?*
- Integration of the beam-telescope demonstrator matrix in the Caribou DAQ system. Characterization in laboratory and test-beam measurements. Comparison of the observed performance to simulations and to the

requirements of beam-telescope sensors and LC vertex detectors.  
*Milestone 2: report on characterisation and simulation results for the beam-telescope demonstrator matrix [mid 2027]. Responsible lead institute: DESY?*

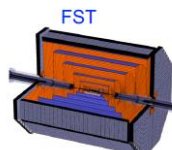
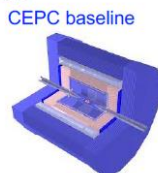
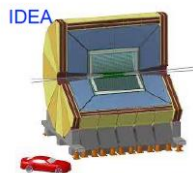
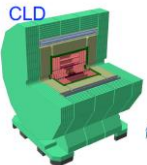
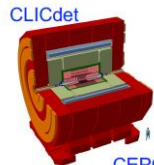
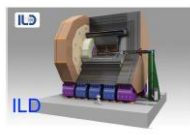
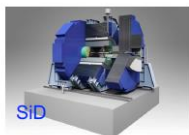
- Evolution of the beam-telescope demonstrator design to a full-size telescope sensor matrix ( $\geq \sim 2 \text{ cm}^2$ ) with all features required for beam telescope sensors. Target technology: same as demonstrator matrix. Submission in shared EP R&D WP 1.2 MPR3 run. Design and production of readout printed-circuit boards compatible with the Caribou modular DAQ system and with existing beam-telescope infrastructure. *Deliverable 2: beam-telescope sensors submitted for production in MPR3 [mid 2028]. Responsible lead institute: IPHC?*

- Further development of the beam-telescope sensor architecture towards low power consumption, minimal periphery area and higher timing precision, compatible with the requirements for LCs. Design of a full-size LC sensor demonstrator matrix ( $\geq 1 \text{ cm}^2$ ) to be based on the results obtained with the previously characterized demonstrators (Milestones 1 and 2) and optimized using TCAD and Allpix-Squared Monte Carlo simulations. Submission in a shared engineering production run.  
*Milestone 3: LC sensor demonstrator matrix submitted for production [end 2029]. Responsible lead institute: IPHC?*

- Integration of the LC sensor demonstrator matrix in the Caribou DAQ system. Characterization in laboratory and test-beam measurements. Comparison of the observed performance to simulations and to the requirements of LC vertex detectors.  
*Deliverable 3: report on characterisation results for the LC sensor demonstrator matrix [end 2030]. Responsible lead institute: DESY?*

# Tracking/vertexing detectors in future e<sup>+</sup>e<sup>-</sup> colliders

Collider	ILC		CLIC	FCCee			CEPC	
Bunch separation (ns)	330/550		0.5	20/990/3000			25/680	
Power Pulsing	yes		yes	no			no	
beamstrahlung	high		high	low			low	
Detector concept	SiD	ILD	CLICdet	CLD	IDEA	Lar	Baseline	IDEA
B Field (T)	5	3.5	4	2	2	2	3	2
Vertex	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel
Vertex Rmin (mm)	16	16	31	~12	~12	~12	16	16
Tracker	Si-strips	TPC	Si-Pixel	Si-Pixel (+RICH ?)	DC/Si-strips	DC/Si-strips or Si-Pixels	TPC or Strips	DC/Si-strips
Tracker Rmax (m)	1.25	1.8	1.5	2.2	2.0	2.0	1.8	2.1
Disks layers	4 + 4	2 + 5	6 + 7	3 + 7	3 (150 mrad)		2+6	



(From D. Dannheim)

Large similarities between the concepts but also significant differences

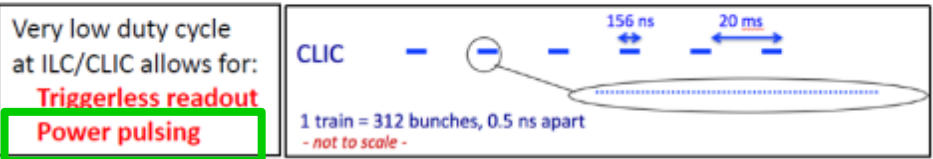
# General considerations on future e+e- colliders

Parameter	ILC		CLIC		
	250 GeV	500 GeV	380 GeV	1.5 TeV	3 TeV
Luminosity L ( $10^{34} \text{cm}^{-2} \text{sec}^{-1}$ )	1.35	1.8	1.5	3.7	5.9
$L > 99\%$ of $\sqrt{s}$ ( $10^{34} \text{cm}^{-2} \text{sec}^{-1}$ )	1.0	1.0	0.9	1.4	2.0
Repetition frequency (Hz)	5	5	50	50	50
Bunch separation (ns)	554	554	0.5	0.5	0.5
Number of bunches per train	1312	1312	352	312	312
Beam size at IP $\sigma_x/\sigma_y$ (nm)	515/7.7	474/5.9	150/2.9	~60/1.5	~40/1
Beam size at IP $\sigma_z$ ( $\mu\text{m}$ )	300	300	70	44	44

ILC: Crossing angle 14 mrad,  $e^-$  polarization  $\pm 80\%$ ,  $e^+$  polarization  $\pm 30\%$   
 CLIC: Crossing angle 20 mrad,  $e^-$  polarization  $\pm 80\%$

Very small beams + high energy  
 => beamstrahlung

Very small bunch separation at CLIC drives timing requirements for detector



Parameter	FCC-ee			CEPC	
	Z	Higgs	ttbar	Z (2T)	Higgs
$\sqrt{s}$ [GeV]	91.2	240	365	91.2	240
Luminosity / IP ( $10^{34} \text{cm}^{-2} \text{s}^{-1}$ )	230	8.5	1.7	32	1.5
no. of bunches / beam	16640	393	48	12000	242
Bunch separation (ns)	20	994	3000	25	680
Beam size at IP $\sigma_x/\sigma_y$ ( $\mu\text{m}/\text{nm}$ )	6.4/28	14/36	38/68	6.0/40	20.9/60
Bunch length (SR/BS) (mm)	3.5/12.1	3.3/5.3	2.0/2.5	8.5	4.4
Beam size at IP $\sigma_z$ (mm)					

Beam transverse polarisation  
 => beam energy can be measured to very high accuracy ( $\sim 50$  keV)

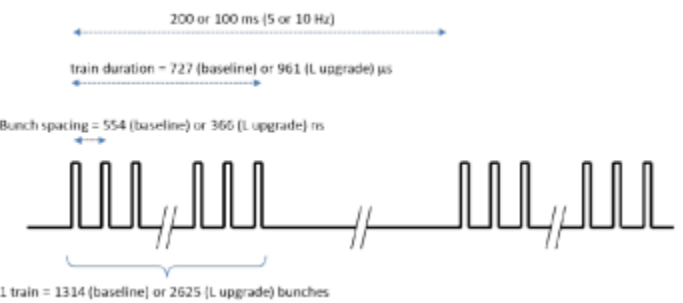
At Z-peak, very high luminosities and very high  $e^+e^-$  cross section (40 nb)

- => Statistical accuracies at  $10^{-4}$ - $10^{-5}$  level => drives detector performance requirements
- => Small systematic errors required to match
- => This also drives requirement on data rates (physics rates 100 kHz)
- => Triggerless readout likely still possible

Beam-induced background, from beamstrahlung + synchrotron radiation

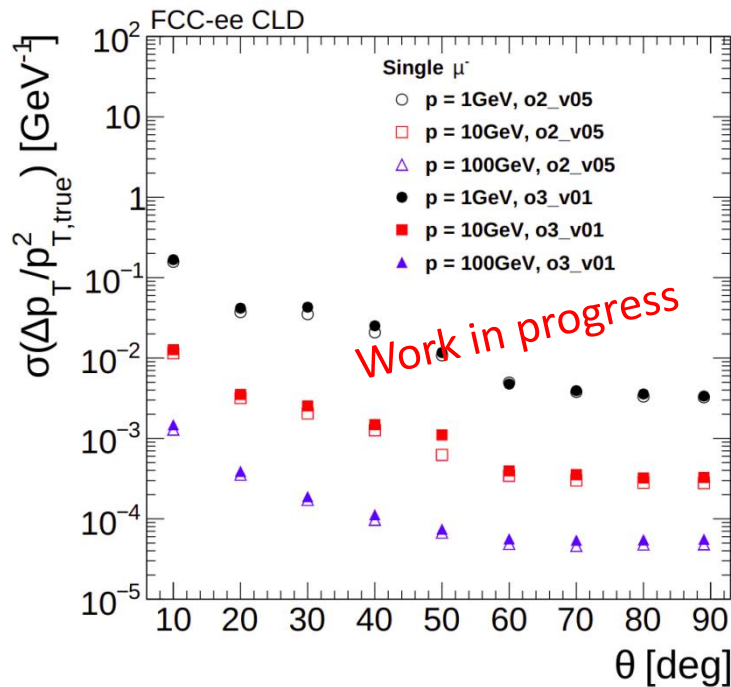
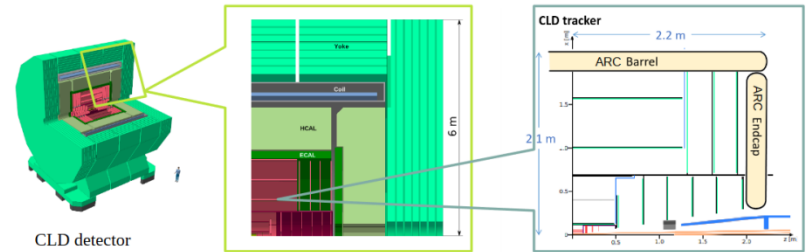
- Most significant at 365 GeV
- Mitigated through MDI design and detector design

(slide from Mogens Dam/Lucie Linssen)



# An example of Full sim performances in CLD

- CLD\_01\_v04 = former geometry
- CLD\_02\_v05 = new beam pipe radius & material budget
  - ✓ 5  $\mu\text{m}$  Au + 2 x 350  $\mu\text{m}$  layers of BeAl + liquid parafin ~ 0.6 %  $X_0 \Rightarrow$  mat. Budget +33%
  - ✓ Inner radius: 15 mm  $\Rightarrow$  10mm
- CLD\_03\_v01 = Adding a RICH
  - ✓ + Array of RICH Cells (ARC)



D0 resolution – single  $\mu^-$  – CLD\_o1\_v04

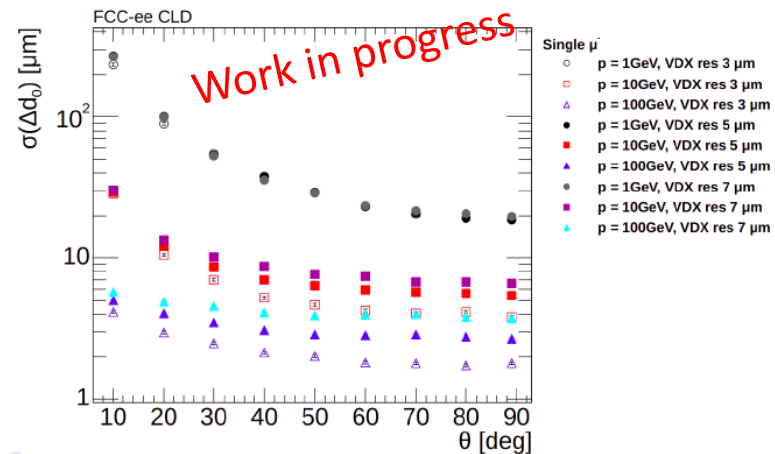


Figure: D0 resolution (10k events)

- Need to reassess the performances plots optimization for FCCee with respect to ILC context.
- Comparing resolutions between detector concepts has to be taken with caution (Different level of realism and conservatism on the technologie future performances)

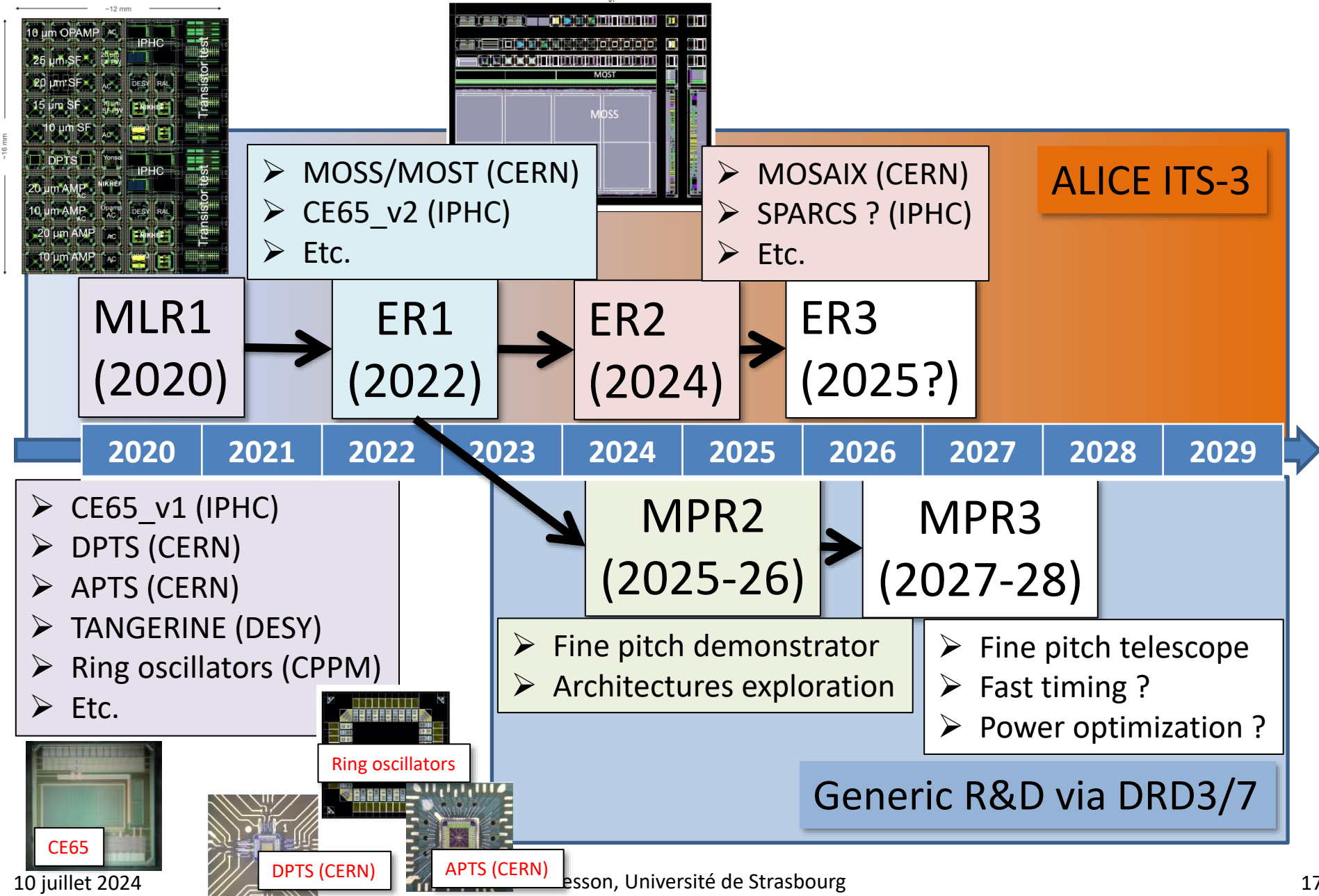
# CMOS technology: moving from 180 nm to 65 nm

Technology	TowerJazz 180 nm	TPSCo 65 nm
Available since	2013 ( mature technology)	2020 (access through CERN)
Large surface projects	<ul style="list-style-type: none"> <li>• ALPIDE for ALICE ITS-2</li> <li>• MIMOSIS for CBM-MVD</li> <li>• OBELIX for Belle-II upgrade</li> </ul>	<ul style="list-style-type: none"> <li>• MOSAIX for ALICE ITS-3</li> <li>• DRD3/7 R&amp;D ?</li> </ul>
Price	affordable	More expensive
Wafer	<ul style="list-style-type: none"> <li>• 8 inches (20 cm)</li> </ul>	<ul style="list-style-type: none"> <li>• Larger: 12 inches (30cm)</li> </ul> ⇒ stitching + bent sensors
Epitaxial layer thickness	<ul style="list-style-type: none"> <li>• 18/25/30/40/50 <math>\mu\text{m}</math></li> </ul>	<ul style="list-style-type: none"> <li>• 10</li> </ul>
Process options	<ul style="list-style-type: none"> <li>• « standard »</li> <li>• « modified », « gap »</li> </ul>	<ul style="list-style-type: none"> <li>• « standard »</li> <li>• « modified », « gap »</li> </ul>
Technology	<ul style="list-style-type: none"> <li>• Feature size (180 nm)</li> <li>• V (1.8V)</li> <li>• 6 Metal Layers</li> </ul>	<ul style="list-style-type: none"> <li>• Feature size (65nm)</li> <li>• Lower V (1.2 V)</li> <li>• 7 Metal layers</li> </ul> ⇒ Pitch reduction, power saving, more fonctionnalites, etc.

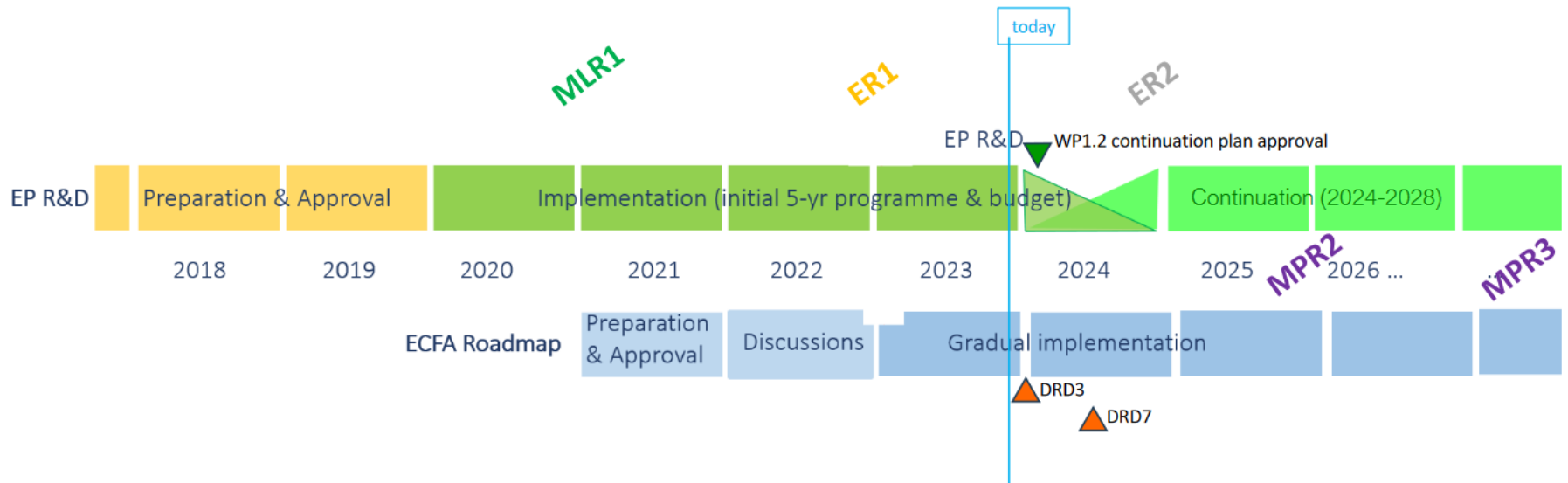
⇒ Strong motivations to switch to a smaller feature size to increase the performances space



# TPSCo 65nm Submissions



# CERN WP 1.2



<https://indico.cern.ch/event/1339888/>

# How to adapt ITS-3 approach to FCCee ?

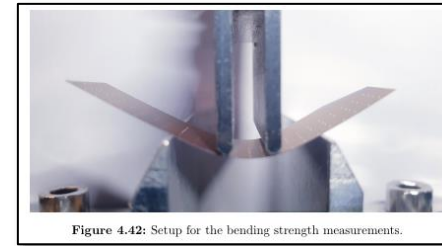
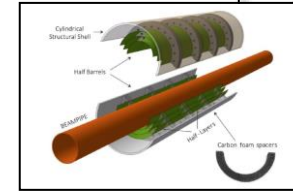
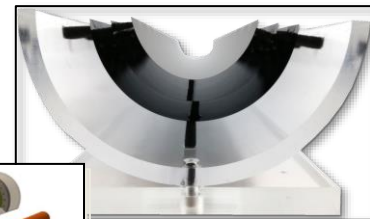
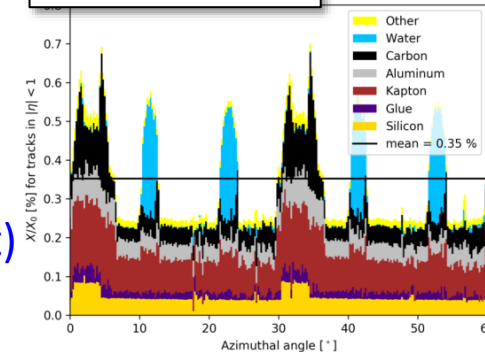


Figure 4.42: Setup for the bending strength measurements.

ALICE ITS2  
Material budget



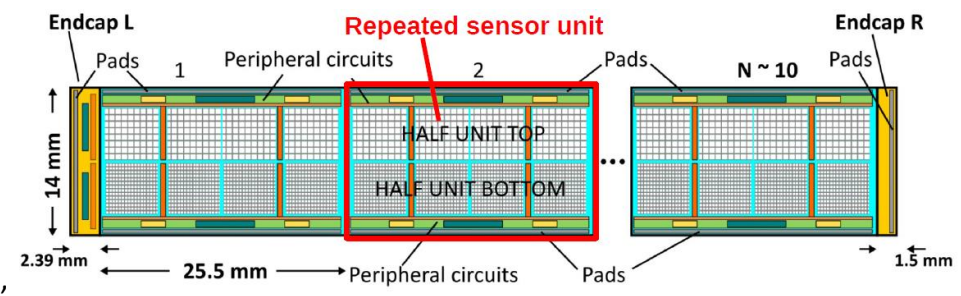
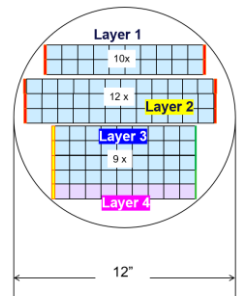
• ALICE-ITS3/CERN drives the R&D on stitching + bent sensors:

- ✓ Sensor part ~15% of total material budget
- ✓ Sensors thinned down to 50  $\mu\text{m}$  or less ?
  - Tests performed by ALICE (cf. ITS3-TDR)
- ✓ Minimizing overlapping regions,
- ✓ minimizing minimal radius around the beam pipe

• Challenges and caveats (for  $e^+e^-$  colliders)

- ✓ Mechanics ? Bonding ? Air cooling only ?
- ✓ Power dissipation map could be a challenge
- ✓ Design: Minimizing peripheral circuits (Fill factor ~90%)
- ✓ Bent sensor performances ? Yield ? Radiation hardness ?
- ⇒ design rules constraints the minimal pitch (~22  $\mu\text{m}$ ) ?
- ✓ ITS-3 do not have disk (chip periphery adds Z position constraint)
- ✓ Approach validated in a limited radius range ( $R > 18\text{mm}$ ) ?

- Trials performed by ALICE down to  $R = 10\text{mm}$  (thickness 30-50  $\mu\text{m}$ )



A.Besson,

# Bent sensor tests for different thicknesses/radii

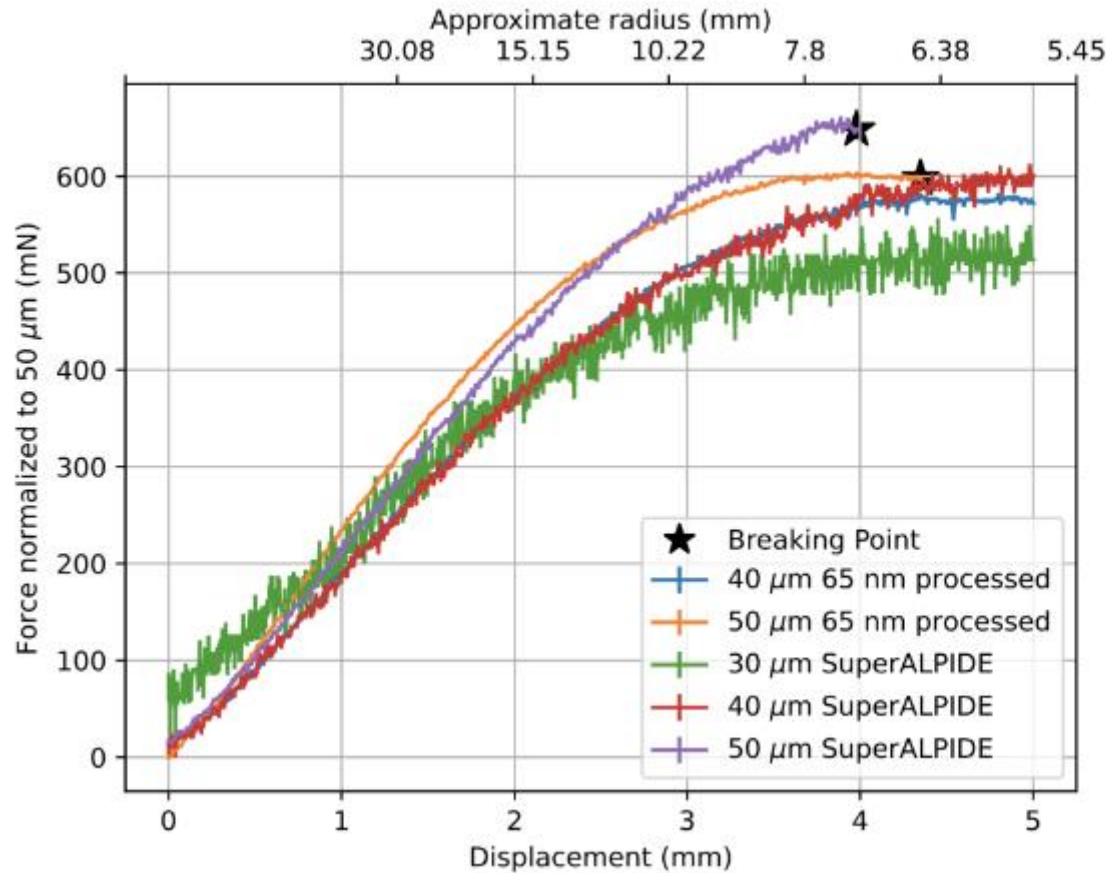
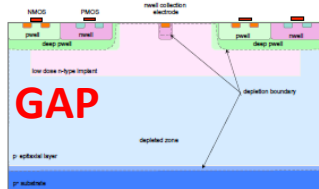
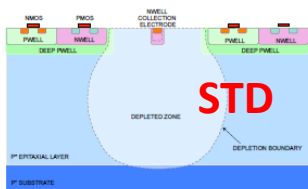


Figure 4.41: Setup for the bending strength measurements.

ALICE ITS-3 TDR: Bent layers at  $R \sim 12$  mm seems doable

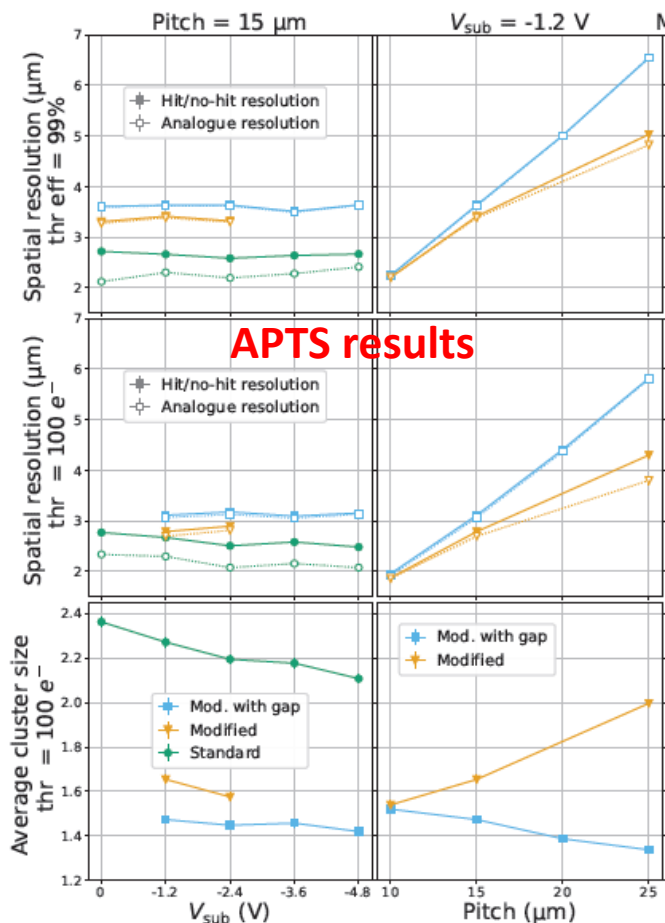
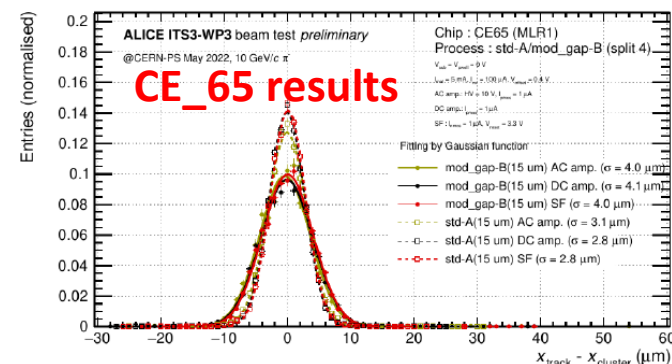
# TPSCo 65 nm & spatial resolution



PCB	Geo	Process	Pitch(um)	HV(V)	Sp. Res.(um) (telescope resolution subtracted)
	<b>CE_65 results</b>				
10	SQ	GAP	22.5	10	~5.1
02	SQ	GAP	18	10	~4.1
19	SQ	GAP	15	10	~3.2
18	SQ	STD	22.5	10	~2.4
23	SQ	STD	18	10	~1.8
06	SQ	STD	15	10	~1.3

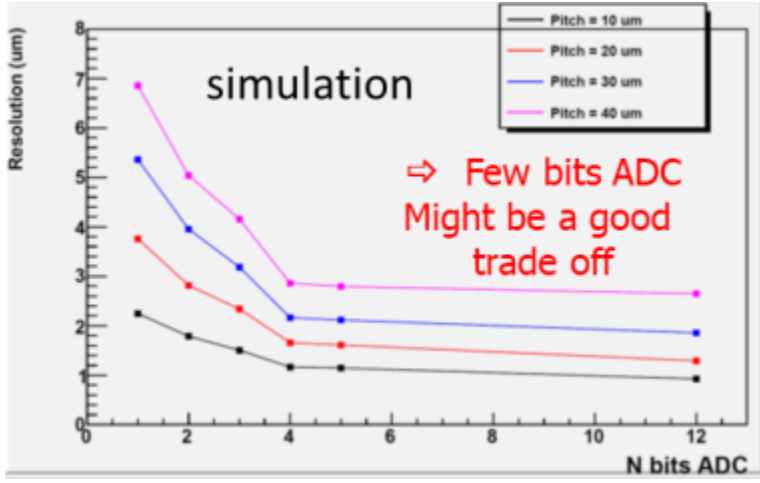
Telescope resolution: ~2.1um (Calculated from <https://mmager.web.cern.ch/telescope/tracking.html>)

- 3 um resolution with Analog output
  - ✓ STD ⇒ pitch ~ 25 um
  - ✓ GAP ⇒ pitch ~ 14 um
- 3 um resolution with Binary output
  - ✓ STD ⇒ pitch ~ 17 um
  - ✓ GAP ⇒ pitch ~ 14 um
- Few bits ADC valuable with presence of charge sharing
  - ✓ On going studies

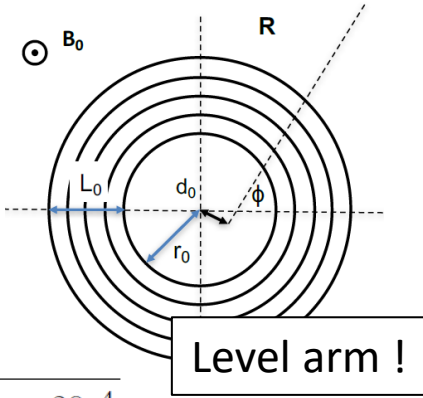


# Spatial resolution in Higgs factories

- Typical targets:
  - ✓  $\sigma_{sp} \sim 3 \mu\text{m}$  for the vertex layers
  - ✓  $\sigma_{sp} \sim 5\text{-}10 \mu\text{m}$  for the outer tracker layers
- Resolution in each layer depends on
  - ✓ Pitch
    - In conflict with the functionalities inside the pixel
    - Favored by small feature size technology
  - ✓ Charge deposition
  - ✓ Charge sharing (SNR vs resolution)
    - Depletion:
    - Staggered pixels
  - ✓ Charge encoding
    - Binary output / ADC / Tot / etc.



$$\sigma_{d0}^2 = a^2 + \left( \frac{b}{p \cdot \sin^{3/2}\theta} \right)^2$$

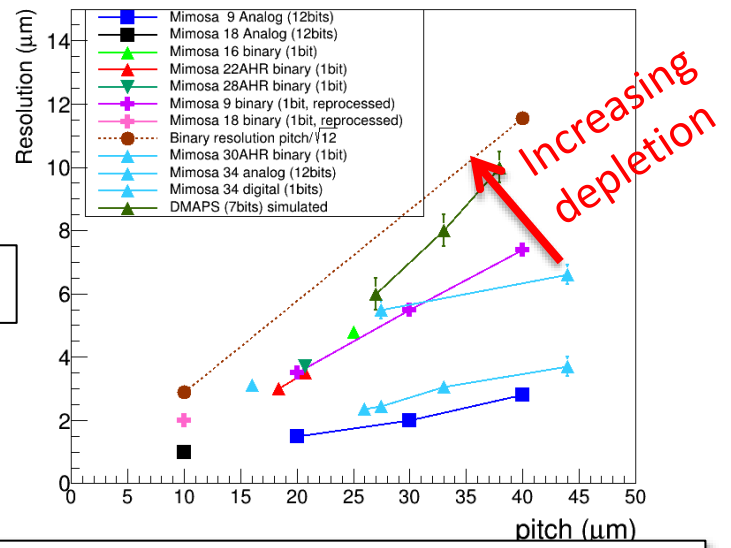


$$\Delta d_0|_{res.} \approx \frac{3\sigma_{r\phi}}{\sqrt{N+5}} \sqrt{1 + \frac{8r_0}{L_0} + \frac{28r_0^2}{L_0^2} + \frac{40r_0^3}{L_0^3} + \frac{20r_0^4}{L_0^4}}$$

$$\Delta d_0|_{m.s.} \approx \frac{0.0136 \text{ GeV}/c}{\beta p_T} r_0 \sqrt{\frac{d}{X_0 \sin \theta} \sqrt{1 + \frac{1}{2} \left( \frac{r_0}{L_0} \right) + \frac{N}{4} \left( \frac{r_0}{L_0} \right)^2}}$$

d = layer thickness, N = # layers

CMOS pixel resolution vs pitch



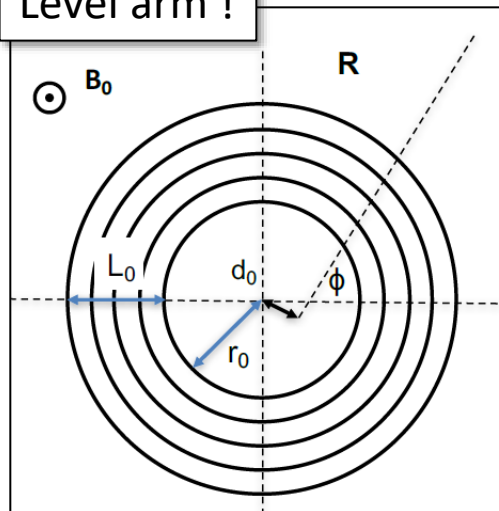
⇒  $\sigma_{sp} \sim 3 \mu\text{m}$  ⇔ pitch  $\sim 15\text{-}20 \mu\text{m}$   
 (assuming binary output,  $\sim 20 \mu\text{m}$  epi.thickness & large depletion in 180nm tech.)

# Tracker requirements

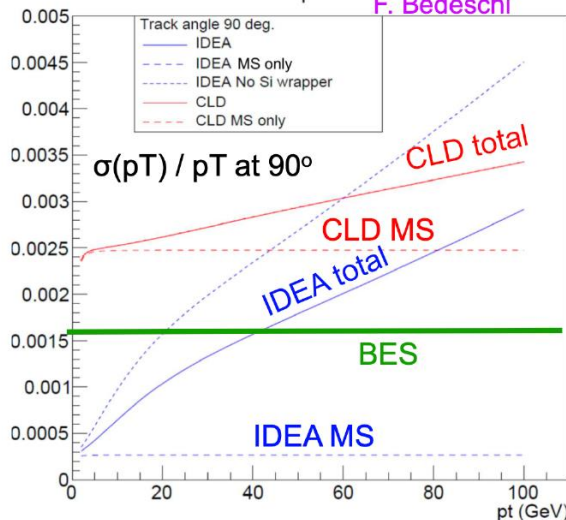
## Expected performances

$$\frac{\sigma_{p_T}}{p_T^2} \sim 2 \times 10^{-5} \text{ GeV}^{-1}$$

Level arm !



F. Bedeschi



- ⇒ Momentum resolution
- ⇒ Tracking efficiency
- ⇒ Track separation, low p fake tracks
- ⇒ Etc.

- Level arm also plays a crucial role for the VTX

- Material budget vs intrinsic resolution
  - ✓ Typically  $\sigma_{sp} \sim 5\text{-}10 \mu\text{m}/\text{layer}$  ; material  $\sim 1\text{-}2\% X_0/\text{layer}$  ; Power  $\sim < 100 \text{ mW}/\text{cm}^2$
  - ✓ Low momentum vs high momentum  $\leftrightarrow$  physics input
- 2 main options:
  - ✓ All silicon (CLD, CLICdet, SiD)
    - Few high resolution layers
    - Possibly timing capabilities
  - ✓ Silicon + Gaseous detector
    - TPC (ILD) / Drift Chamber (IDEA) / RICH (CLD ?)
    - dEdx/dNdx capabilities,
    - More hits, overall less materials
    - TPC: Ion back flow issue for circular colliders
- PID Strategy to be included (RICH, timing, dEdx, etc.)

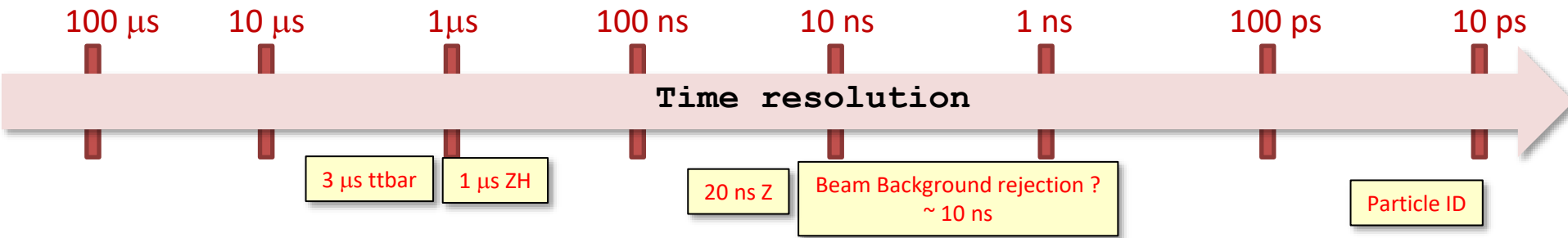
Drasal, Riegler, <https://doi.org/10.1016/j.nima.2018.08.078>

$$d_{tot}/X_0 = (N + 1)d/X_0. \quad d = \text{layer thickness, } N = \# \text{ layers}$$

$$\left. \frac{\Delta p_T}{p_T} \right|_{m.s.} \approx \frac{0.0136 \text{ GeV}/c}{0.3\beta B_0 L_0} \sqrt{\frac{d_{tot}}{X_0 \sin\theta}} \quad \left. \frac{\Delta p_T}{p_T} \right|_{res.} \approx \frac{12 \sigma_{r\phi} p_T}{0.3 B_0 L_0^2} \sqrt{\frac{5}{N+5}}$$

m.s. term dominates for  $p_T \sim < O(100) \text{ GeV}/c$

# Timing & 4-D tracking



- Time resolution  $\Delta t$ 
  - ✓ Bunch separation (3  $\mu$ s / 1  $\mu$ s / 20 ns @ FCCee)
  - ✓ Background rejection ? (1-10 ns range)
  - ✓ Particle ID (10-100 ps)
- Usual drawbacks to go faster
  - ✓ Power consumption
  - ✓ Active Cooling & geometrical acceptance due to services
  - ✓ In pixel circuitry  $\Rightarrow$  larger pixels (or multipixels)
  - ✓ Fill factor, dead time
  - ✓ PID Restricted to low momentum particles ( $\sim <$  few GeV/c)
- Still
  - ✓ Forward region not covered by a central gaseous detector (TPC)
  - ✓ Added value for intermediate radii (e.g. LLPs ?)
- Specialized layers
  - ✓ Doesn't compromise the other requirements (material budget and granularity)
    - Probably not in the most inner layers



# Particle ID and time resolution DRD4 & 1/3

<b>TF#1</b> Gaseous Detectors Anna Colaleo Leszek Ropelowski	<b>TF#2</b> Liquid Detectors Rosanne Guenette Jocelyn Monroe	<b>TF#3</b> Solid State Detectors Nicola' Cartiglia Giulio Pellegrini	<b>TF#4</b> Photon Detectors & PID Neville Harnwell Peter Krizan	<b>TF#5</b> Quantum & Emerging Technologies Marcel Demarteau Michael Doser	<b>TF#6</b> Calorimetry Roberto Ferrari Roman Poeschl
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More details here:

<https://indico.cern.ch/event/1202105/contributions/5402790/attachments/2662086/4612032/FCC-DRD4.pdf>

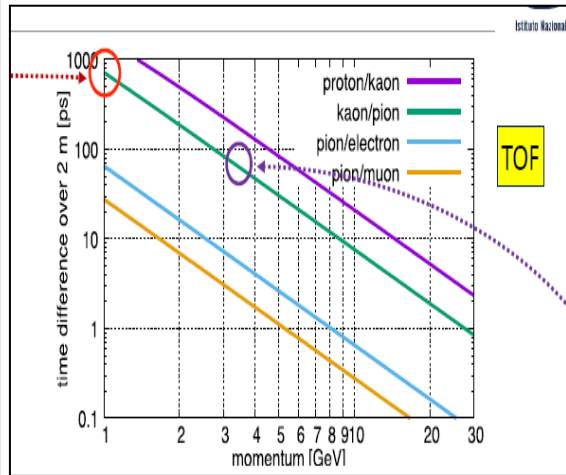
## • Goal:

- ✓  $K/\pi$ ,  $\pi/e^-$  separation, etc.  $\Rightarrow$  Interest to push beyond 10 ps resolution
- ✓ Even more important for the physics program @ Z peak

Fast timing (<100 ps)  
Solid state (pixelated) detector (DRD3)

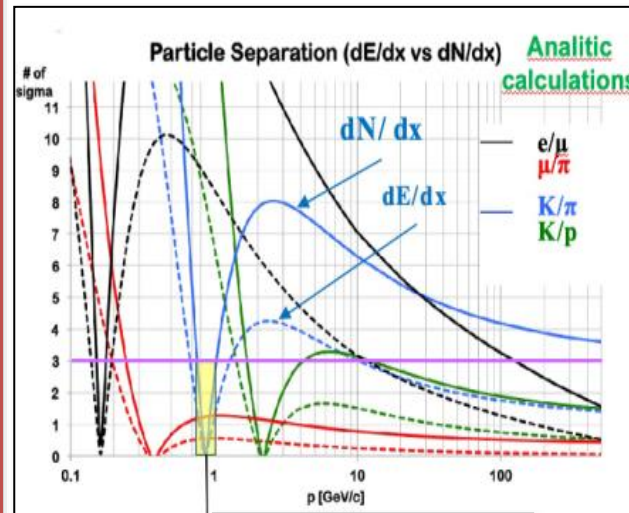
$dE/dx + dN/dx$   
Mainly gaseous detector, e.g. TPC, DC, RICH (DRD1)

Time difference (ps)



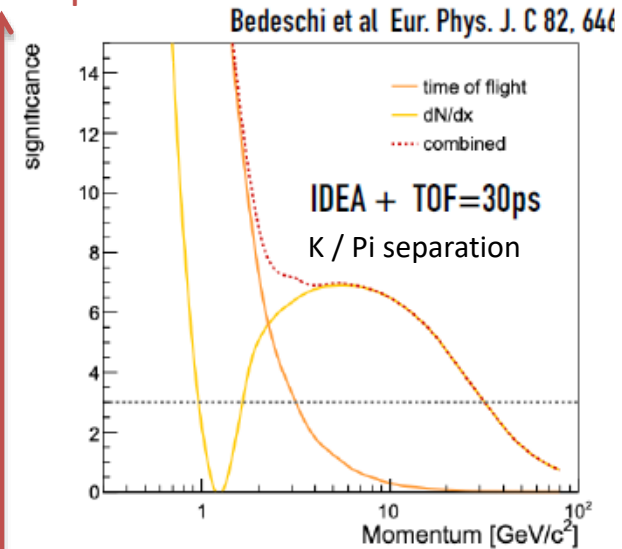
Time of Flight

Separation Power (significance)



$dE/dx - dN/dx$

Separation Power



Combined measurement

Momentum (GeV/c)

# Particle ID and time resolution DRD4 & 1/3

<b>TF#1</b> Gaseous Detectors Anna Colaleo Leszek Ropielewski	<b>TF#2</b> Liquid Detectors Rosanne Guenette Jocelyn Monroe	<b>TF#3</b> Solid State Detectors Nicolo' Cartiglia Giulio Pellegrini	<b>TF#4</b> Photon Detectors & PID Neville Harnwell Peter Krizan	<b>TF#5</b> Quantum & Emerging Technologies Marcel Demarteau Michael Doser	<b>TF#6</b> Calorimetry Roberto Ferrari Roman Poeschl
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More details here:

<https://indico.cern.ch/event/1202105/contributions/5402790/attachments/2662086/4612032/FCC-DRD4.pdf>

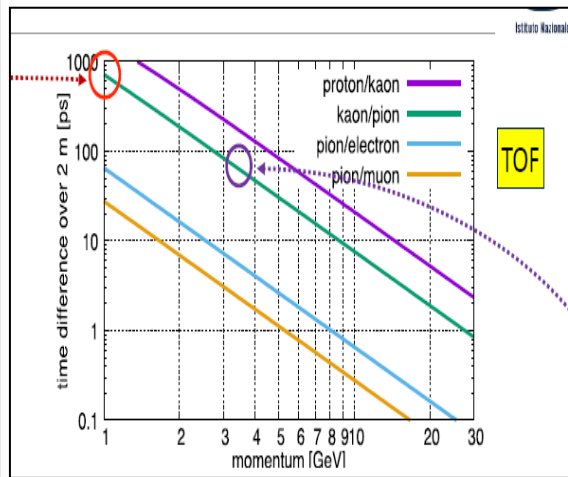
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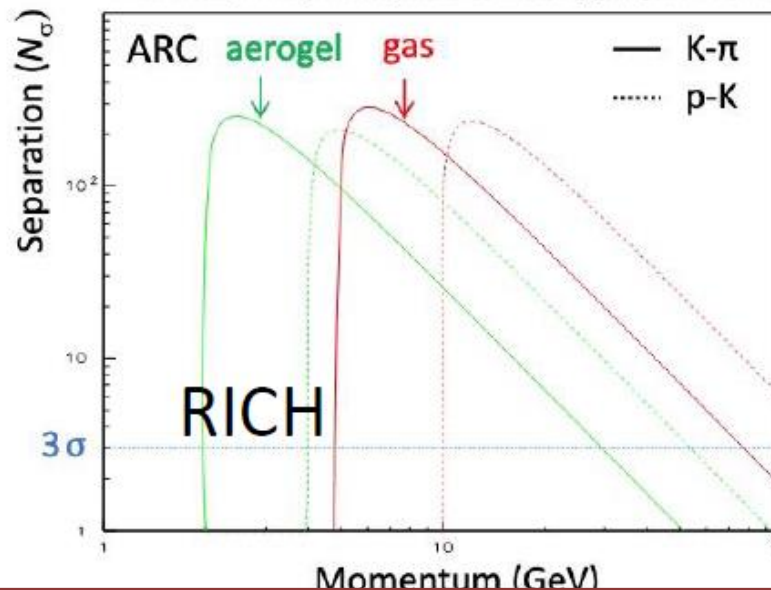
Time difference (ps)



Time of Flight

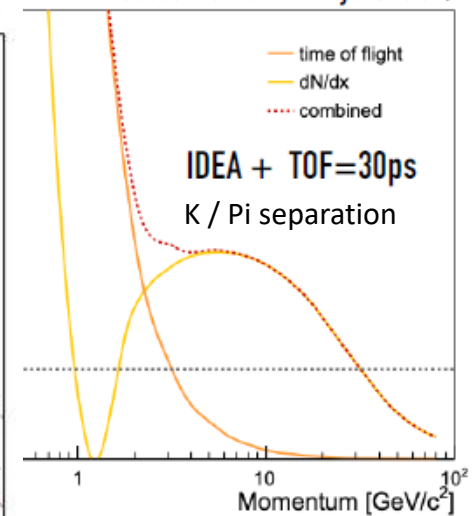
Separation ( $N_\sigma$ )

Preliminary analytic calc., assumes focusing target achieved



Separation Power

Bedeschi et al Eur. Phys. J. C 82, 646



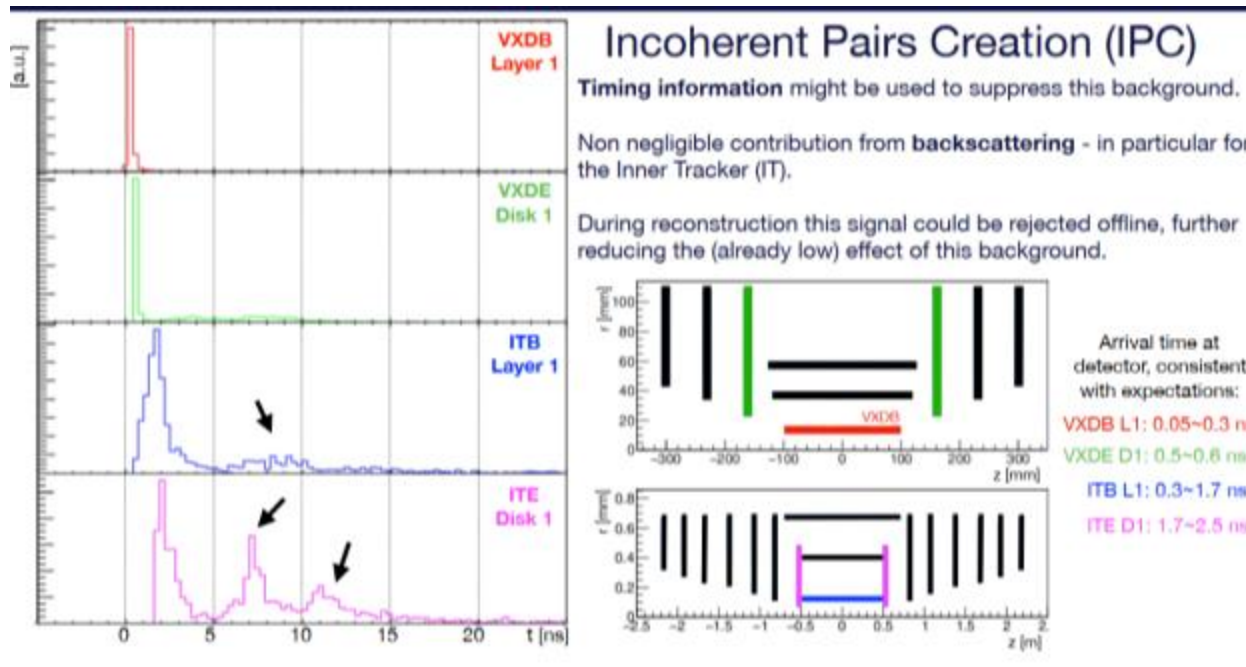
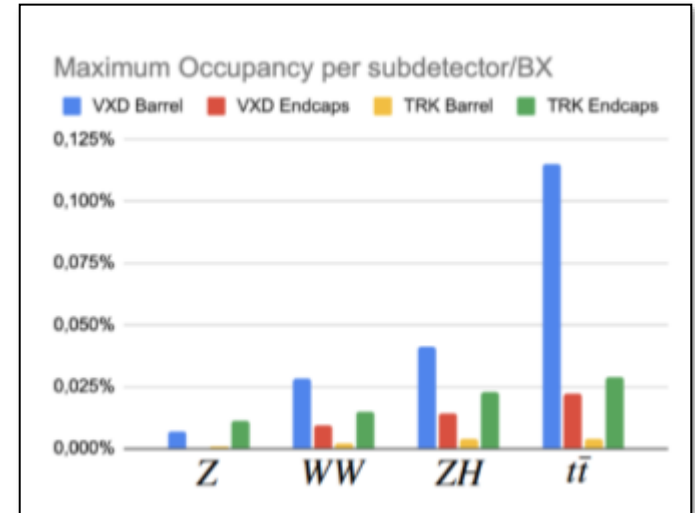
combined measurement

Particle ID has to be integrated in the VTX/TRK concept

# Example of study in CLD

	Z	WW	ZH	Top
<b>Bunch spacing [ns]</b>	30	345	1225	7598
<b>Max VXD occ. 1us</b>	2.33e-3	0.81e-3	0.047e-3	0.18e-3
<b>Max VXD occ.10us</b>	23.3e-3	8.12e-3	3.34e-3	1.51e-3
<b>Max TRK occ. 1us</b>	3.66e-3	0.43e-3	0.12e-3	0.13e-3
<b>Max TRK occ.10us</b>	36.6e-3	4.35e-3	1.88e-3	0.38e-6

[US FCC workshop 25/04/2023 Ciarma](#)

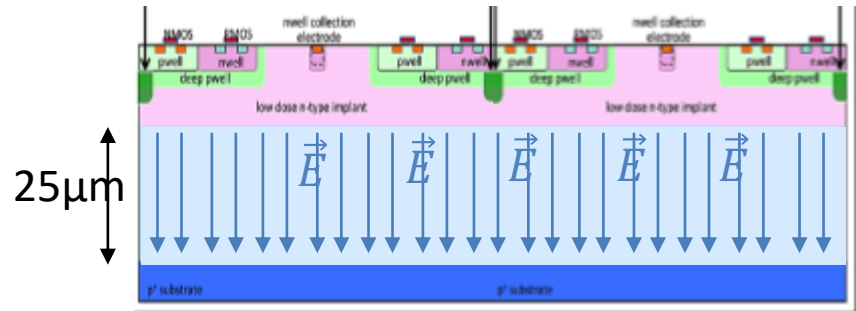
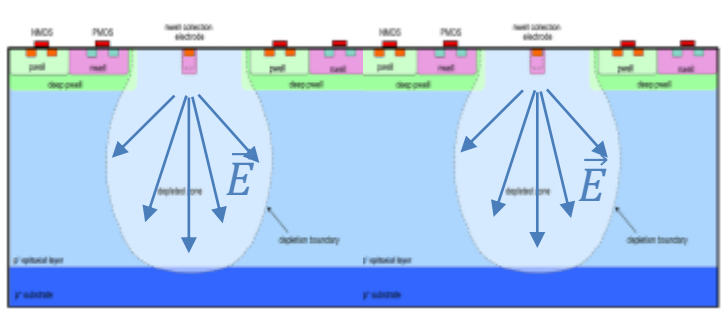


BX rate might be an issue at the Z-pole

Timing resolution range to reject background ~ 1 ns

# Example: MIMOSIS (CBM-MVD) & Decision on options for sensing elements

## Process modification: Standard? P-stop? N-Gap?



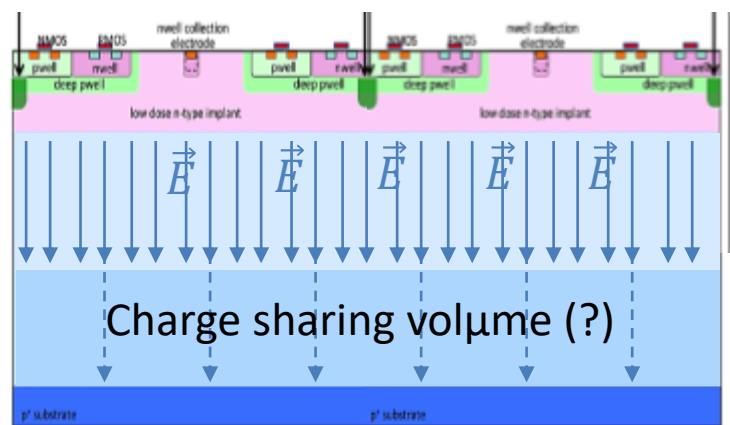
25 or 50 μm epi?

$\sigma = 4 - 5 \mu\text{m}$   
 $> 3 \times 10^{13} n_{\text{eq}}/\text{cm}^2$

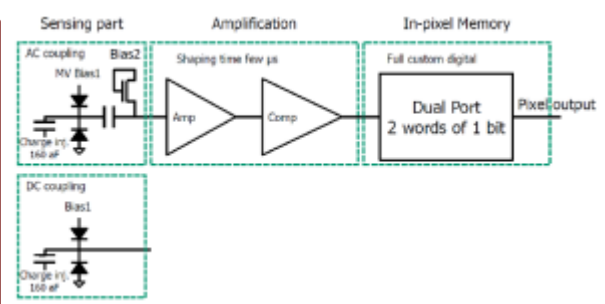
Spatial resolution  
 Rad. hardness

$\sigma = 5 - 7 \mu\text{m}$   
 $> 30 \times 10^{13} n_{\text{eq}}/\text{cm}^2$

Process options inherited from ALPIDE



AC? DC? pixel

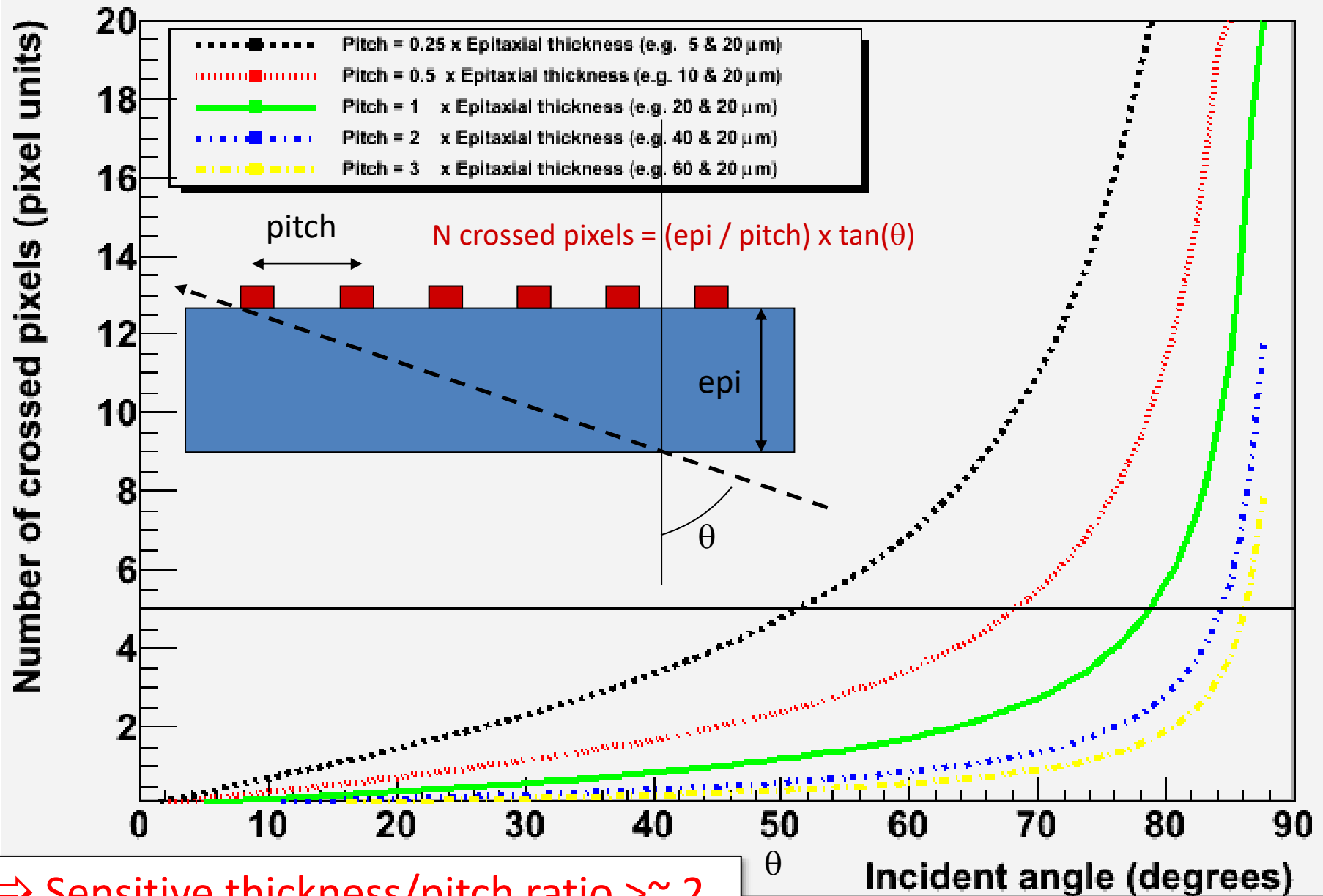


- Better spatial res. at given rad. tolerance?
- Higher S/N => Robustness to external noise?
- Nuclear fragment ID by dE/dx?

- DC pixel – limited rad. hardness.
- AC Pixel – more biasing lines.

W. Snoeys et al., NIM-A Vol.871 (2017) 90–96.  
 Munker, Vertex 2018, Status of silicon detector R&D at CLIC

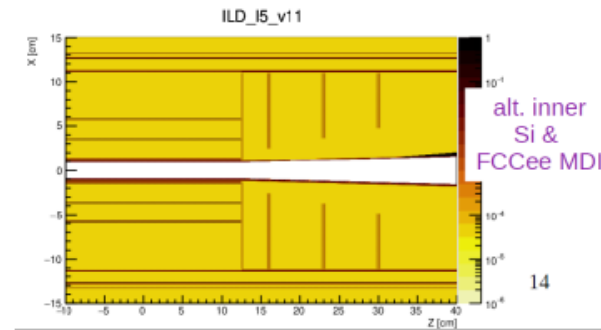
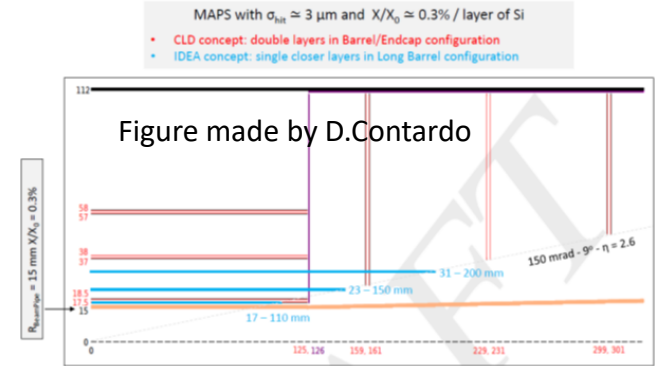
# Elongated clusters: low pT tagging



⇒ Sensitive thickness/pitch ratio  $> \sim 2$

# Vertex detector proposal @ ILD for FCCee

CLD and IDEA Vertex Detectors designs (superimposed)



D.Jeans

Technology: CMOS pixel sensor as a baseline

✓ (probably the generation after TPSCo 65nm)

MDI constraints (implemented by D. Jeans in the simulation)

✓ Inner layer as close as possible to the beam pipe:  $R_{min} \sim 12 \text{ mm}$

Geometry partly determined by the main tracker

✓ Adaptable to any detector concept

Requirements

✓ Minimized material budget ( $\sim < 0.15\% X_0$  per layer)

▪ Beam pipe radius/mat. budget fixes the requirement

✓ Spatial resolution  $\sim 3 \mu\text{m}$  / layer

✓ Time resolution:  $\sim 500 \text{ ns}$

✓ Moderate Power dissipation ( $\sim < 50 \text{ mW/cm}^2$ ) allowing for air flow cooling

✓ 5-6 layers in the inner radius ( $\sim < 6-10 \text{ cm}$ )

▪ Robustness / standalone tracking ( $\neq$  IDEA choice)

▪ Double sided option still considered but not easily compatible with a stitched approach

▪ « long barrel » preferable  $\Rightarrow$  minimize the distance between IP and the first hit

▪ Low momentum tracking capabilities

▪ Track seeding @ different radii : e.g. FIPs, highly ionizing particles, LLPs, etc.

✓ « merge » VTX and SIT ?

▪ Same technology ?  $\Rightarrow$  Power dissipation optimization

✓ Other pixel layers close to the main tracker

✓ Stitched sensor: very promising approach by ALICE ITS-3

▪ At least in the z dimension

▪ Bent sensor considered (caveat: acceptance)

Timing measurement capabilities ( $< 100 \text{ ps}$ )

▪ Either in a specialized/dedicated layer

▪ Or preferably included in the same technology if R&D allows it