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## Embedded FPGAs for Data Processing in Future e+e- Detectors

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Embedded field programmable gate array (eFPGA) technology allows the implementation of reconfigurable logic within the design of an application-specific integrated circuit (ASIC), enabling fast and flexible machine learning (ML) in readout electronics. Detectors at a future e+e- Higgs factory can benefit from increased intelligence throughout the data pipeline, for reduced data rates, faster inference, and increased efficiency. Furthermore, eFPGAs can be designed with a variety of open-source frameworks, lowering the cost of entry for institutional groups. This work presents recent developments of eFPGAs for collider applications, namely the design and tapeout of two eFPGAs and their ability to implement ML for at-source pixel readout. Next steps in eFPGA development are discussed, along with applications of eFPGA technology to other data processing tasks at Higgs factories and beyond.

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