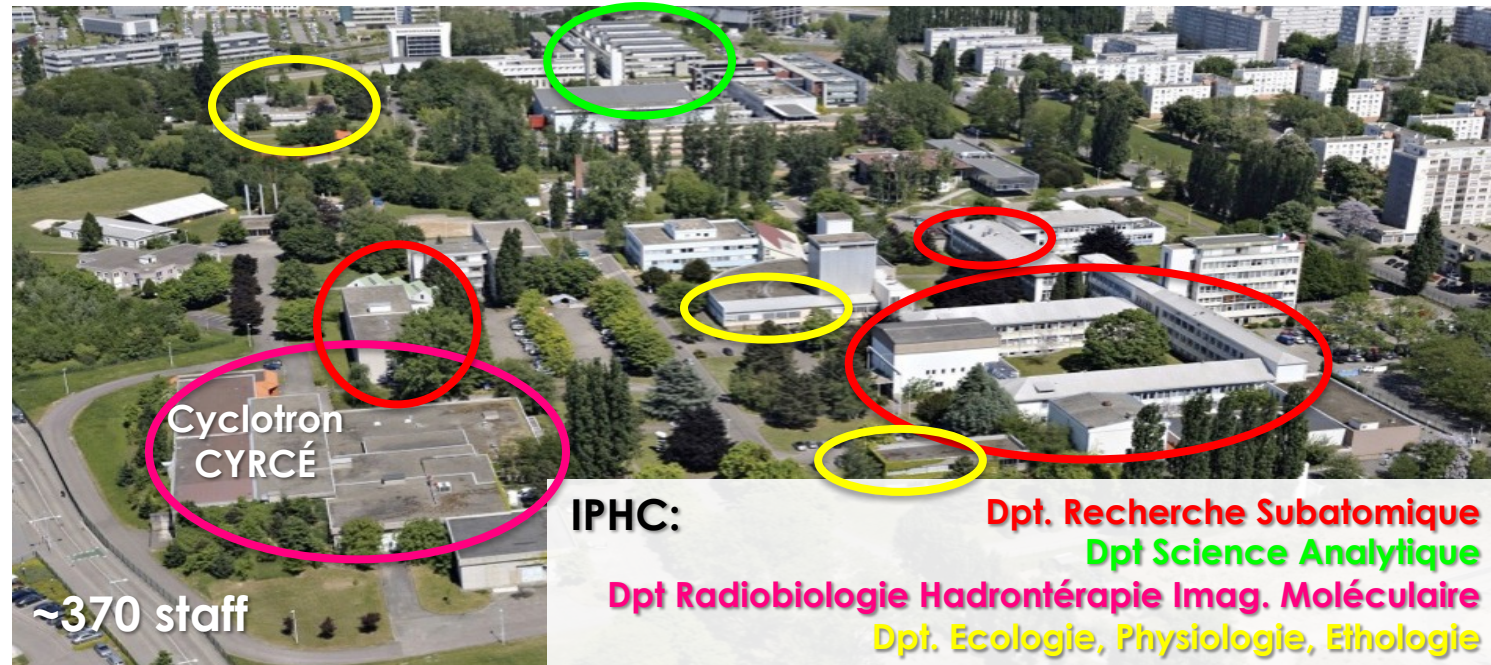
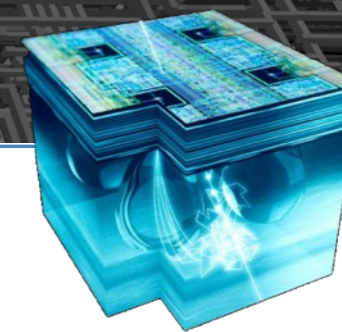


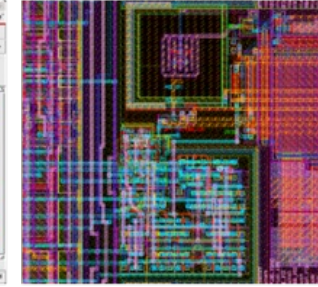
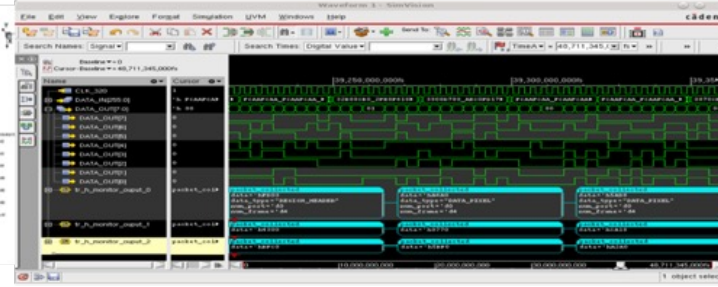
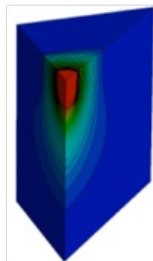
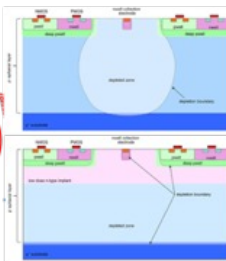
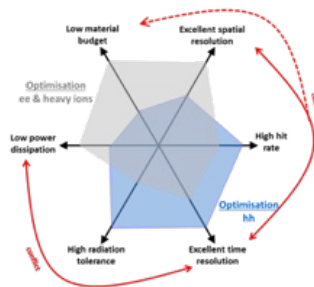
# Welcome at the core-facility C4Pi



- Expertise & missions
- Team, infrastructure & network
- Scientific drivers
- Recent achievements
- Focus on design /  $\mu$ -technologies



## Monolithic Active Pixel Sensor in CMOS technology & detection modules

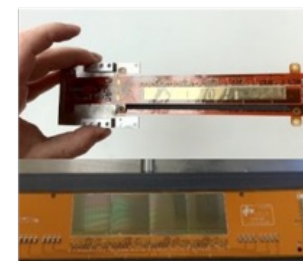
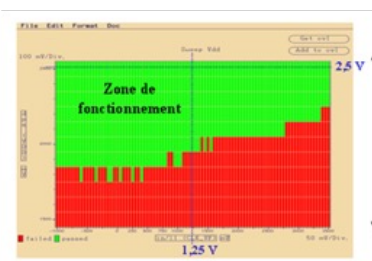


Requirements

Charge collection

Front-End (analog+digital) / Functional blocs / Syst. architecture

Fab interface

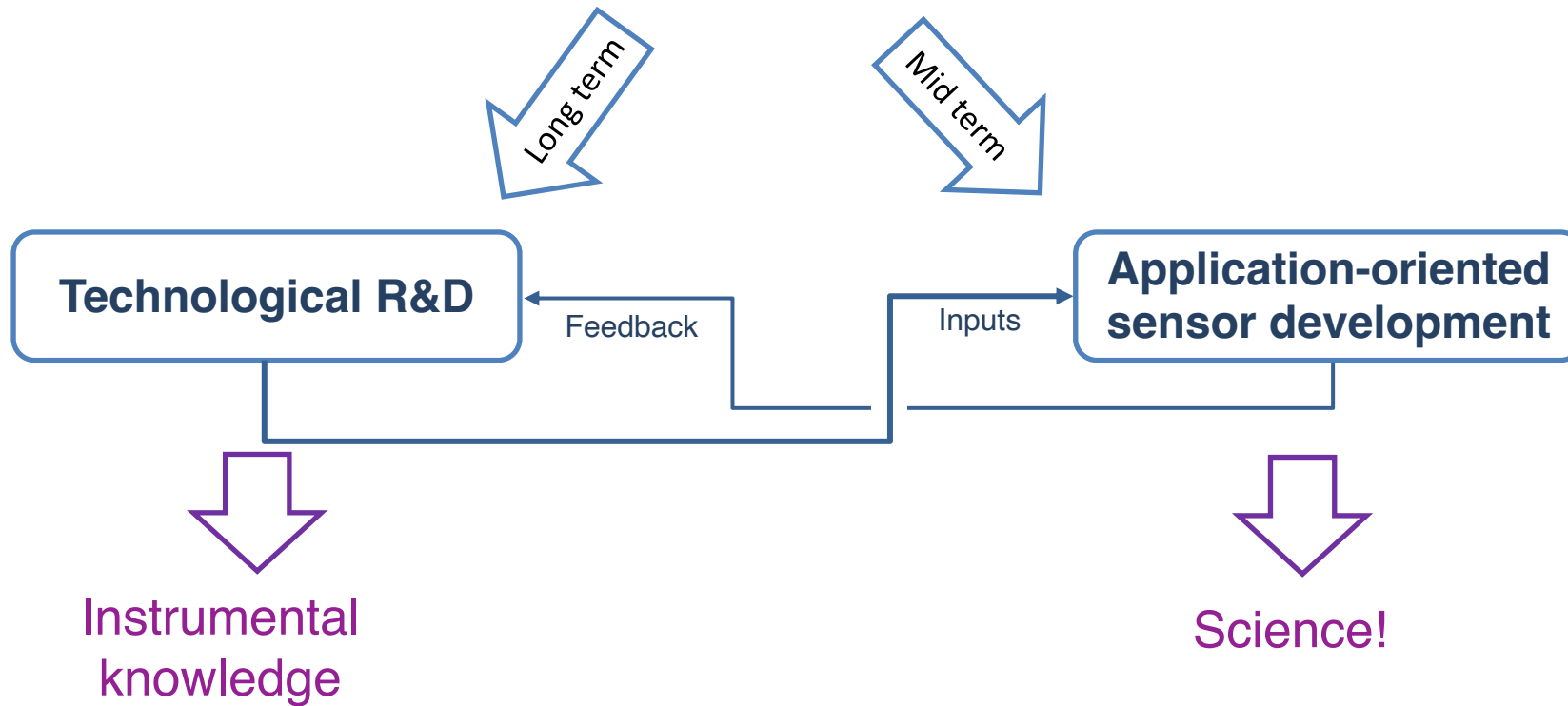


DAQ development & Tests

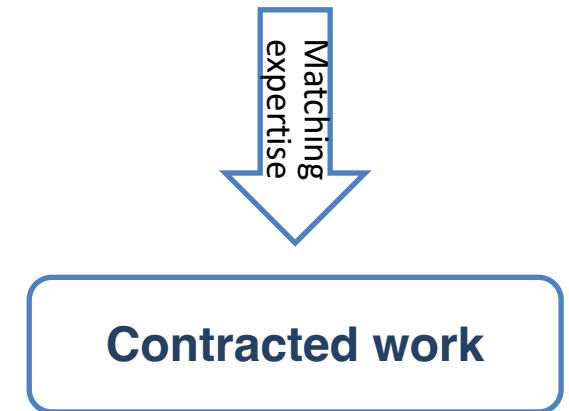
Integration

Construction Installation

Requests from scientific groups



Requests from users  
(lab or company)

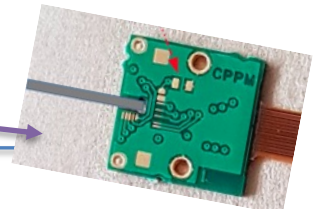
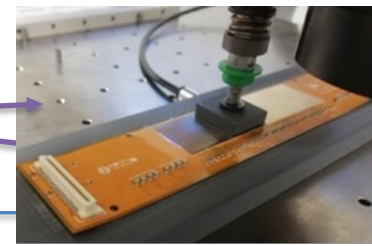
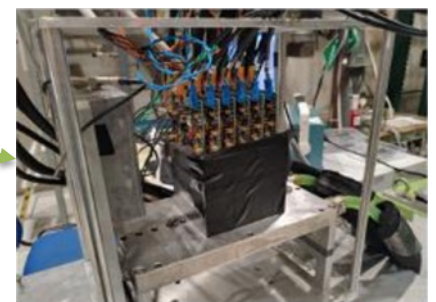
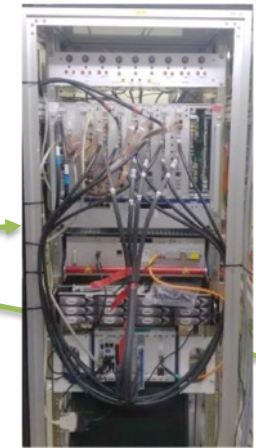
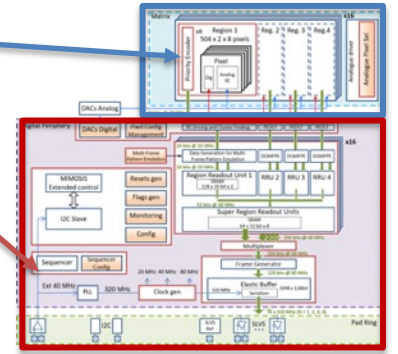
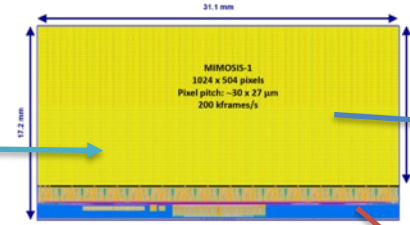
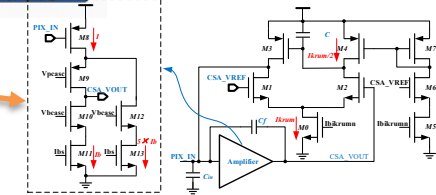
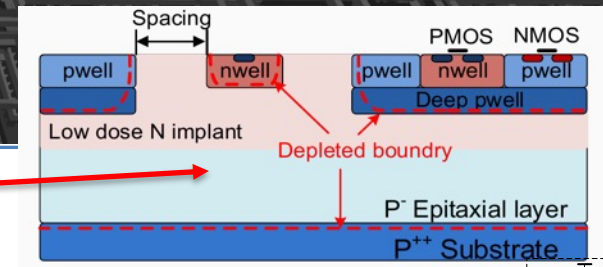


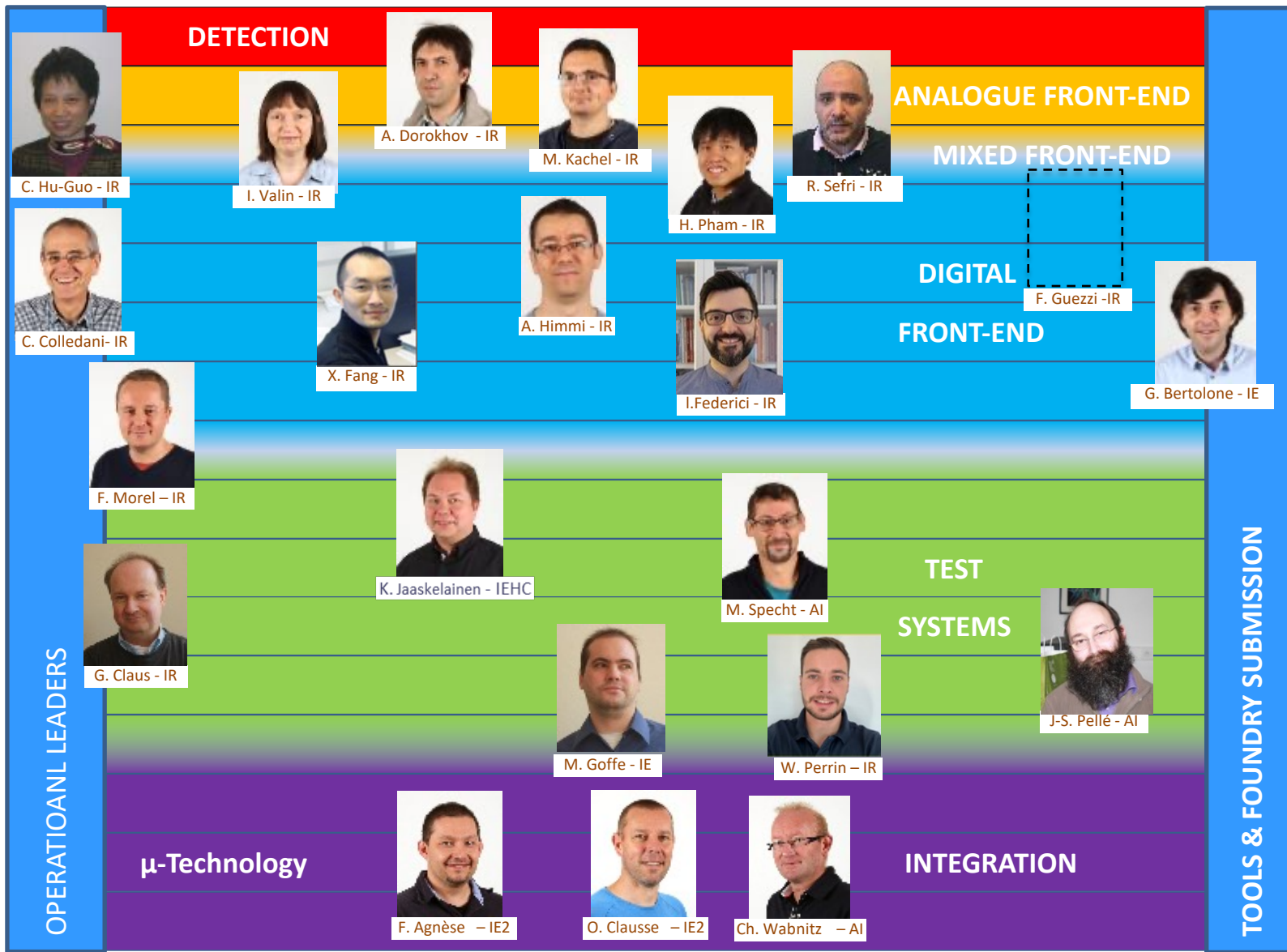
**Within IN2P3** { share expertise on MAPS  
facilitate CMOS imaging technology access  
foster structured projects

# Expertise

<b>Sensitive layer</b>	<b>Collection node</b>
In-pixel analogue front-end	Bias monitoring
Digital conversion	Digitisation
Digital front-end & back-end	Read-out architecture
Register Transfer Level	Top description
Integration	Synthesis, Layout
Design Rule Checking	Verification
IP, component creation	Fabrication
Test systems prototyping	DAQ, Control, data transfert systems design
Board, flex, detection module design	
Hardware / Firmware / Software design	Test setup intergration
IP validation	Functional tests
	Physical characterisation
Laboratory tests	Beam tests
	Production tests
Board/flex component population	tool design
ASIC/wafer probe testing	Module validation
Manual/automatic assembly	Bonding
	2D/3D Metrology

TOOLS & Foundry support





- PhD**



J. Soudier – Doc3



C. Lemoine (CERN) – Doc2



H. Shamas – Doc1



E. Sacchetti – Doc1

- Masters**

C. Antony, G. Morel, N. Vergara – M2 microelectronics

N. Favriou – M2 physics

- Bachelors**

M. Grau – L2 electronics

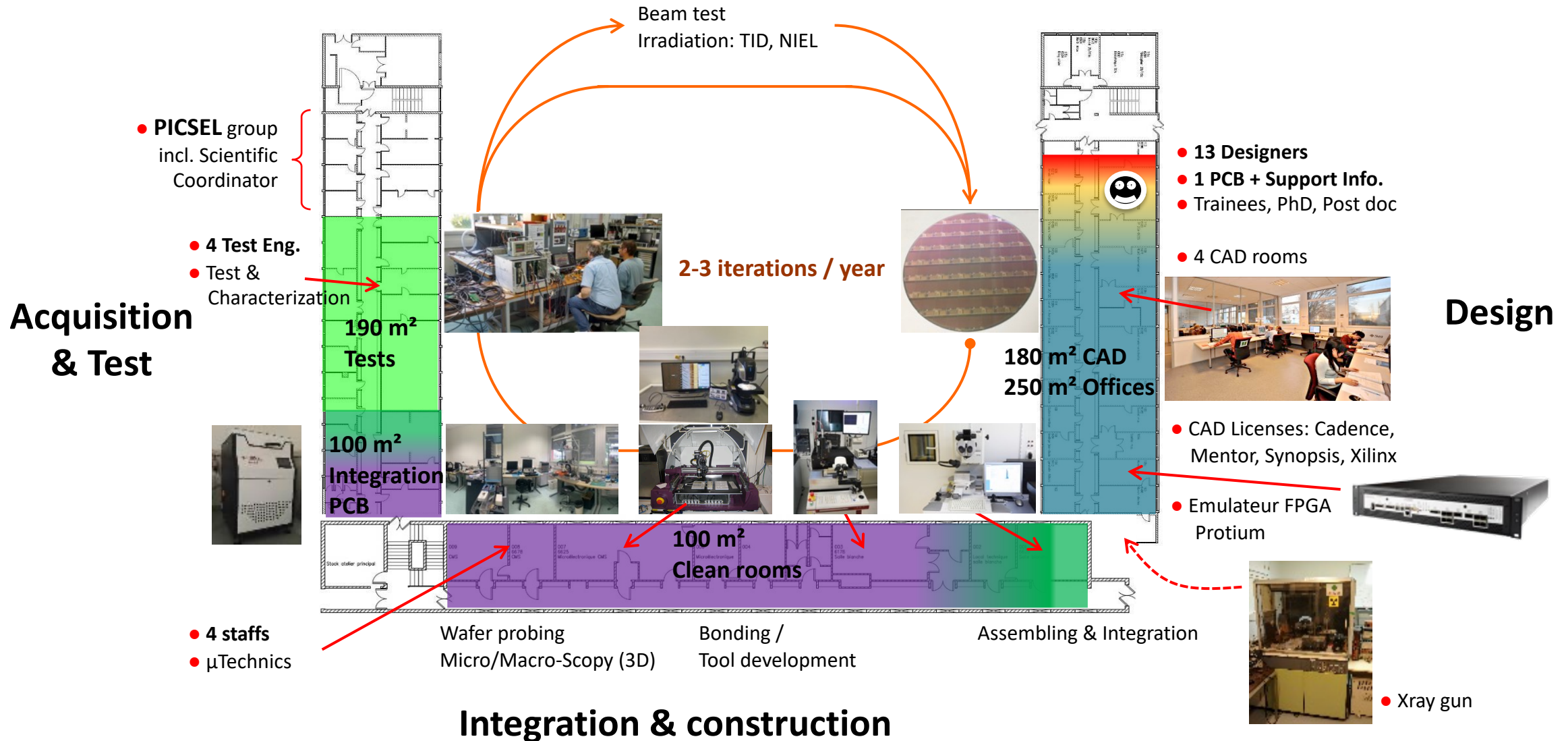
- Apprentices**



B. Faechtig – LPro1



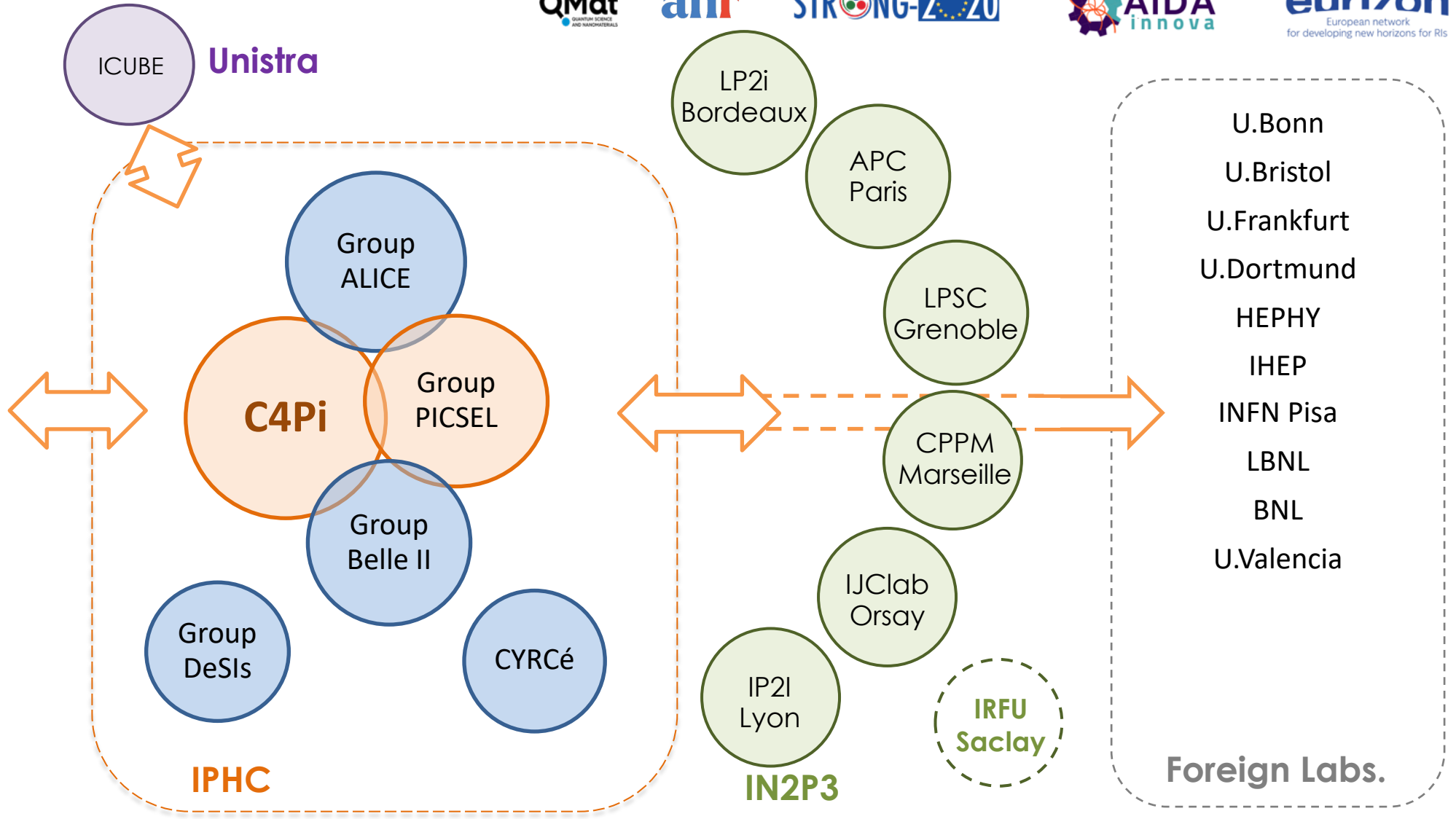
T. Jacques – BUT3

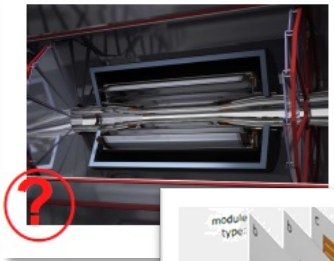


# Network



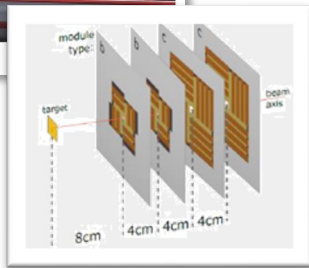
Access to technologies  
characterisation  
Applications





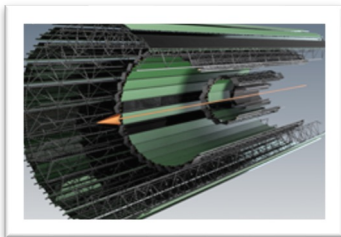
Future ee coll.  
Update ~2026

- CE-65 & SPARC programs for vertex (VTX) and tracker
  - Vertex sensor to renew reference planes of beam telescope
  - Tracker sensor in synergy with ALICE3



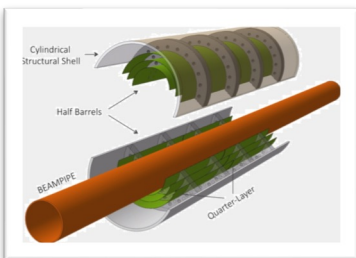
CBM  
→ ~2028

- MIMOSIS & CE-18 programs for vertex (MVD)



Belle II  
→ ~2029

- OBELIX program for vertex upgrade (VTX)



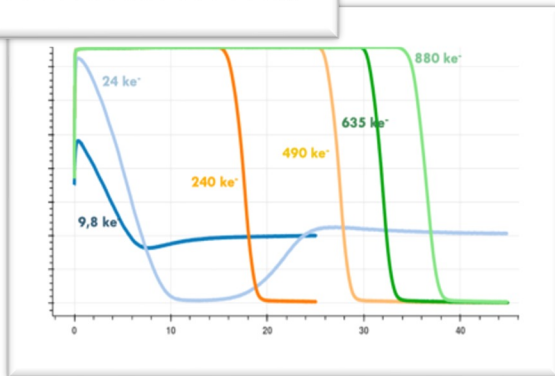
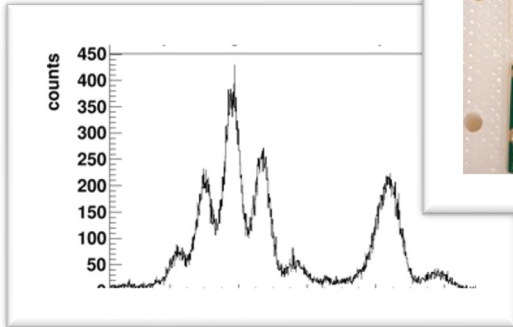
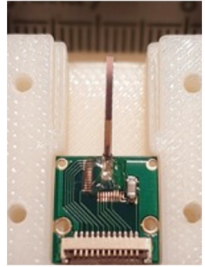
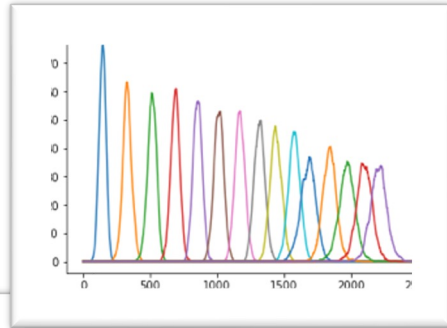
ALICE  
→ 2027  
→ 2033

- MOSS/MOSAIX program for new vertex ITS3
- Common R&D CE-65 for tracker ALICE3



# Sensor comparison

## Applications



- **Monolithic-Imager**

multi-purpose with analogue output: dosimetry/spectroscopy

back-side illumination for sensitivity to very-low penetrating quanta

- **TIIMM / TIXX**

Ion tracking & identification (through  $\Delta E$ )

Signal digitized (ToT) with  $10^4$  dynamic

- **IMIC**

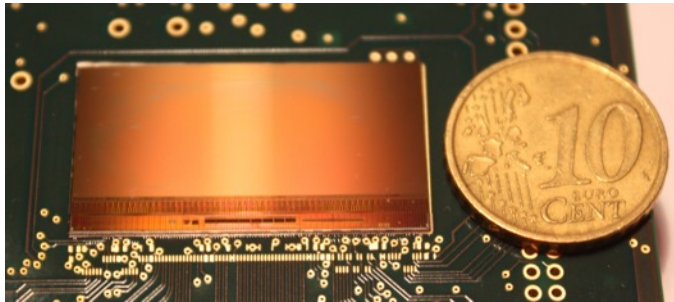
$\beta$  imaging in awake rodents brain

ALPIDE made a needle

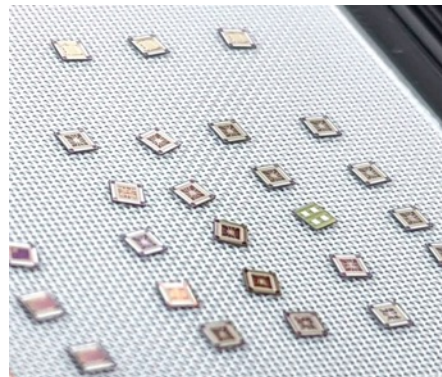
- **MIMOSIS**

– Various applications possible in spectroscopy

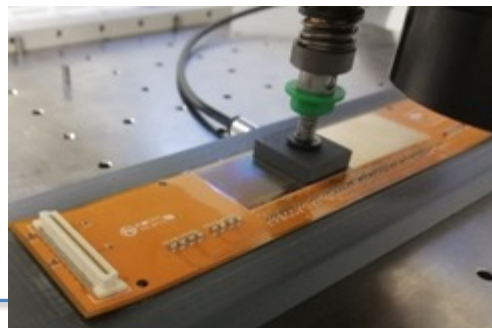
# Recent achievements



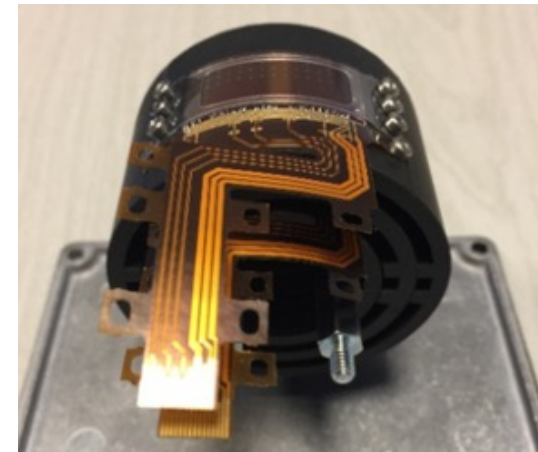
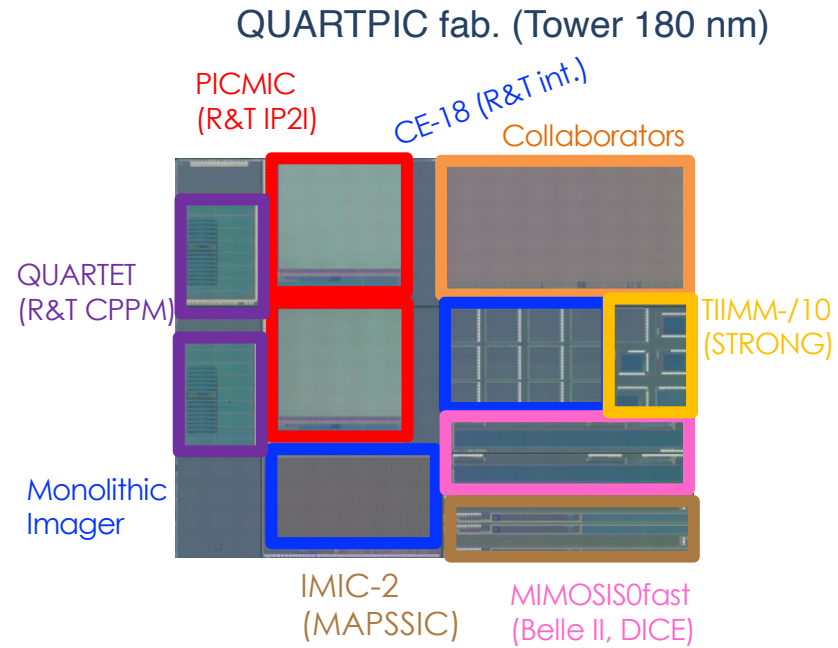
MIMOSIS sensor



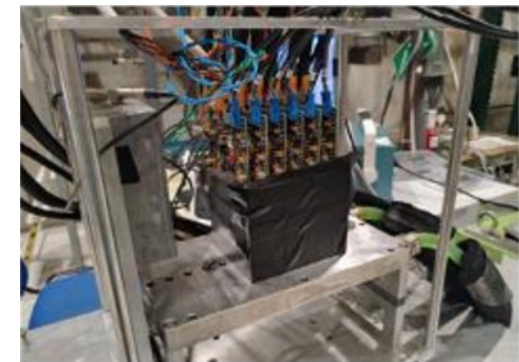
New TPSCo 65 nm proc.



FOOT  
module

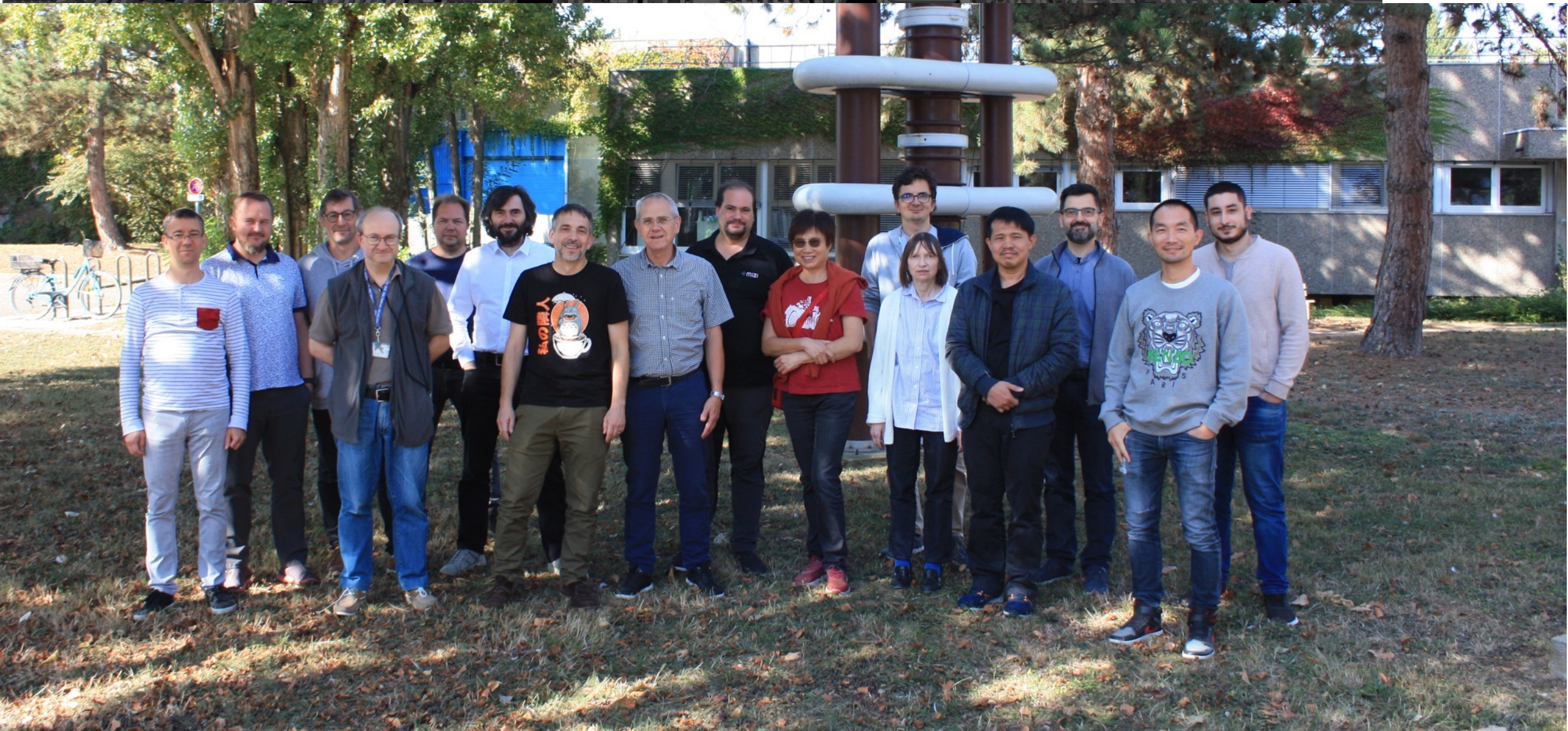


Bended sensors



New beam telescope

# Part of the Strasbourg technical team developing MAPS (Oct.23)



# Focus on micro-technology activities

---



- Maintaining access to processes costs resources → **limited nb of CMOS processes** handled at C4Pi
- C4Pi strategy: **mainstream processes** → fabrication robustness guaranteed

MIMOSA-1, 1999, AMS 600 nm

- AMS 350 nm

- **2004**-2011
- MIMOSA-26
- MIMOSA-28

- XFAB 350 nm

- ALPHABeast for DeSis 2022

- Tower 180 nm

- Since **2011**
- CE-18 + others for R&D
- ALPIDE for ALICE-ITS2
- MIMOSIS for CBM-MVD
- OBELIX-1 for Belle II-VTX
- TIIX for STRONG EU-project

- TPSCo 65 nm

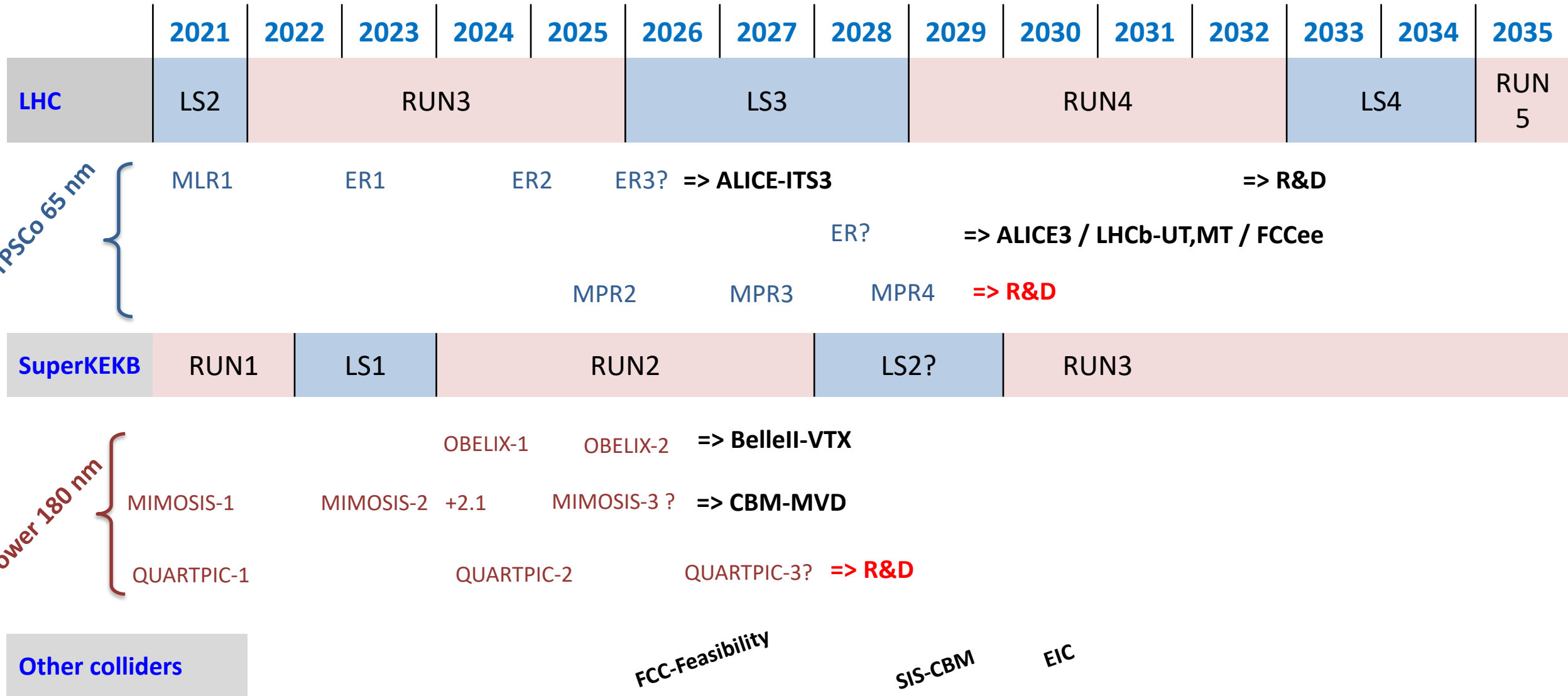
- Since **2020**
- MOSS for ALICE-ITS3
- CE-65 series for R&D
- **Main choice for ECFA-DRDs**



Current processes

**C4Pi proposal to ECFA-DRD7 for 180 nm contact-point (2024-27)**

# Roadmap on Tower technologies



Scientific applications → R&D @ C4Pi with general trend = maximise CMOS-MAPS potential

- R&D = upstream advances (+ PhD / Master student)

## ■ Amplification in silicon

- Low gain <10, get rid of analogue front-end  
=> Reduce pixel pitch, reduce power dissipation, increase sensitivity
- Start with Tower 180 nm, then switch to 65 nm
- Programmed financed by French ANR / DRD3: Tower, Icube, CPPM, CERN

## ■ Read-out architecture

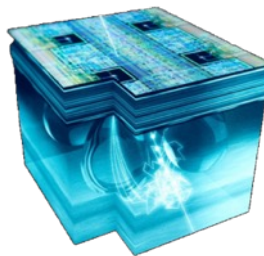
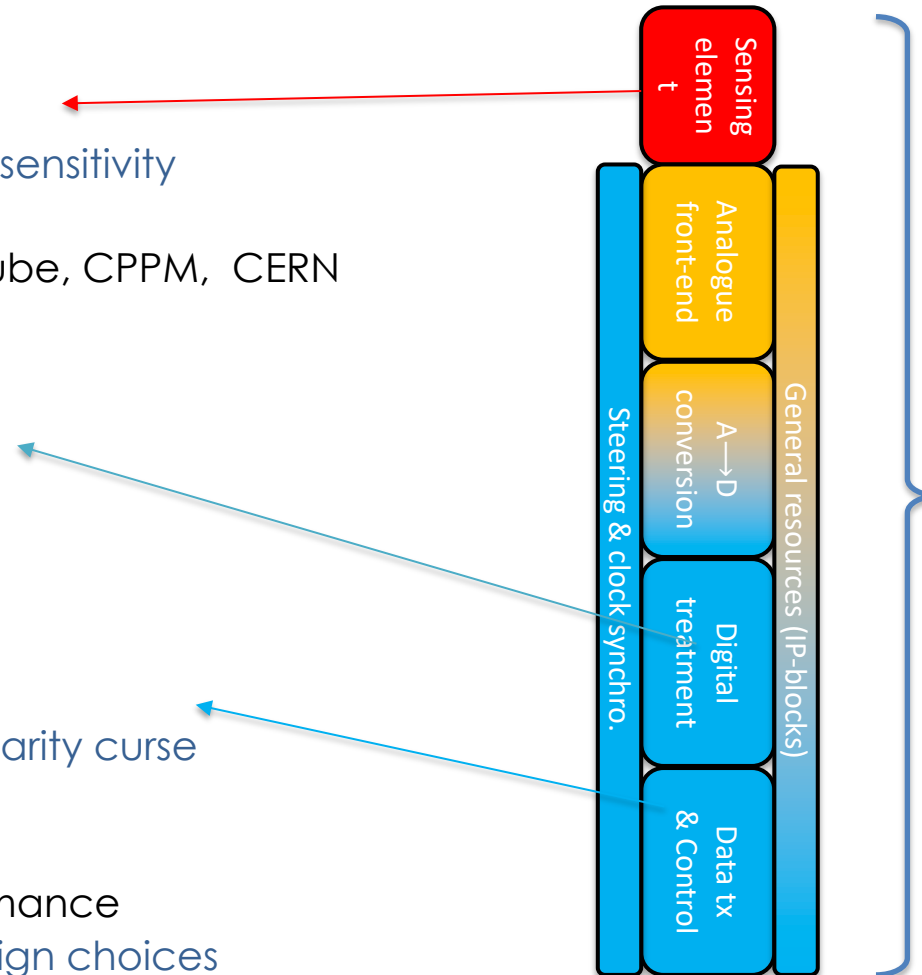
- Explore asynchronous logic (entirely event driven)  
=> Increase versatility (hit-rate, area), decrease power
- Within TPSCo 65 nm R&D: DRD3/DRD7

## ■ Intelligence on sensor

- Embedded processor and artificial intelligence  
=> Data reduction, ease control/calibration against granularity curse

## ■ Prototyping

- Develop full simulation chain from Physics to Final performance  
=> Prediction at early stage of development for better design choices
- Within DRD7

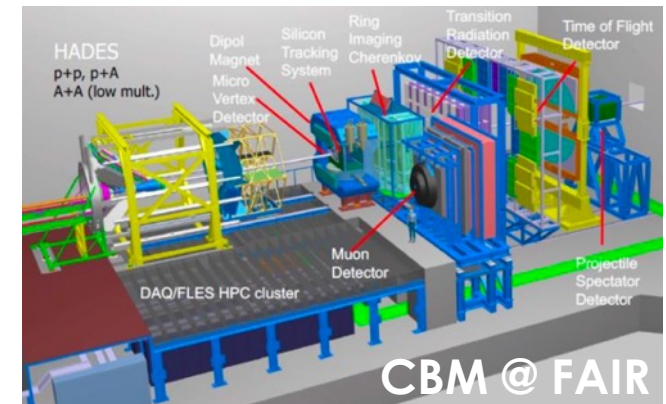




# Direct application: CBM-MVD

## MIMOSIS sensor

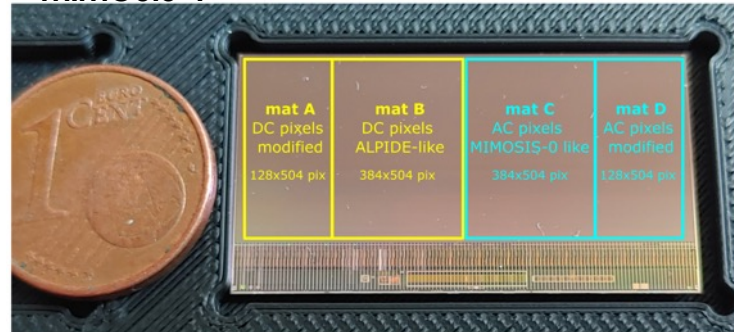
- Match CBM vertex requirements & achieve step forward / Higgs-Factories  
**combine** position res. ( $\sim 5\mu\text{m}$ ) & low-power ( $< 100\text{ mW/cm}^2$ ) & high hit-rate ( $> 50\text{ MHz/cm}^2$ )
- Specificity of CBM collisions: 100 kHz Au+Au @ 11 AGeV and 10GHz p+Au @ 30 AGeV  
 $\Rightarrow$  large hit-rate fluctuation & operation in vacuum



### Full specs for MIMOSIS sensor

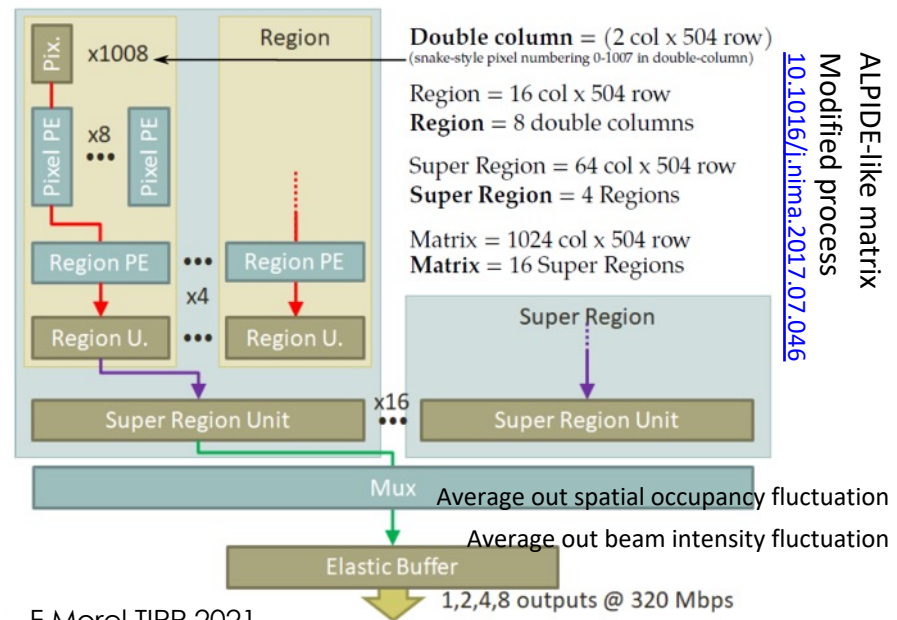
Position resolution	$\sim 5\mu\text{m}$
Time resolution / continuous r.o.	$\sim 5\mu\text{s}$
Power dissipation	$< 100 - 200\text{ mW/cm}^2$
Hit rate (average/50 $\mu\text{s}$ peak)	20/70 MHz/cm <sup>2</sup>
Material budget / layer	0.05 % X <sub>0</sub>
Operation temp in <b>vacuum</b>	- 40°C to +30°C
Radiation* (non-ionizing)	$\sim 7 \times 10^{13}\text{ n}_{\text{eq}}/\text{cm}^2$
Radiation* (ionizing)	$\sim 5\text{ Mrad}$
Radiation gradient	100 %
Heavy Ions-tolerance	10 Hz/mm <sup>2</sup>

MIMOSIS-1



Parameter	Value
Technology	TowerJazz CIS 180 nm
Epitaxial layer	$\sim 25\mu\text{m}$ thick, $> 1\text{ k}\Omega\text{-cm}$
Sensor thickness	300 $\mu\text{m}$ or 60 $\mu\text{m}$
Pixel size	26.9 $\mu\text{m} \times 30.2\mu\text{m}$
Pixel array	1024 $\times$ 504 pixels
Sensitive area	$\approx 4.2\text{ cm}^2$
Array readout time	$\approx 5\mu\text{s}$
Power consumption	$< 100\text{ mW/cm}^2$

### Full digital on top design



ALPIDE-like matrix  
 Modified process  
[10.1016/indico.2017.07.046](https://indico.in2p3.fr/event/10.1016/indico.2017.07.046)

## MIMOSIS sensors

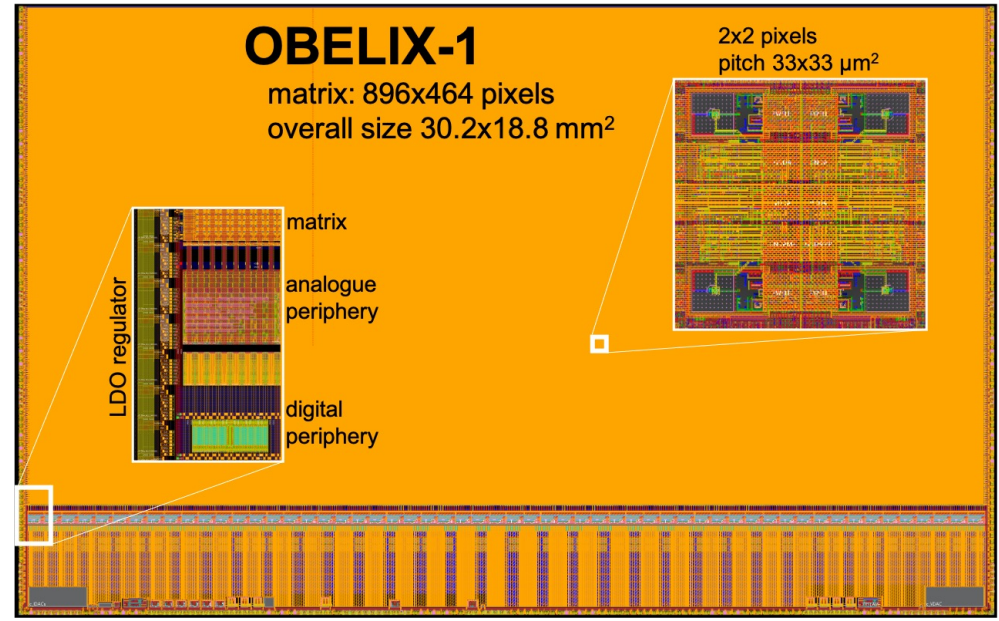
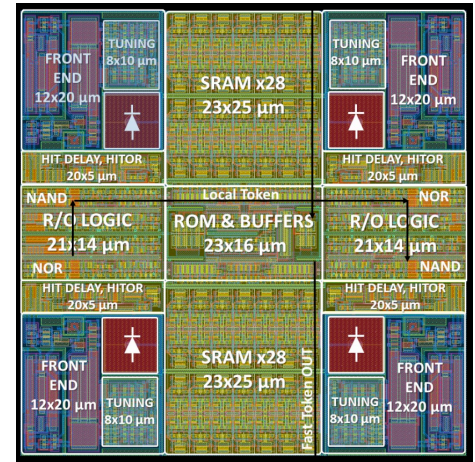
- MIMOSIS-1 (2020)
- MIMOSIS-2 (submitted 2022)
- Final MIMOSIS-3

F.Morel TIPP 2021,  
<https://indi.to/vf5Vs>

## OBELIX sensor

- Dedicated sensor for the upgrade of the VTX
- Based on TJ-MONOPIX-2 (Tower 180 nm)
  - C. Bepin VCI 2022 <https://indi.to/5BR2P>
- Collaboration between
  - Bergamo, Bonn, Dortmund, Gottingen, KEK-Tsukuba, Marseille, Pavia, Strasbourg\*, Valencia and Vienna
- Specifications
  - Larger matrix / TJ-MONOPIX2: 896x464 pixels
  - Triggered @ 30 kHz with up to 10  $\mu$ s delay
    - ✓ Maximum hit rate 120 MHz/cm<sup>2</sup>
  - Time binning 100 ns
    - ✓ Additional precision ~3ns @ low multiplicity (~10 MHz/cm<sup>2</sup>)
  - LDO regulator for voltage supply
  - Power budget 170-200 mW/cm<sup>2</sup>
  - Exploratory feature to input trigger
    - ✓ Fast output (~100 ns) with coarse granularity

TJ-Monopix2 pixel



# Example of distributed design

## OBELIX sensor for Belle II

IFIC

Bonn, IPHC, CPPM, Bergamo/Pavia, KEK

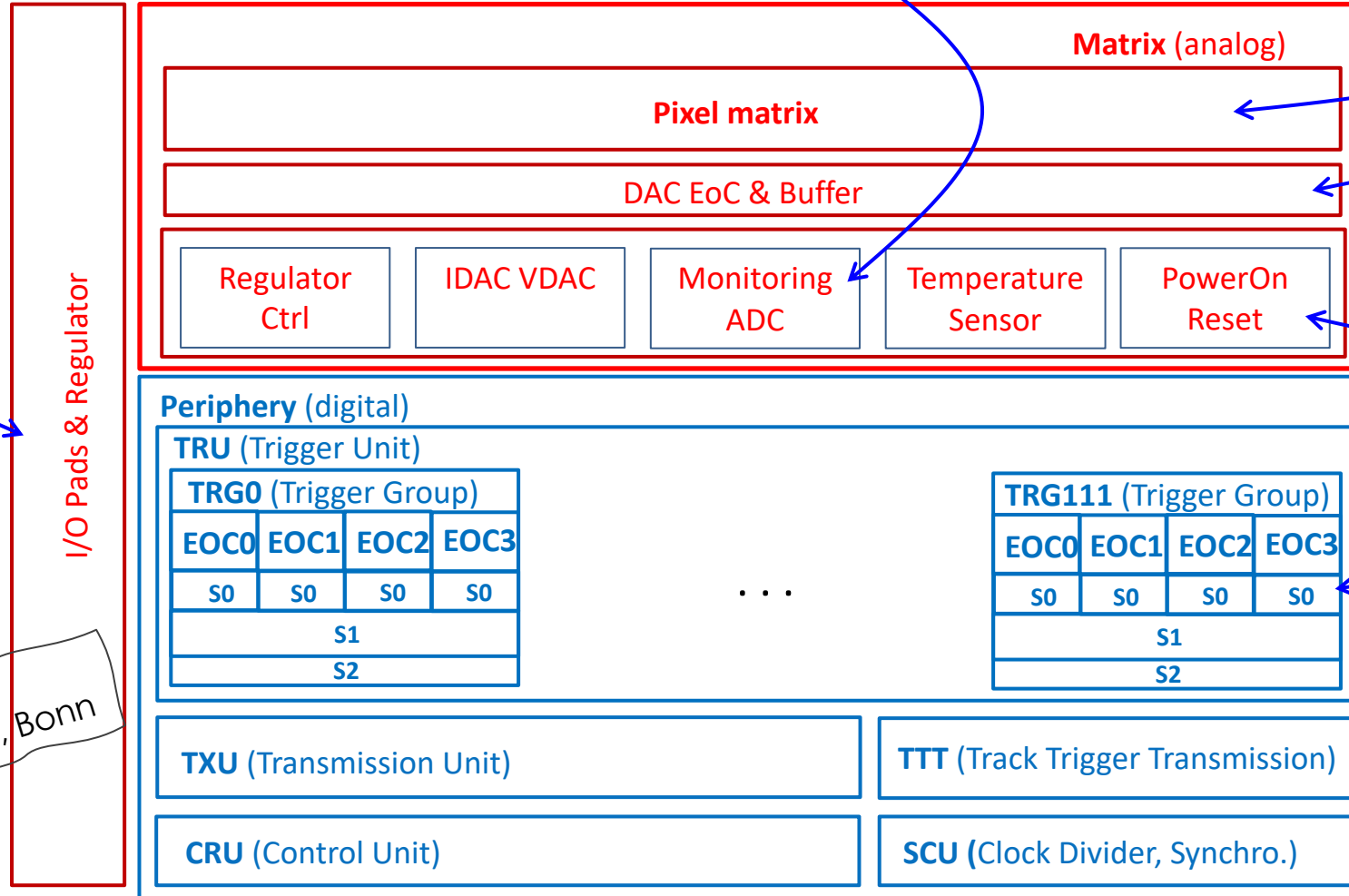
Dortmund  
IPHC

**Floorplan & Integration:**  
IPHC

IPHC Bergamo/Pavia, KEK

HEPHY, CPPM

**TESTS:**  
Same + Pisa + Göttingen  
+QML



## ■ Analogue

- Matrix quasi-complete
  - Operating Std and HV flavours together:  
Hung working on duplicating biases
  - Pulse signal amplitude:  
new buffer from Kishishita to be integrated in layout soon
  - Test columns (1 column per flavour):  
Pavia simulating to introduce new monitoring points
  - General monitoring:  
ADC by Jose moving well (could be in time)  
multiplexing by Hung well
- LDO
  - Real person power issue => being worked on

## ■ Simulation

- Lot of work done by Roua
- Need a dedicated meeting to “wrap-up”

## ■ Digital

- Quasi-complete since some time wrt code
  - Still adaptation needed with
- Big think now is implementing the layout
  - Danwei’s work

## ■ Floorplan

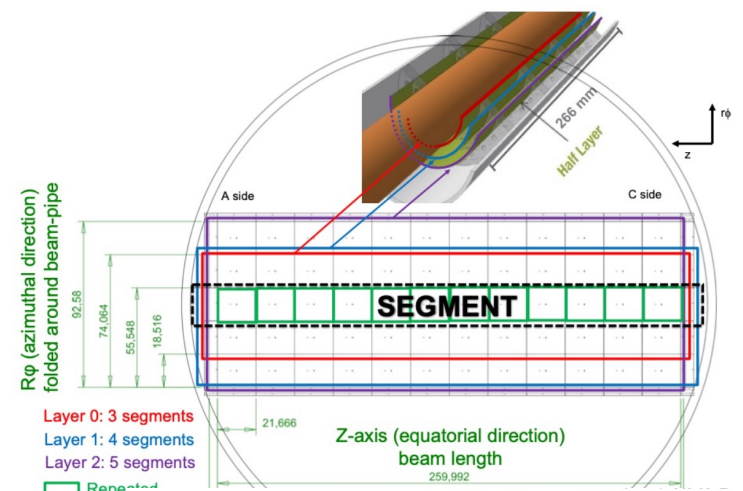
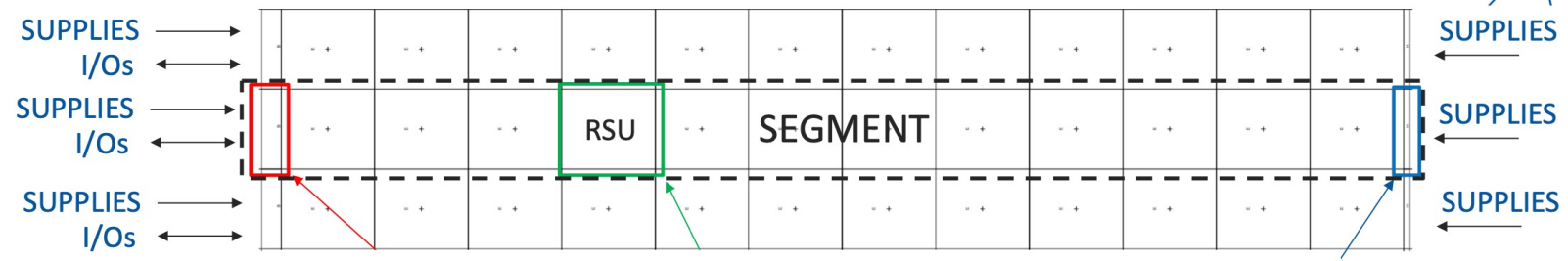
- Continuous effort by Kader
- Will be the last part after the rest is done

## ■ Verification

- UVM (Luca):
  - first step on registers achieved
  - Top-functionality on-going
- Cocotb:
  - Max has initial tests
  - Possibly need manpower to extend them

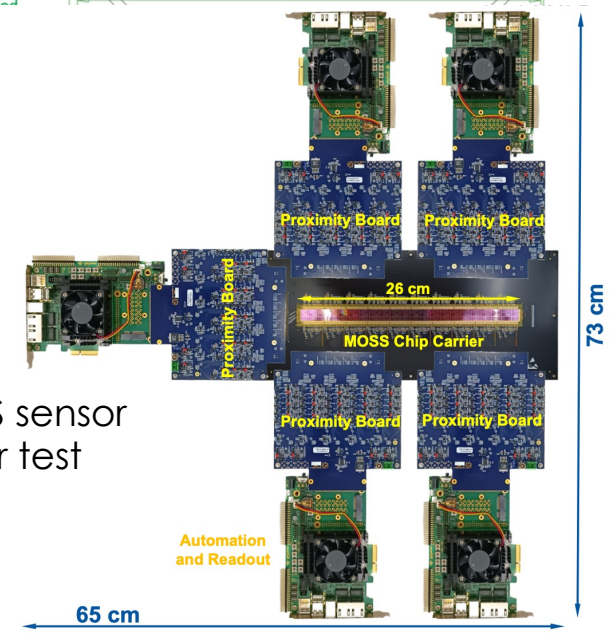
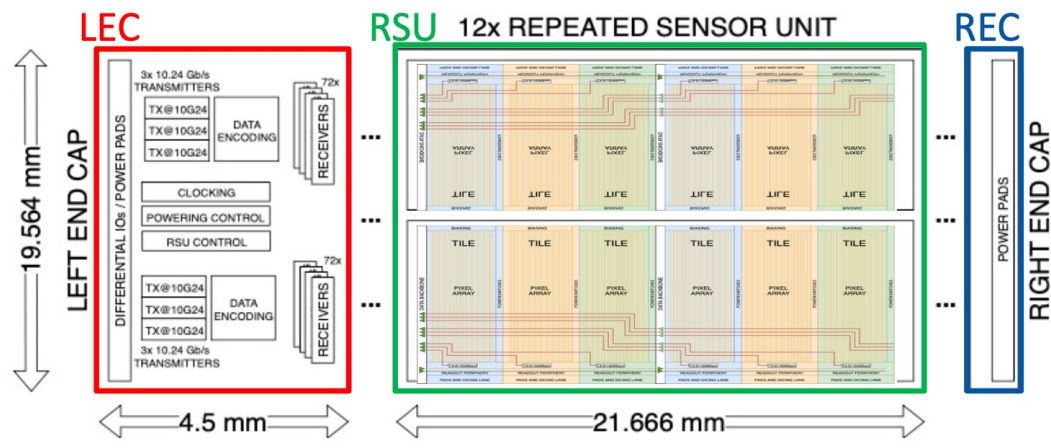
## ■ MOSAIX sensor

- Dedicated sensor for ITS3
- Based on MOSS (TPSCo 65 nm)
  - developed with MLR1-ER1
- Collaboration lead by CERN-ALICE
  - C4Pi have the responsibility of pixel array integration



Layer 0: 3 segments  
 Layer 1: 4 segments  
 Layer 2: 5 segments

□ Repeated Sensi (RSU)



MOSS sensor under test

# DRDs prospect for sensors in TPSCo 65 nm

- Already a lot known from MLR1+ER1 runs

- Techno performance with CE-65, APTS, DPTS
- Still completed this year (including tests at AR-TestBeam)
- Yield of stitched sensor MOSS

- Additional prototypes in ER2

- APTS with pitch up to 50  $\mu\text{m}$
- SPARC with asynchronous read-out
- 1<sup>st</sup> version of MOSAIX

	ALICE ITS3	ALICE3 vertex	FCCee vertex	ALICE3 OT-ML	LHCb UT	FCCee tracker
<b>Data taking in</b>	2029	2035	>2040	2035	2035	>2040
<b>Spatial res. (<math>\mu\text{m}</math>)</b>	~5	2.5	3	10	pitch O(30)	10
<b>Mat. budget (%X0)</b>	0.05	0.1	0.15	1	0.3?	<1?
<b>Hit rate (MHz/cm<sup>2</sup>)</b>	~10	100	50	0.06-1.7	160	<10
<b>Time figure (ns)</b>	1000	100 (RMS)	1000	100 (RMS)	O(1)	10 <sup>2</sup> -10 <sup>3</sup>
<b>Power (mW/cm<sup>2</sup>)</b>	20	70	20	20	100-300	50
<b>Rad.hard. (kGy) (n<sub>eq</sub>/cm<sup>2</sup>)</b>	3 3x10 <sup>12</sup>	3000 1x10 <sup>16</sup>	20 5x10 <sup>11</sup>	2 - 50 ~6x10 <sup>12</sup> - 2x10 <sup>14</sup>	2400 3x10 <sup>15</sup>	20 5x10 <sup>11</sup>

## ■ Fine pitch

- Main goal = position resol.  $\lesssim 3 \mu\text{m}$
- Other features:
  - Timing merit 1 to few 100 ns
  - Hit rate range 50-100 MHz/cm<sup>2</sup>
  - Power < 70 mW/cm<sup>2</sup>
  - NIEL tolerance  $10^{14} n_{\text{eq}}/\text{cm}^2$  (?)
  - Size not critical (reticule OK)
- Realisation:
  - Pitch 15-20  $\mu\text{m}$
  - Binary output
  - Read-out architecture to be decided
- Projects interested:
  - Future e+e- colliders (>> 2040)
  - ALICE-3 IRIS layers inside beam pipe (2033)
  - test-beam reference plane (2028)

## ■ Tracking over large area

- Main goal = power dissipation  $\sim 20 \text{ mW}/\text{cm}^2$
- Other features:
  - Timing merit 1 ns
  - Position resol. 10  $\mu\text{m}$
  - Hit rate & Radition tolerance => large range
- Realisation:
  - Pitch 25 to 30  $\mu\text{m}$ 
    - 50  $\mu\text{m}$  made from merging 4x 25  $\mu\text{m}$  pixels
  - Digitised output (3-6? Bits) like ToT or very small ADC
  - Optional: time resolution 10-100 ps with TDC outside matrix
  - Asynchronous read-out architecture
    - Should provide 25 ns tagging
- Project interested:
  - Future e+e- collider (>>2040)
  - ALICE-3 tracker (2033)
  - LHCb upstream tracker (2033)
  - Fixed target experiments
  - Calorimeters

# Focus on test & setup activities

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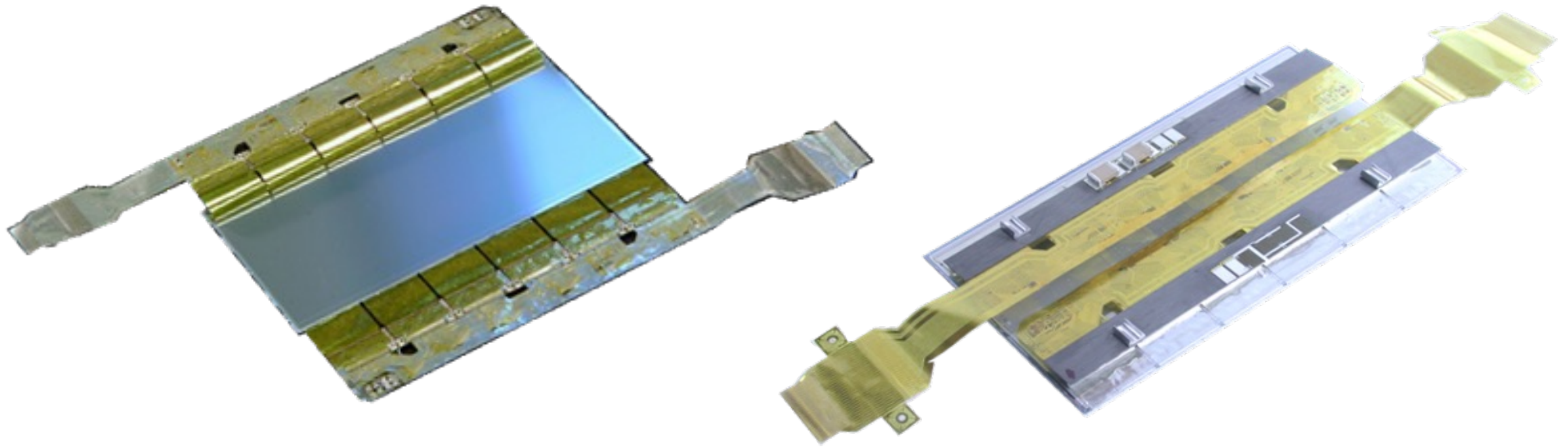


# Focus on integration activities

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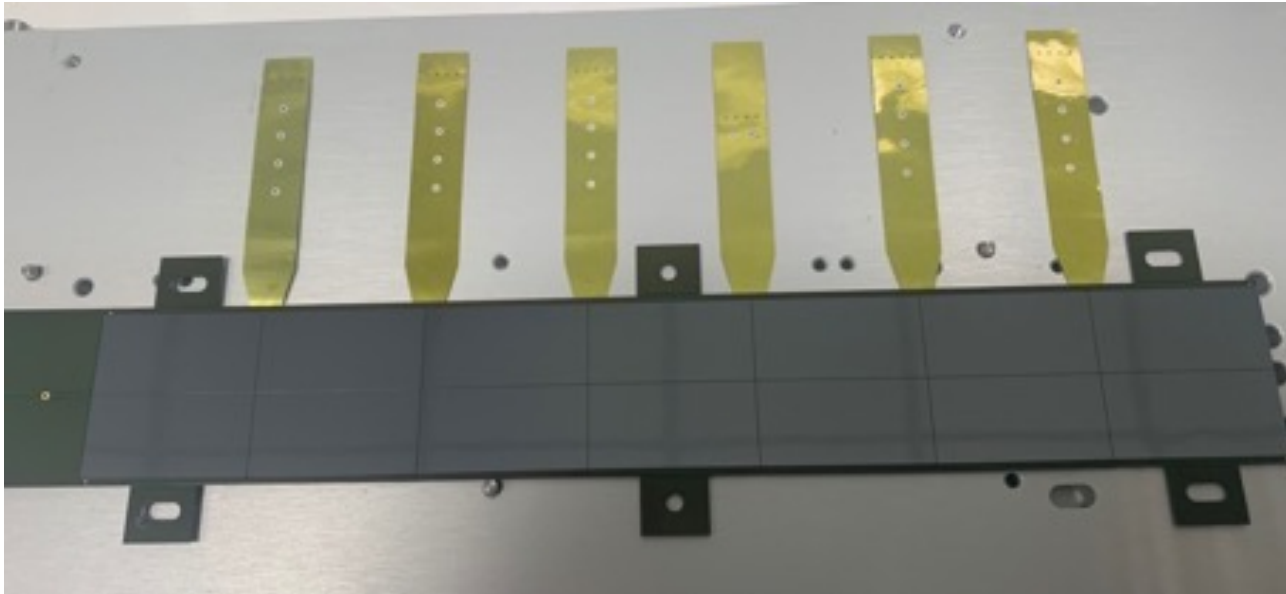


# ALICE ITS1 production: 200 modules in 2005

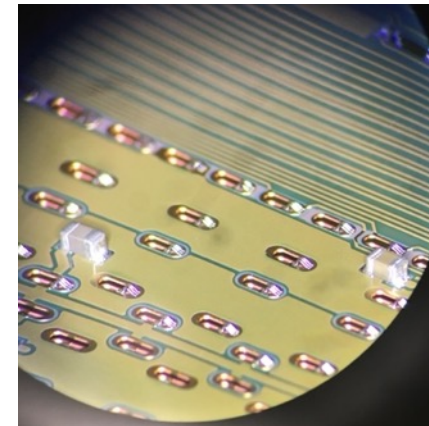


Double Sided Silicon Strip Detector (DSSD) 6x128 strips / side  
=> TAB bonding used to wrap ASIC ontop of sensors

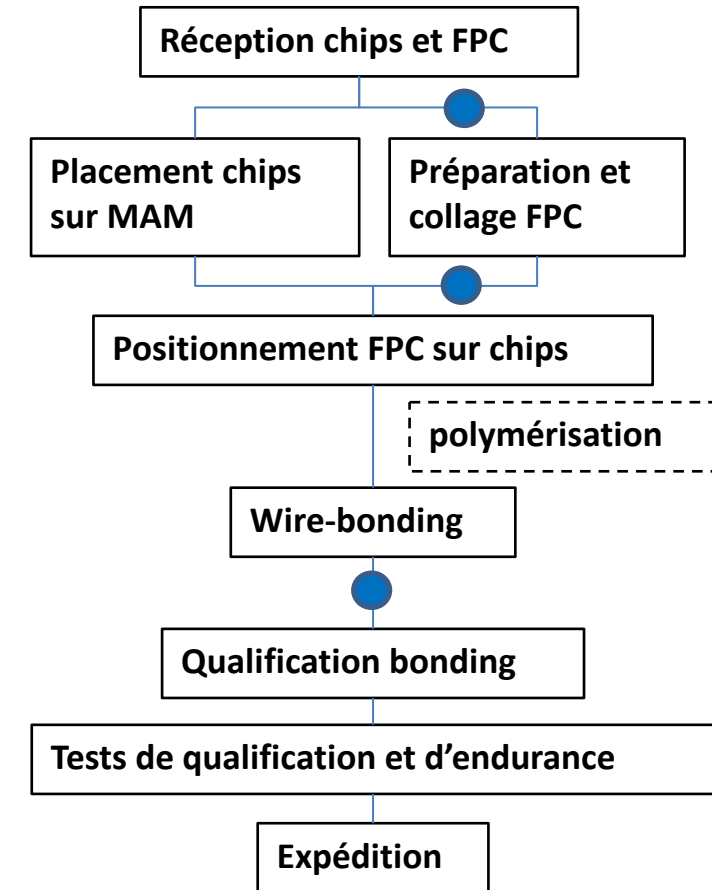
# ALICE ITS2 production: 500 modules in 2017-19



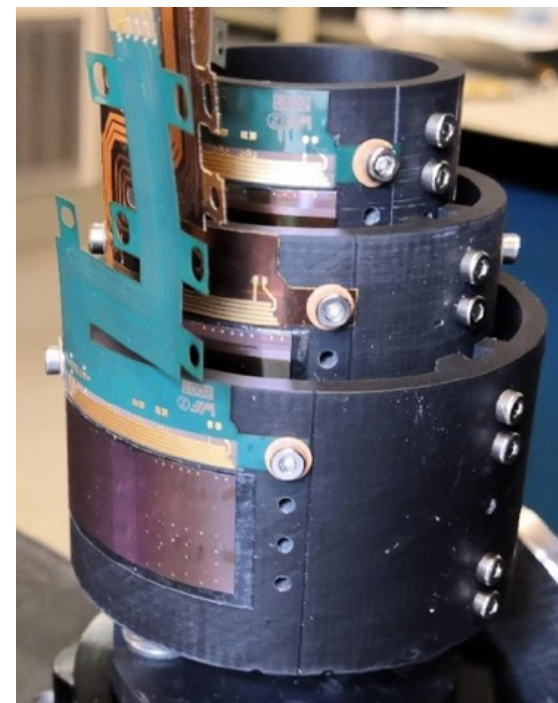
2 rows of 7 ALPIDE sensors (15x30mm, ép. 100um)



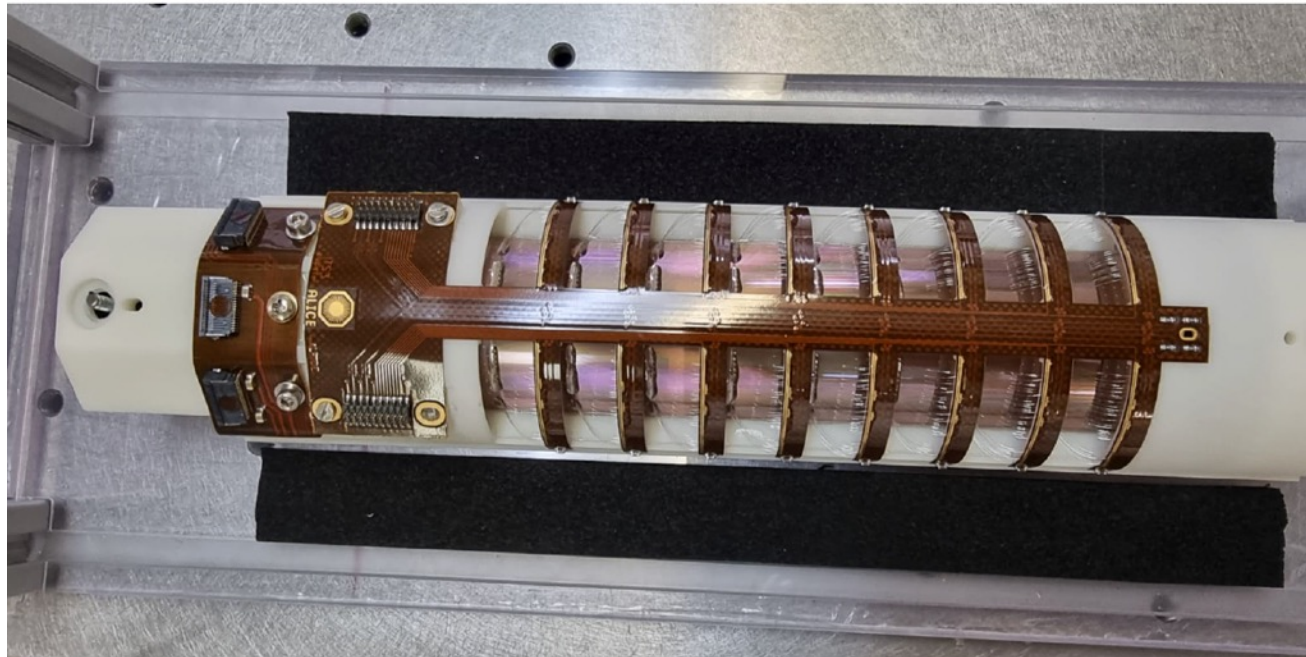
3 bonds / pads



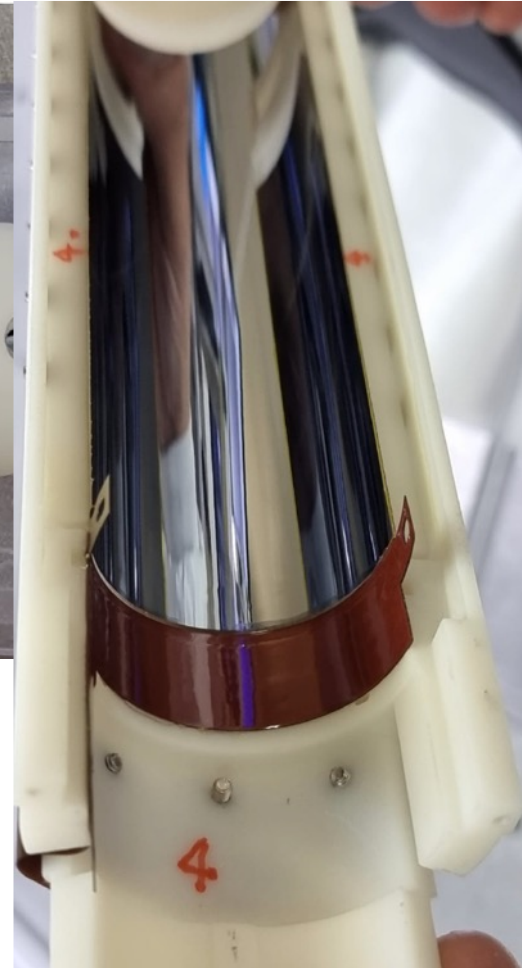
● Contrôle qualité



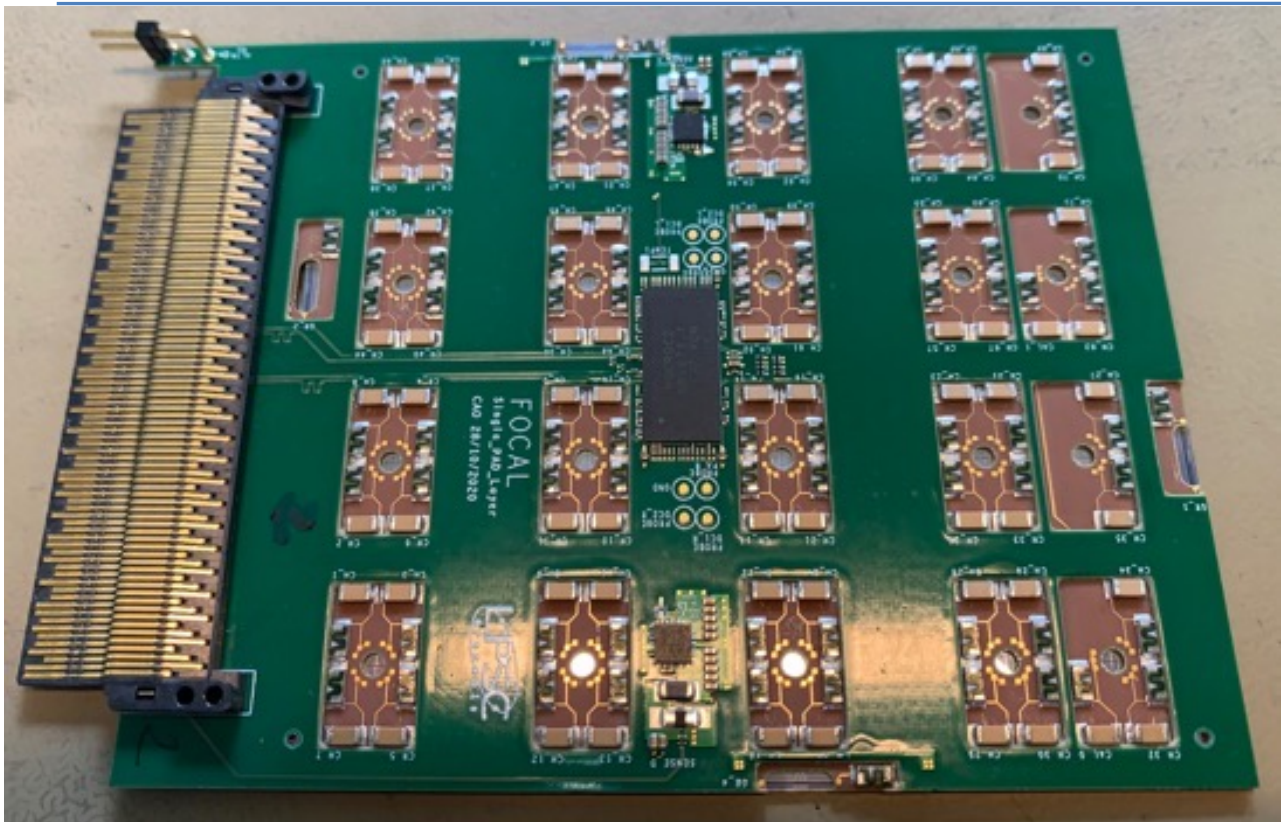
1<sup>er</sup> bended mini-tracker  
Radii: 18, 24, 30mm



2x9 capteurs de 15x30mm, épaisseurs 50um  
montés sur exosquelette  
Rayon de courbure de 18mm.  
En cours : ép. 30um



# Illustration of specific bonding service

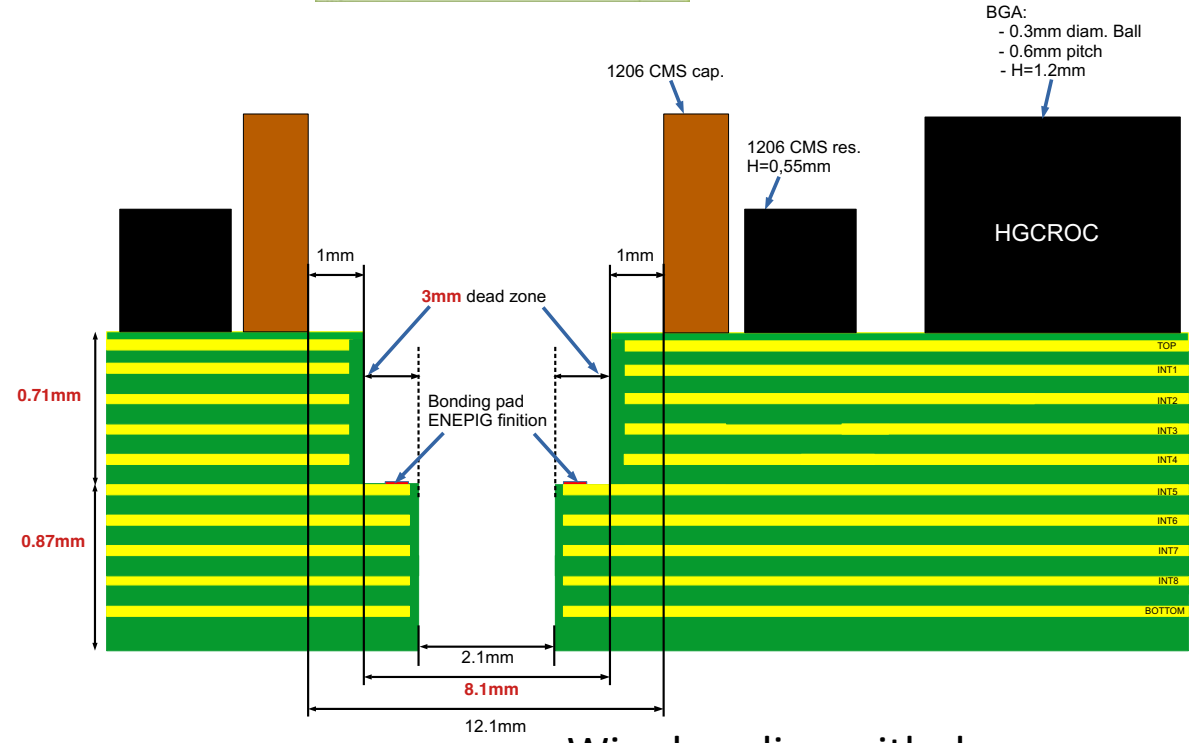


## LPSC – ALICE FoCAL

Détecteur Silicium : 9x8 pixels de 1cm x 1cm  
 Démonstrateur : 18 cartes  
 Final ? : 396 PCB de 45 x 8 (5 capteurs)  
 (soit 1980 capteur silicium).

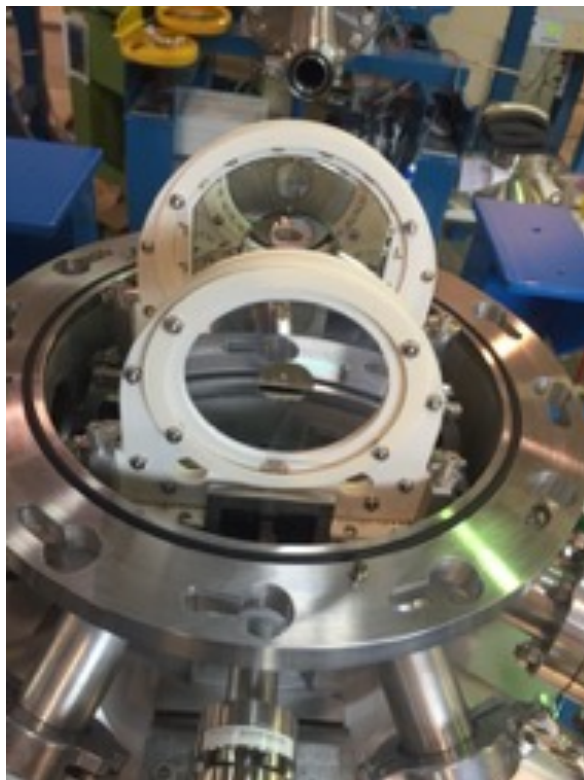


- Strasbourg : EOST, ICS, I3
- Bordeaux : IMS
- Clermont- Fd : LPC
- Grenoble : LPSC
- Lyon : IPNL
- Marseille : LAM
- Nantes : SUBATECH/IMT
- Paris : OMEGA, IPNO



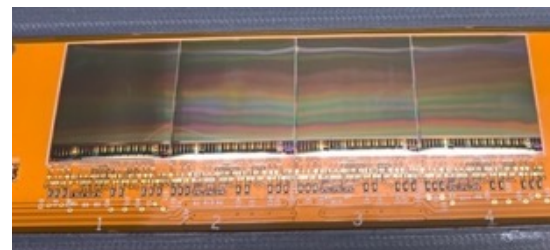
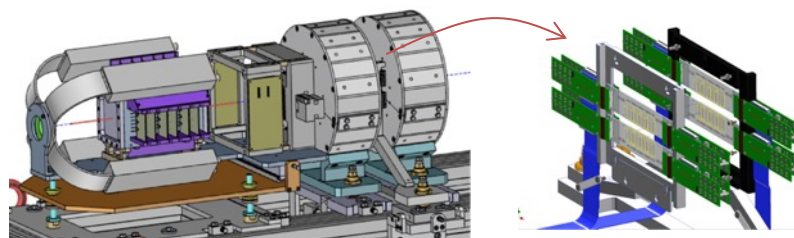
Wire bonding with deep-access

## STELLA : 2015 (DSSD for nuclear physics)



Collab. : GANIL, IJCLab, Univ. York (UK),  
Univ. Surrey (UK), Obs. Genève

## FOOT (INFN): 2021-23 Measurement of nuclear cross-sections of interest for Carbon-therapy



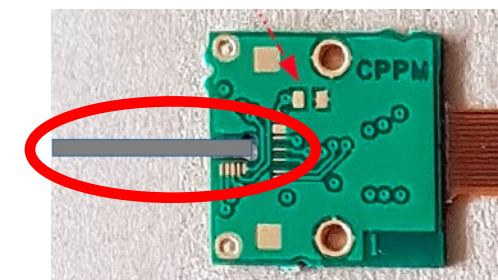
Module de détection  
Capteur MIMOSA 28 20 x 20 mm  
Epaisseur 50um



Cross-section module de détection

## MAPSSIC: 2023 Implantable $\beta$ -probe for neuro-imaging

IN2P3 : CPPM, IJCLAB ; INSB : Lyon Neuroscience, NeuroPSI



2 capteurs CMOS (12x0,65mm, ép. 200um) collés dos à dos.

