

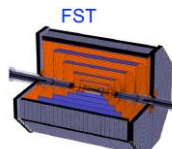
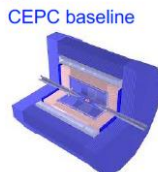
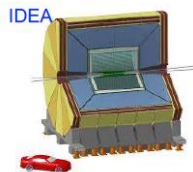
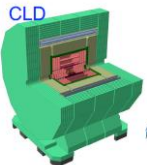
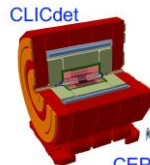
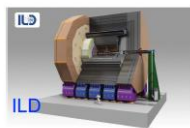
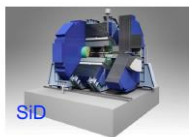


Introduction: a reminder of the possible tracking concepts and technologies

Tracking vertexing requirements
Technologies

Tracking/vertexing detectors in future e⁺e⁻ colliders

Collider	ILC		CLIC	FCCee			CEPC	
Bunch separation (ns)	330/550		0.5	20/990/3000			25/680	
Power Pulsing	yes		yes	no			no	
beamstrahlung	high		high	low			low	
Detector concept	SiD	ILD	CLICdet	CLD	IDEA	Lar	Baseline	IDEA
B Field (T)	5	3.5	4	2	2	2	3	2
Vertex	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel	Si-Pixel
Vertex Rmin (mm)	16	16	31	12	12	12	16	16
Tracker	Si-strips	TPC	Si-Pixel	Si-Pixel (+RICH ?)	DC/Si-strips	DC/Si-strips or Si-Pixels	TPC or Strips	DC/Si-strips
Tracker Rmax (m)	1.25	1.8	1.5	2.2	2.0	2.0	1.8	2.1
Disks layers	4 + 4	2 + 5	6 + 7	3 + 7	3 (150 mrad)		2+6	

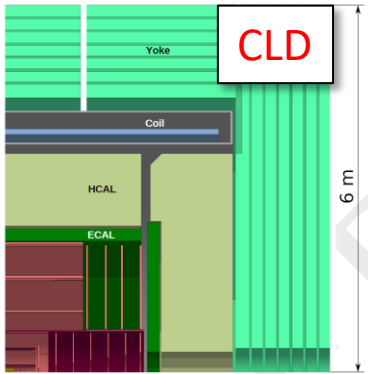


(From D. Dannheim)

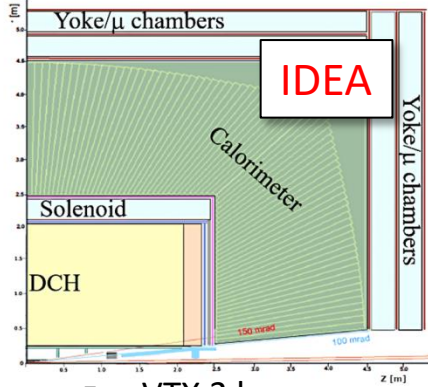
Large similarities between the concepts
but also significant differences

Detector concept & tracking

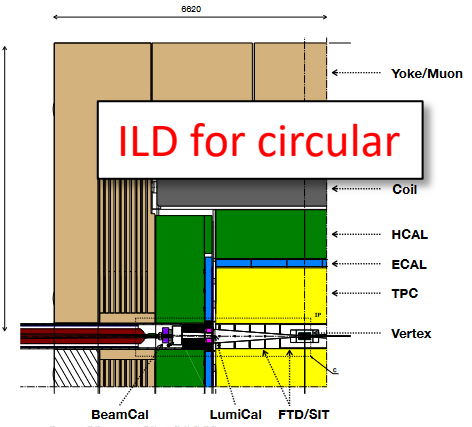
ILD & CLD concepts (Poeschl)



IDEA concept (Giacomelli)



ALLEGRO concept (Aleksa)



- Inspired from CLICdet
- « Full Silicon »
- Possibly + RICH
- Calo. inside coil

- VTX 3 layers

- Calo. outside coil
- Drift Chamber
- Si wrapper

IDEA drift chamber (Filippis)

- VTX: 3 double layers
- Calo. inside coil
- TPC? FCC & TPC (D. Jeans)

Wireless (Rarbi)

- VTX \Rightarrow Silicon pixels (CMOS-MAPS)
- Added dedicated timing layer ?
- Shared concerns: MDI, beam background, integration, cooling, etc.

Cooling (Winter)

Vertexing

- Tagging capabilities (b, c, τ , s)
- Impact parameter resolution
- Standalone tracking capabilities
- Low pT tracking
- Vertex charge determination
- Hit/track separation
- Displaced vertex (LLP, etc.)
- Acceptance

65nm CMOS (Besson)

Tracking

- Tracking efficiency/fake tracks
- Momentum resolution
- Charge determination
- Robustness/redundancy
- Association with calo. clusters

MPGD (Jeanneau)

Timing CMOS (Schwemling)

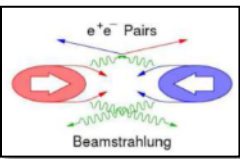
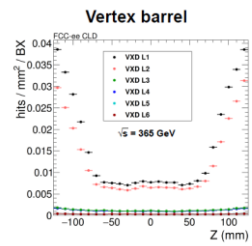
Picosec / micromegas (Kallitsopoulou)

Particle ID

- dEdx
- Timing measurement
- RICH

Defining the figure of merits

Physics program priorities
 \sqrt{s}



Vertex detector requirements (ILC/FCCee)

Physics $O(\text{Hz}/\text{cm}^2)$
 Beam background $O(10\text{-}50 \text{ MHz}/\text{cm}^2)$

- Physics
- ⇒ Flavour tagging
 - ⇒ Low p_T tracks
 - ⇒ Vertex/Jet charge determination
 - ⇒ Track seeding
-

CLD and IDEA Vertex Detectors designs (superimposed)

MAPS with $\sigma_{\text{hit}} \approx 3 \mu\text{m}$ and $X/X_0 \approx 0.3\%$ / layer of Si

- CLD concept: double layers in Barrel/Endcap configuration
- IDEA concept: single closer layers in Long Barrel configuration

- Vertex reconstruction
- ⇒ granularity
 - ⇒ Pitch $\sim 17\text{-}20 \mu\text{m}$
 - ⇒ $(\sigma_{\text{sp}} \sim 3\text{-}4 \mu\text{m})$

- Material Budget
- ⇒ $\sim 0.15\%$ X_0 / layer
 - ⇒ $< 1\%$ X_0 for the whole VTX
 - + $\sim 0.4\text{-}0.6\%$ X_0 for the beam pipe

$$\Delta d_0|_{\text{res.}} \approx \frac{3\sigma_{r\phi}}{\sqrt{N+5}} \sqrt{1 + \frac{8r_0}{L_0} + \frac{28r_0^2}{L_0^2} + \frac{40r_0^3}{L_0^3} + \frac{20r_0^4}{L_0^4}}$$

$$\Delta d_0|_{\text{m.s.}} \approx \frac{0.0136 \text{ GeV}/c}{\beta p_T} r_0 \sqrt{\frac{d}{X_0 \sin \theta}} \sqrt{1 + \frac{1}{2} \left(\frac{r_0}{L_0}\right) + \frac{N}{4} \left(\frac{r_0}{L_0}\right)^2}$$

(Figure: D. Contardo)

Low material detectors & supports structures

$$\sigma_{d_0} = a \oplus \frac{b}{p \sin^{3/2} \theta}$$

$a \simeq 5 \mu\text{m}$ $b \sim 10 - 15 \mu\text{m} \cdot \text{GeV}$

Beam background

Radiation hardness
 $O(100\text{kRad}/\text{yr})$ & $O(10^{11})n_{\text{eq}}/\text{yr}$

Rad.Tol. devices

Time resolution
 $O(100\text{ns}\text{-}1 \mu\text{s})$

$O(10\text{ns})@CLIC$

Power consumption
 $\sim < 50\text{mW}/\text{cm}^2$

Fast read-out & low Power Architectures ($\sim 20\text{-}50 \text{ mW}/\text{cm}^2$)

Cooling
 Stiffness / Alignment

No Power pulsing @FCCee

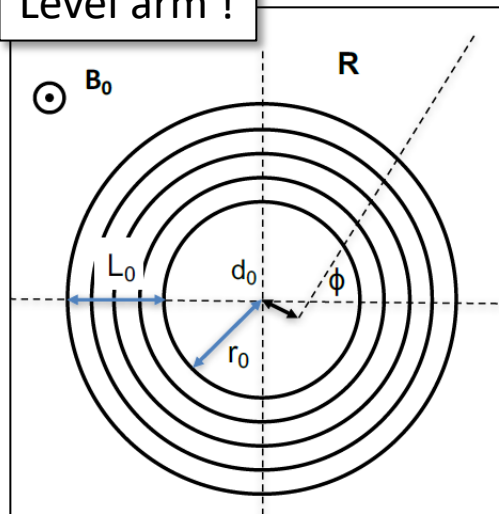
- Design: 5 single layers or 3 double layers ? Inner and outer radius ? Etc.
- R&D: ⇒ Keep excellent spatial resolution, low material budget, moderate Power consumption and push towards better time resolution (BX)

Tracker requirements

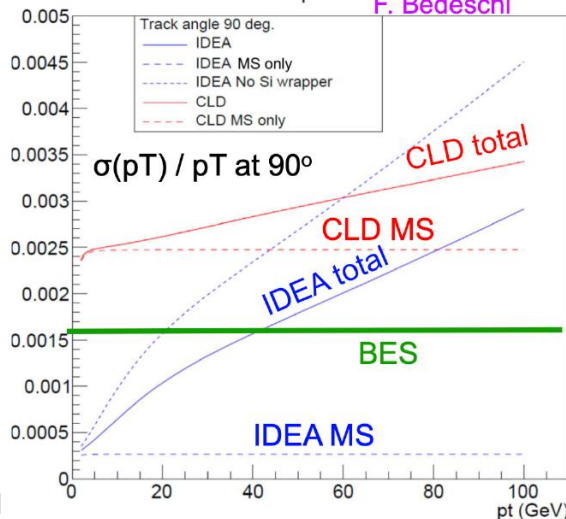
Expected performances

$$\frac{\sigma_{p_T}}{p_T^2} \sim 2 \times 10^{-5} \text{ GeV}^{-1}$$

Level arm !



F. Bedeschi



- Physics
- ⇒ Momentum resolution
 - ⇒ Tracking efficiency
 - ⇒ Track separation, low fake tracks
 - ⇒ Etc.

- Material budget vs intrinsic resolution
 - ✓ Typically $\sigma_{sp} \sim 5\text{-}10 \mu\text{m}/\text{layer}$; material $\sim 1\text{-}2\% X_0/\text{layer}$; Power $\sim < 100 \text{ mW}/\text{cm}^2$
 - ✓ Low momentum vs high momentum \leftarrow physics input
- 2 main options:
 - ✓ All silicon (CLD, CLICdet, SiD)
 - Few high resolution layers
 - Possibly timing capabilities
 - ✓ Silicon + Gaseous detector
 - TPC (ILD) / Drift Chamber (IDEA) / RICH (CLD ?)
 - dEdx/dNdx capabilities,
 - More hits, overall less materials
 - TPC: Ion back flow issue for circular colliders
- PID Strategy to be included (RICH, timing, dEdx, etc.)

Drasal, Riegler, <https://doi.org/10.1016/j.nima.2018.08.078>

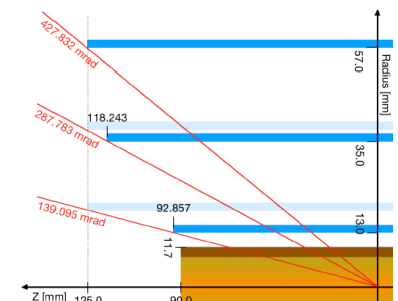
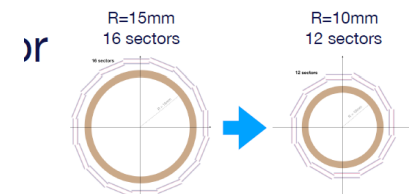
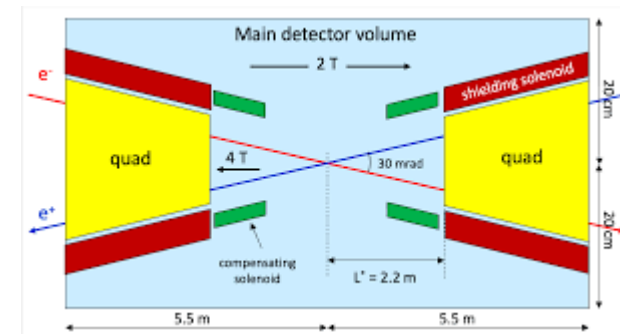
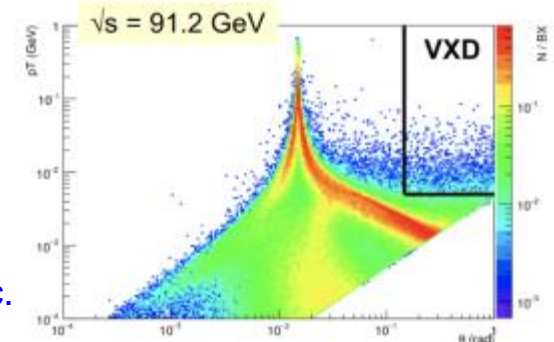
$$d_{tot}/X_0 = (N + 1)d/X_0. \quad d = \text{layer thickness, } N = \# \text{ layers}$$

$$\left. \frac{\Delta p_T}{p_T} \right|_{m.s.} \approx \frac{0.0136 \text{ GeV}/c}{0.3\beta B_0 L_0} \sqrt{\frac{d_{tot}}{X_0 \sin\theta}} \quad \left. \frac{\Delta p_T}{p_T} \right|_{res.} \approx \frac{12 \sigma_{r\phi} p_T}{0.3 B_0 L_0^2} \sqrt{\frac{5}{N+5}}$$

m.s. term dominates for $p_T \sim < O(100) \text{ GeV}/c$

Vertex/tracking detector comments

- Particle ID has to be included in the tracker concept
 - ✓ dEdx and/or dNdx and/or fast timing
 - ✓ RICH \Rightarrow reduced outer radius
- Inner and outer radius are key factors
- Forward acceptance (e.g. asymmetry measurements)
 - ✓ Limited by MDI constraints, beam pipe, luminosity measurements, etc.
 - 30 mrad crossing angle (FCCee) \Rightarrow \sim 150 mrad acceptance
- B-field
 - ✓ Limited to 2 T in circular machine (@ Z-pole)
 - ✓ Calo inside coil ?
- Beam time structure
 - ✓ Power pulsing only for linears
- Beam related Background
 - ✓ Beamstrahlung (incoherent e^+e^- pairs)
 - Occupancy driver for linears
 - Less severe for circular (\Rightarrow Rmin reduction \sim 10mm)
 - ✓ Synchrotron radiation (mainly circulars)
 - Possible shielding (increase beampipe material budget)
- VTX Geometry
 - ✓ Probably 5-6 layers VTX (R < 60 mm)
 - Robustness (standalone tracking)
 - low momentum tracking
 - Track seeding @ different radii
 - e.g. FIPs, highly ionizing particles, LLPs, etc.



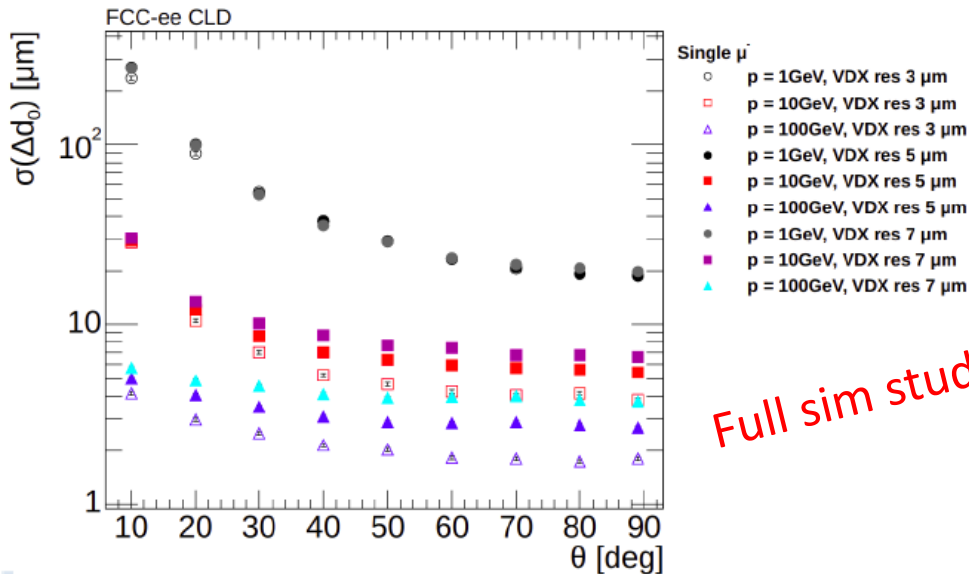
VTX/Tracking detector is highly connected to the MDI and the whole detector concept

Beam pipe discussion

- V05 = new beam pipe radius & material budget
 - ✓ 5 μm Au + 2 x 350 μm layers of BeAl + liquid parafin $\sim 0.6\%$ $X_0 \Rightarrow$ mat. Budget +33%
 - ✓ Inner radius: 15 mm \Rightarrow 10mm

D0 resolution – single μ^- – CLD_o1_v04

D0 & pT resolution – single μ^- – CLD_o2_v05 (10k events)



Full sim studies !

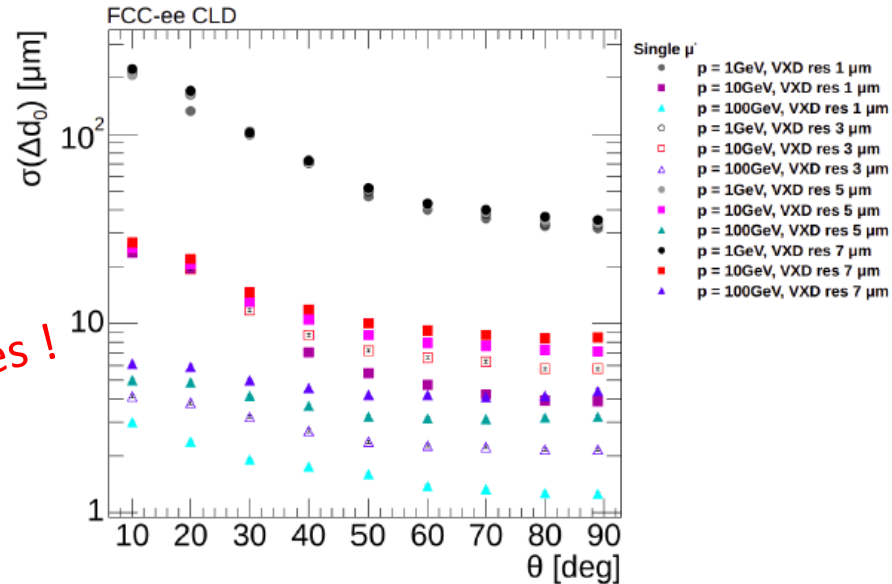


Figure: D0 resolution (10k events)

Figure: D0 resolution

V04 geom; sp. resolution = 3 μm ; p = 10 GeV, $\theta = 90$ deg $\Rightarrow \sigma(\Delta d_0) = 3.8 \pm 0.1 \mu\text{m}$ +42%

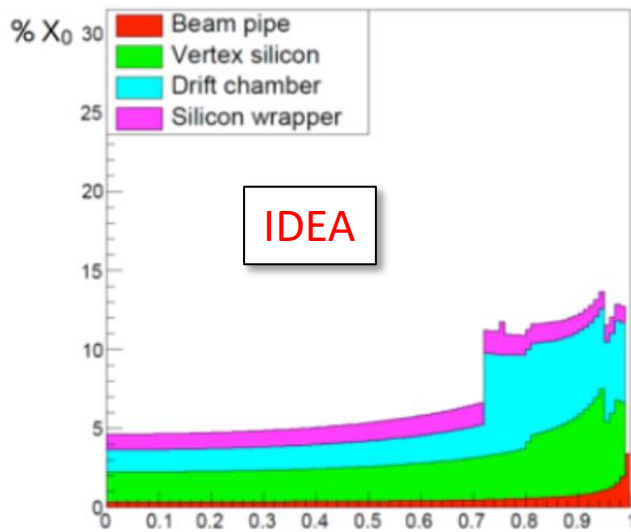
V05 geom; sp. resolution = 3 μm ; p = 10 GeV, $\theta = 90$ deg $\Rightarrow \sigma(\Delta d_0) = 5.7 \pm 0.1 \mu\text{m}$

V04 geom; sp. resolution = 5 μm ; p = 10 GeV, $\theta = 90$ deg $\Rightarrow \sigma(\Delta d_0) = 5.4 \pm 0.1 \mu\text{m}$

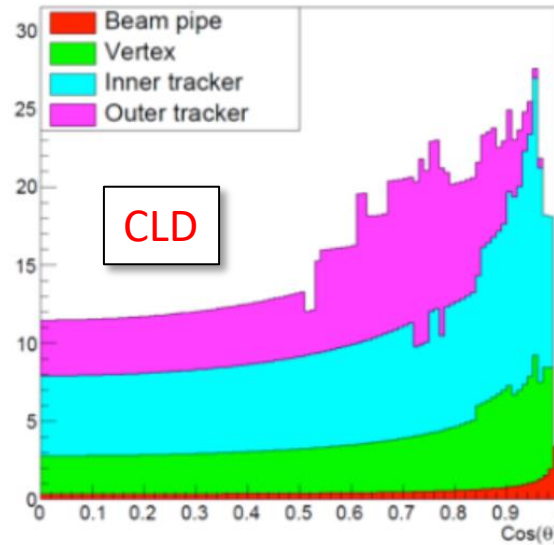
V05 geom; sp. resolution = 5 μm ; p = 10 GeV, $\theta = 90$ deg $\Rightarrow \sigma(\Delta d_0) = 7.1 \pm 0.1 \mu\text{m}$ +24%

Material budget discussion

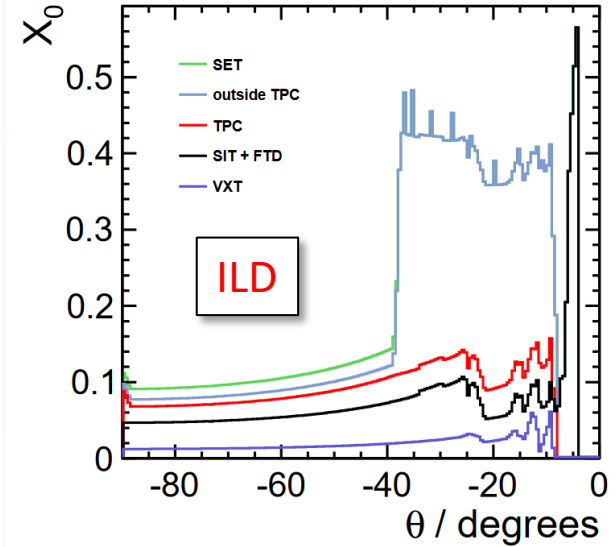
IDEA: Material vs. $\cos(\theta)$



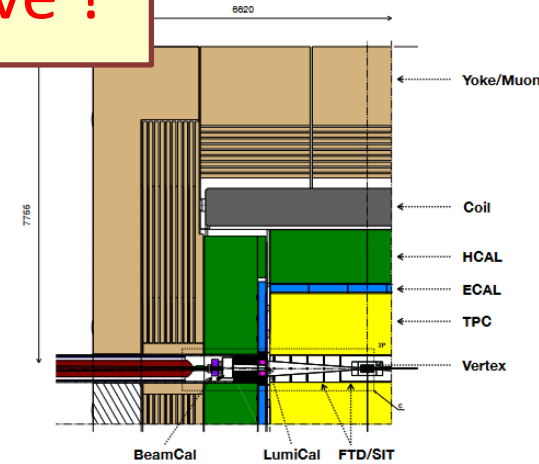
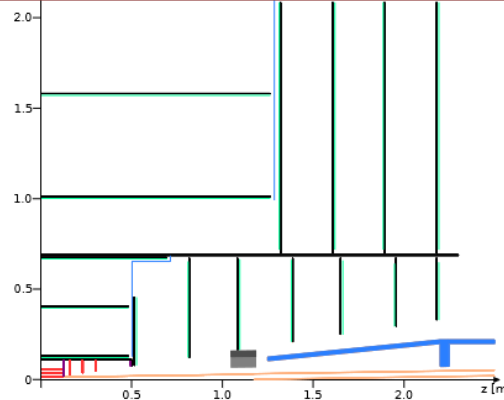
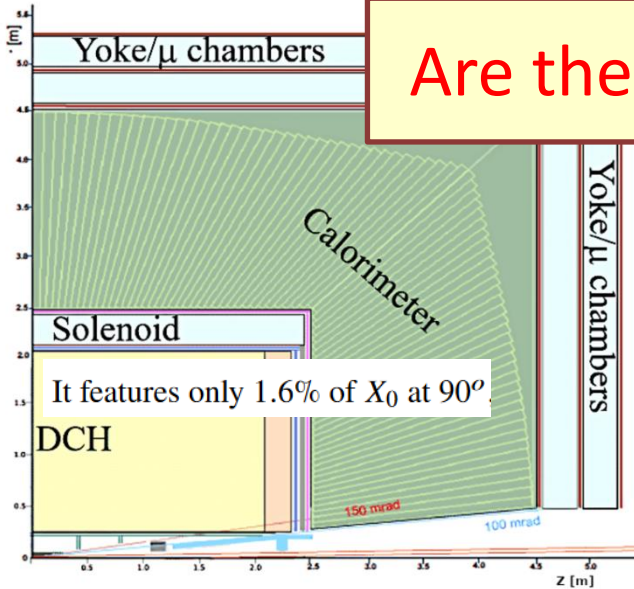
CLD: Material vs. $\cos(\theta)$



ILD material

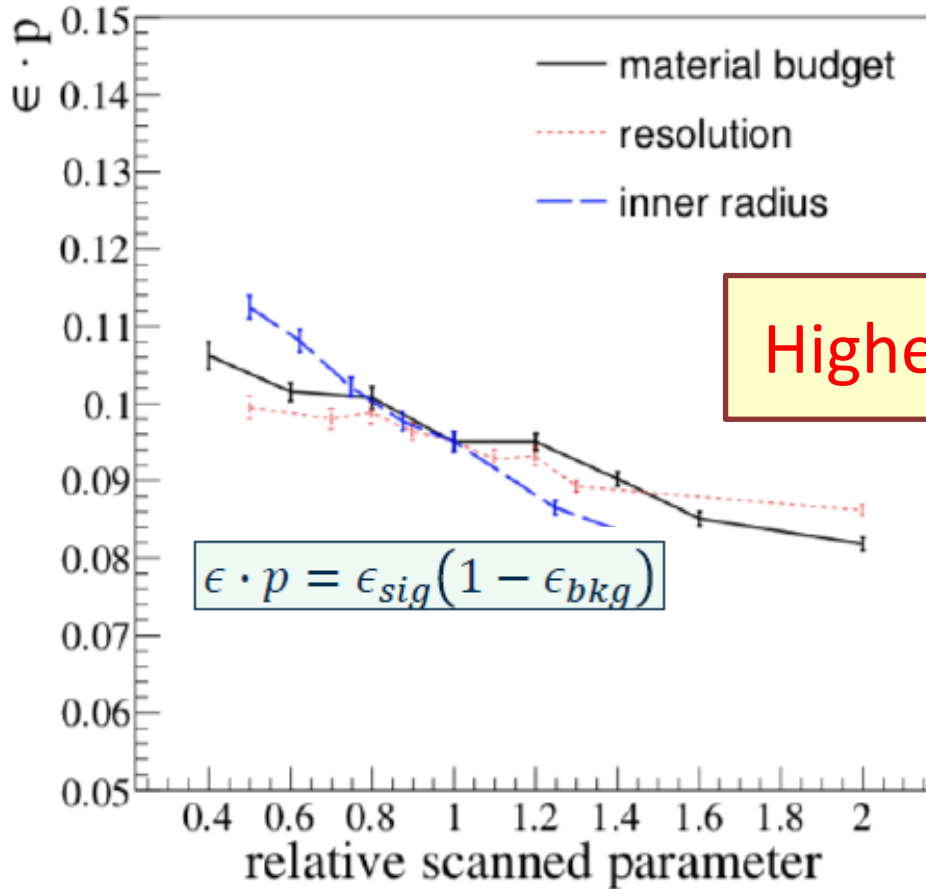


Are these estimates conservative ?



CEPC vertex detector optimization

c-tagging: Detector optimization for CEPC



Higher dependance to mat. budget

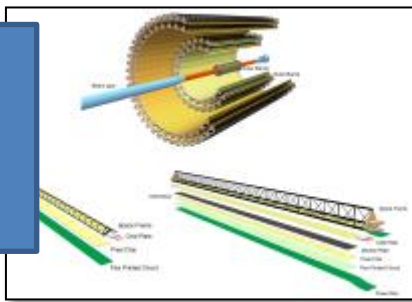
$$\epsilon \cdot p = \epsilon_{sig}(1 - \epsilon_{bkg})$$

M. Ruan, [ECFA WG3: Topical workshop on tracking and vertexing](#)

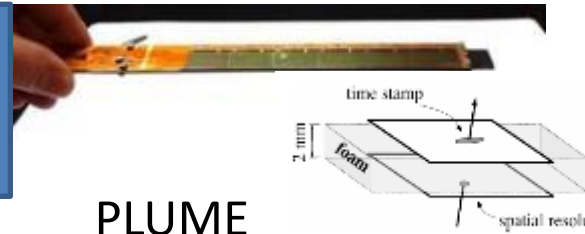
<https://indico.cern.ch/event/1264807/contributions/5344222/attachments/2655752/4599314/ECFA-2.pdf>

Material budget: starting from the layers

Classical single sided layers (e.g. ALICE ITS-2)



Double sided layers

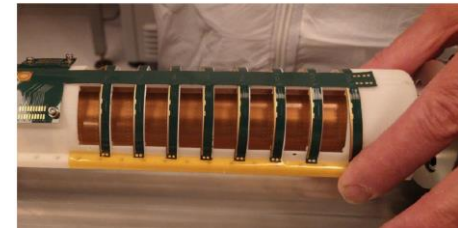


PLUME

(Bristol, DESY, IPHC)
Double sided ladders with minimized material budget
0.35% X_0 reached \Rightarrow $\sim 0.3 X_0$ doable (with air flow cooling)

Pseudo stitching + bent sensors (superALPIDE)

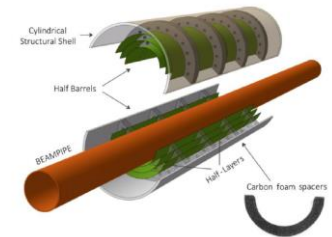
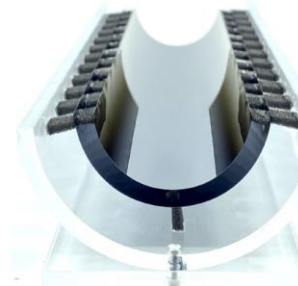
- 1 silicon piece cut from one ALPIDE wafer (9x2 dies, $\sim 1/2$ of layer 0)



Integration !

Stitching + bent sensors ALICE-ITS3

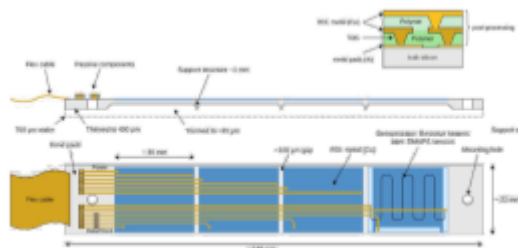
Layers 2+1



Self supported silicon (Belle-2 upgrade)

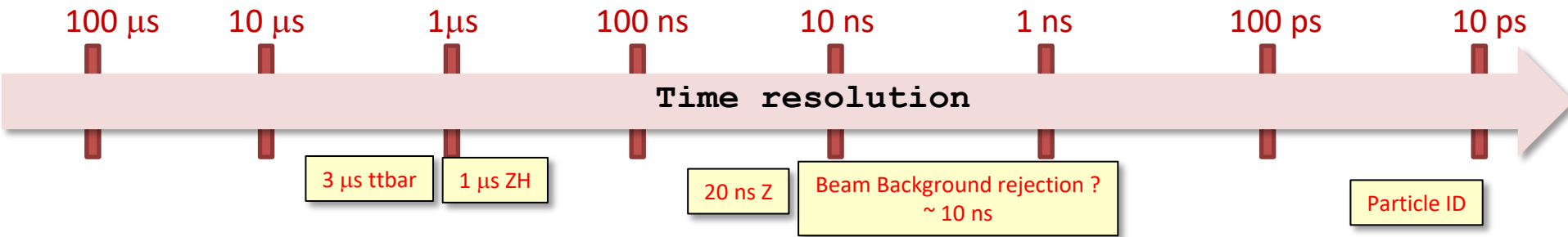


7.1x1.5 cm²
Thickness (edge/center)
430/90 μ m
Planarity $\sim 17 \mu$ m



Inputs for engineering studies

Timing & 4-D tracking



- Time resolution Δt
 - ✓ Bunch separation (3 μs / 1 μs / 20 ns @ FCCee)
 - ✓ Background rejection ? (1-10 ns range)
 - ✓ Particle ID (10-100 ps)
- Usual drawbacks to go faster
 - ✓ Power consumption
 - ✓ Active Cooling & geometrical acceptance due to services
 - ✓ In pixel circuitry \Rightarrow larger pixels (or multipixels)
 - ✓ Fill factor, dead time
 - ✓ PID Restricted to low momentum particles ($\sim < \text{few GeV}/c$)
- Still
 - ✓ Forward region not covered by a central gaseous detector (TPC)
 - ✓ Added value for intermediate radii (e.g. LLPs ?)
- Specialized layers
 - ✓ Doesn't compromise the other requirements (material budget and granularity)
 - Probably not in the most inner layers

Particle ID and time resolution DRD4 & 1/3

TF#1 Gaseous Detectors Anna Colaleo Leszek Ropelowski	TF#2 Liquid Detectors Rosanne Guenette Jocelyn Monroe	TF#3 Solid State Detectors Nicolo' Cartiglia Giulio Pellegrini	TF#4 Photon Detectors & PID Neville Harnwell Peter Krizan	TF#5 Quantum & Emerging Technologies Marcel Demarteau Michael Doser	TF#6 Calorimetry Roberto Ferrari Roman Poeschl
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More details here:

<https://indico.cern.ch/event/1202105/contributions/5402790/attachments/2662086/4612032/FCC-DRD4.pdf>

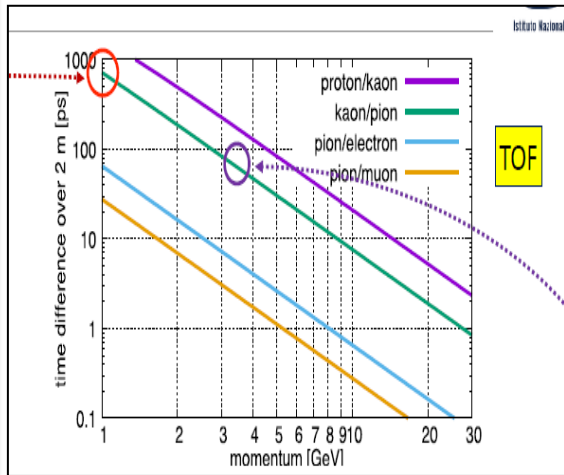
• Goal:

- ✓ K/π , π/e^- separation, etc. \Rightarrow Interest to push beyond 10 ps resolution
- ✓ Even more important for the physics program @ Z peak

Fast timing (<100 ps)
Solid state (pixelated) detector (DRD3)

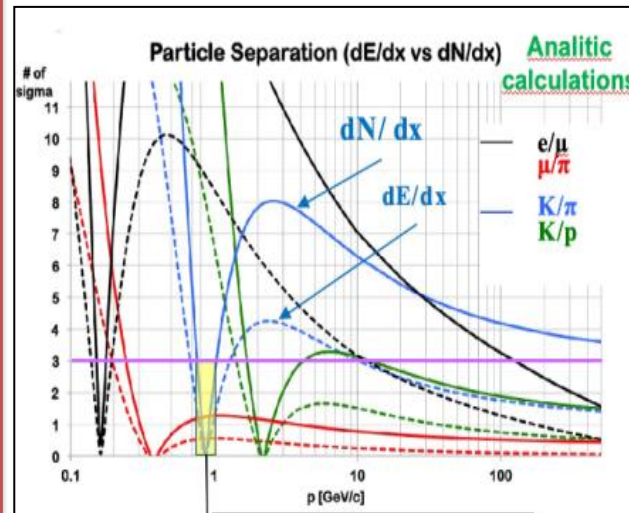
$dE/dx + dN/dx$
Mainly gaseous detector, e.g. TPC, DC, RICH (DRD1)

Time difference (ps)



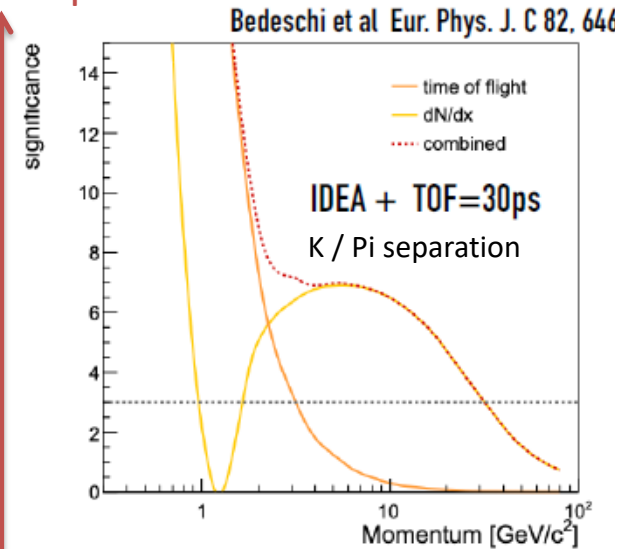
Time of Flight

Separation Power (significance)



$dE/dx - dN/dx$

Separation Power



Combined measurement

Momentum (GeV/c)

Particle ID and time resolution DRD4 & 1/3

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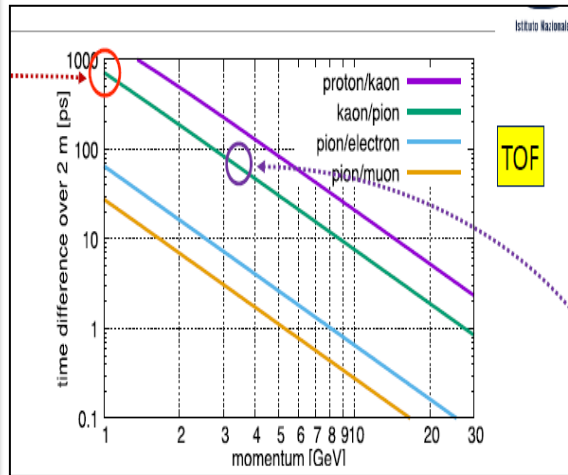
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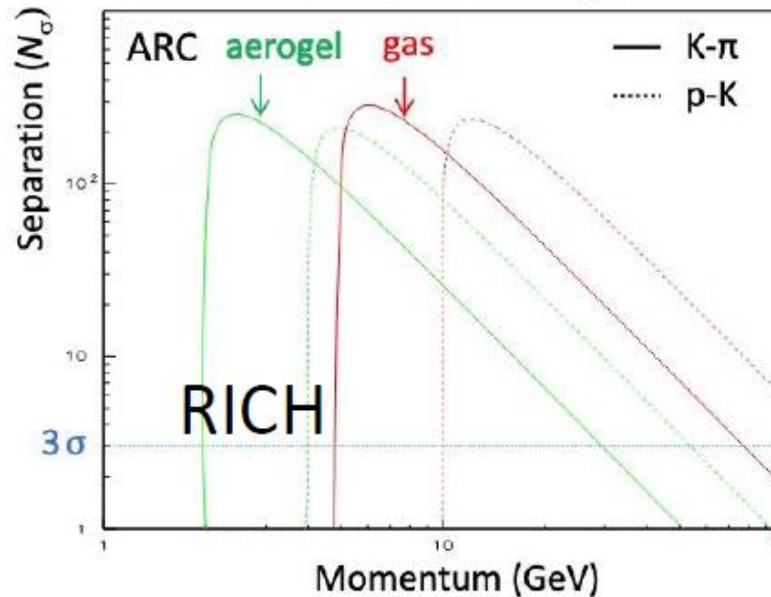
Time difference (ps)



Time of Flight

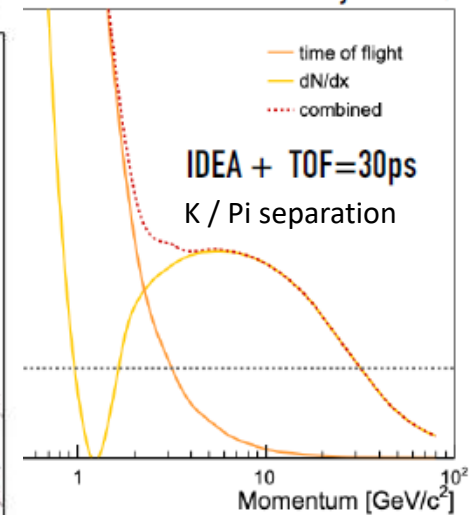
Separation (N_σ)

Preliminary analytic calc., assumes focusing target achieved



Separation Power

Bedeschi et al Eur. Phys. J. C 82, 646



combined measurement

Momentum (GeV/c)

Agenda

CMOS-MAPS, Timing, gaseous detectors (TPC, Drift), wireless, Cooling

1st tracking session

09:00	Introduction: a reminder of the possible tracking concepts and technologies <i>auguste besson</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	09:00 - 09:15
	MCMS TPSco 65nm and electronics <i>auguste besson</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	09:20 - 09:40
	Timing with MCMS + potential application to FCC <i>Philippe schwemling</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	09:45 - 10:00
10:00	picosec-Micromegas <i>Alexandra Kallitsopoulou</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	10:05 - 10:20
	TPC in an FCC environment <i>Daniel Jeans</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	10:25 - 10:40
	IDEA Drift Chamber <i>Nicola De Filippis</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	10:45 - 10:55

Detector concept session

	ALLEGRO concept (presentation and discussion) <i>Martin Aleksa</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	16:30 - 17:00
17:00	ILD' and CLD concept (presentation and discussion) <i>Paolo Giacomelli</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	17:00 - 17:30
	IDEA concept (presentation and discussion) <i>Paolo Giacomelli</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	17:30 - 18:00
18:00	Possible sub-detectors Eol (presentations and discussions) <i>Paolo Giacomelli</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	18:00 - 18:30
	Global discussion (including "next steps in tracking R&D") <i>Paolo Giacomelli</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	18:30 - 19:00
19:00		

Software session

Status of the Key4HEP Ecosystem <i>Ziad El Bitar</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	14:30 - 14:45
Summary of software development in AIDAINNOVA <i>Gérald Grenier</i>	R&D projects / parallel session (if needed)
A preliminary estimation of the fluxes in calorimeters at t... <i>Vincent BOUDRY</i>	
Detailed simulation in LAr Calorimetry <i>Tong LI</i>	
Detailed simulation for Tracking <i>Gaëlle Sadowski</i>	
<i>Piet Mondrian, Bâtiment 25</i>	
	15:00 - 16:00

WADAPT : Wireless Allowing Data and Power Transfer <i>Dr Fairouz MALEK et al.</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	10:30 - 10:45
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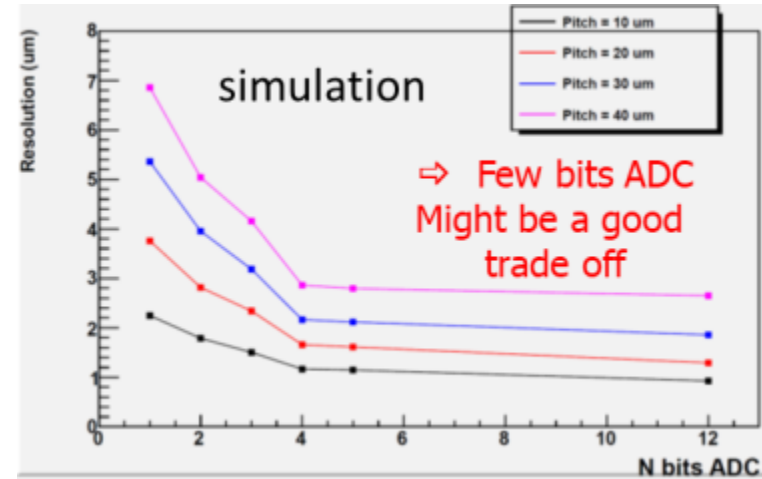
2nd tracking session

MPGD <i>Fabien Jeanneau</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	11:00 - 11:15
A light vertex detector cooling system for the ultimate Super KeKb luminosity <i>Marc WINTER et al.</i> Bâtiment 25, Amphi Grünwald, Institut Pluridisciplinaire Hubert Curien, 23 rue du Loess, 67037 Strasbourg	11:20 - 11:35

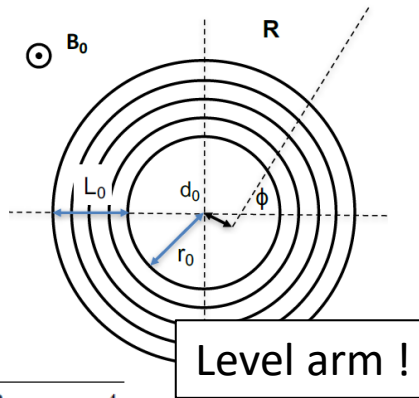
backup

Spatial resolution in Higgs factories

- Typical targets:
 - ✓ $\sigma_{sp} \sim 3 \mu\text{m}$ for the vertex layers
 - ✓ $\sigma_{sp} \sim 5\text{-}10 \mu\text{m}$ for the outer tracker layers
- Resolution in each layer depends on
 - ✓ Pitch
 - In conflict with the functionalities inside the pixel
 - Favored by small feature size technology
 - ✓ Charge deposition
 - Sensitive layer thickness
 - ✓ Charge sharing (SNR vs resolution)
 - Depletion:
 - Staggered pixels
 - ✓ Charge encoding
 - Binary output / ADC / Tot / etc.



$$\sigma_{d0}^2 = a^2 + \left(\frac{b}{p \cdot \sin^{3/2}\theta} \right)^2$$

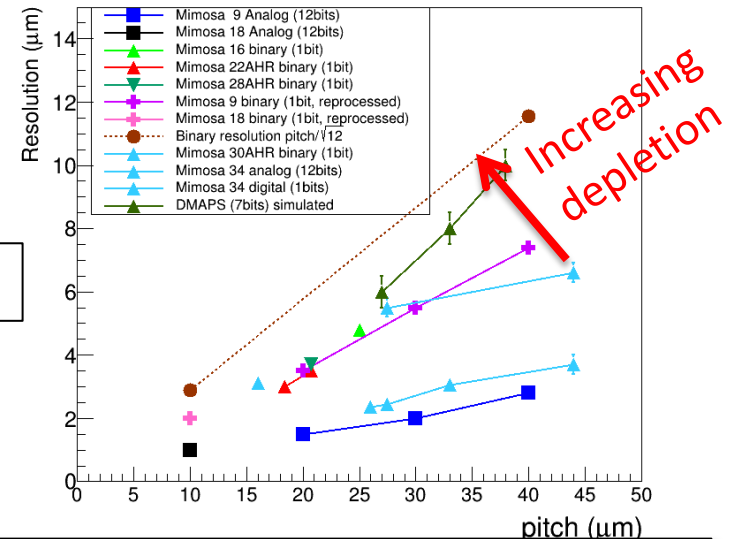


$$\Delta d_0|_{res.} \approx \frac{3\sigma_{r\phi}}{\sqrt{N+5}} \sqrt{1 + \frac{8r_0}{L_0} + \frac{28r_0^2}{L_0^2} + \frac{40r_0^3}{L_0^3} + \frac{20r_0^4}{L_0^4}}$$

$$\Delta d_0|_{m.s.} \approx \frac{0.0136 \text{ GeV}/c}{\beta p_T} r_0 \sqrt{\frac{d}{X_0 \sin \theta} \sqrt{1 + \frac{1}{2} \left(\frac{r_0}{L_0} \right) + \frac{N}{4} \left(\frac{r_0}{L_0} \right)^2}}$$

d = layer thickness, N = # layers

CMOS pixel resolution vs pitch

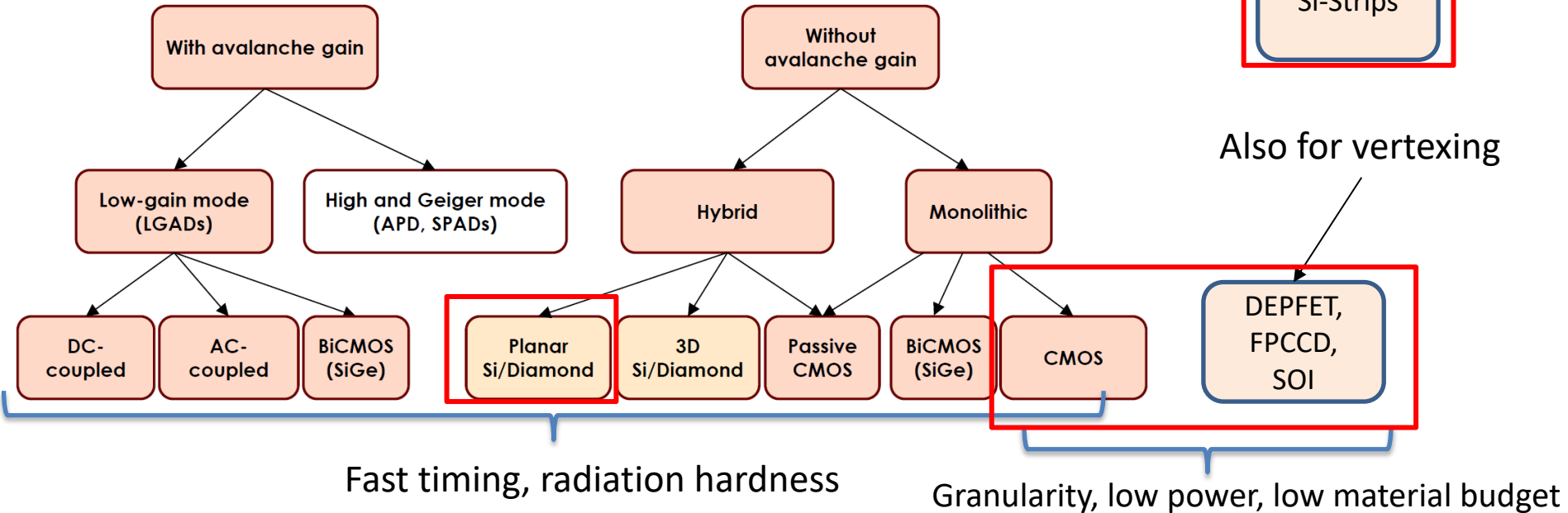


⇒ $\sigma_{sp} \sim 3 \mu\text{m}$ ⇔ pitch $\sim 15\text{-}20 \mu\text{m}$

(assuming binary output, $\sim 20 \mu\text{m}$ epi.thickness & large depletion in 180nm tech.)

Pixel detectors landscape for FCCee detectors

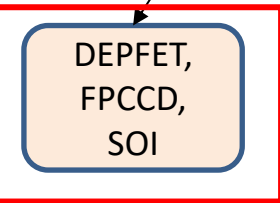
Solid state detectors for future (4D) trackers



Also for tracking



Also for vertexing



Fast timing, radiation hardness

Granularity, low power, low material budget

- VTX hierarchy of the driving parameters
 - ✓ Granularity & material budget > Power > time resolution > Radiation hardness
- Outer tracker
 - ✓ Material budget still a must. Relaxed granularity ⇒ possible focus on Power, time resolution
- Specialized timing layers
 - ✓ Timing layer ⇒ Price to pay: granularity and/or Power
- R&D needed to improve the parameter space

Power vs fast timing vs pixel size



Brief considerations about electronics: power

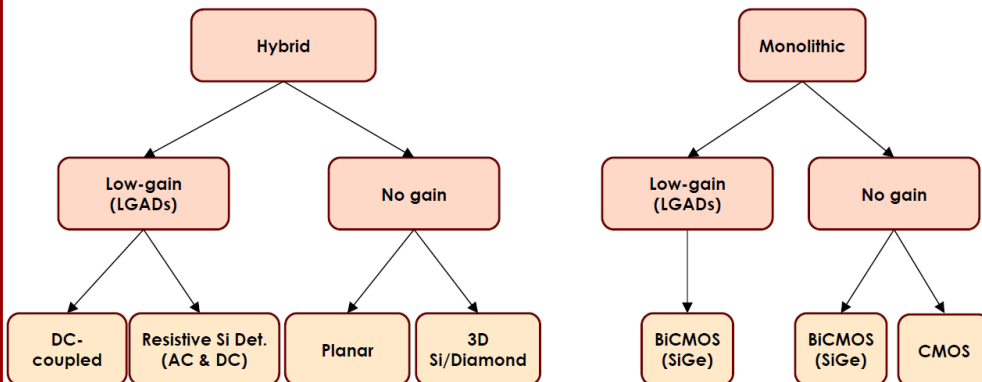
Nicolò Cartiglia, INFN, Torino, VCI2022, 25/02/22

Name	Sensor	node	Pixel size	Temporal precision [ps]	Power [W/cm ²]
ETROC	LGAD	65	1.3 x 1.3 mm ²	~ 40	0.3
ALTIROC	LGAD	130	1.3 x 1.3 mm ²	~ 40	0.4
TDCpic	PIN	130	300 x 300 μm ²	~ 120	0.45 (matrix) + 2 (periphery)
TIMEPIX4	PIN, 3D	65	55 x 55 μm ²	~ 200	0.8
TimeSpot1	3D	28	55 x 55 μm ²	~ 30 ps	5-10
FASTPIX	monolithic	180	20 x 20 μm ²	~ 130	40
miniCACTUS	monolithic	150	0.5 x 1 mm ²	~ 90	0.15 – 0.3
MonPicoAD	monolithic	130 SiGe	25 x 25 μm ²	~ 36	40
Monolith	LGAD monolithic	130 SiGe	25 x 25 μm ²	~ 25	40

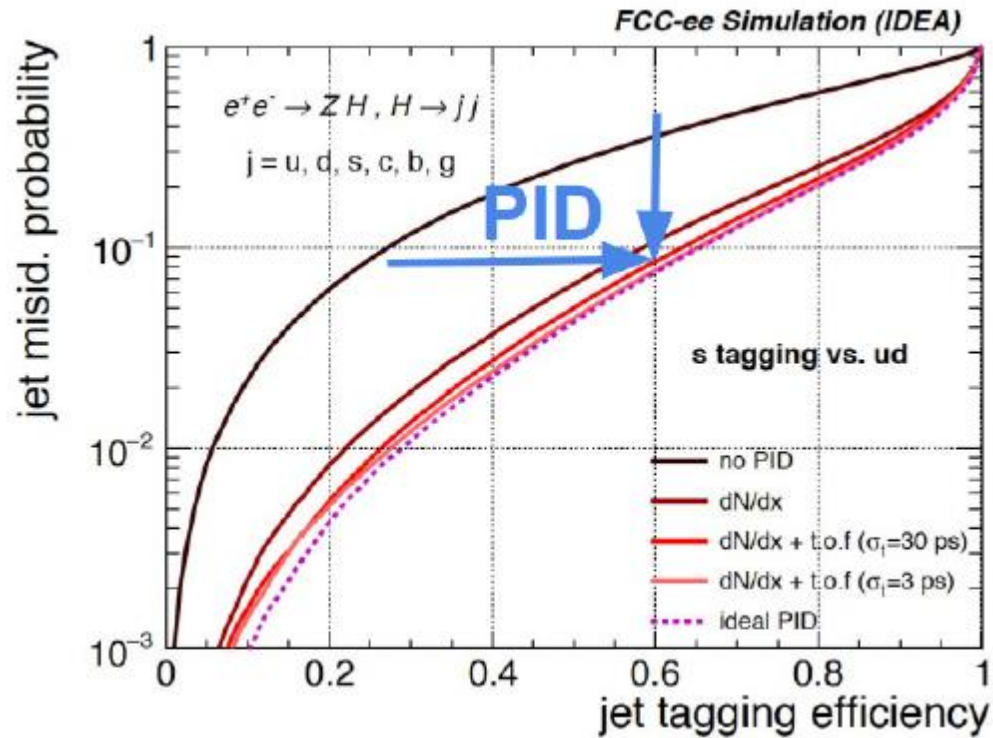
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Price to pay: additional cooling system (additional material)

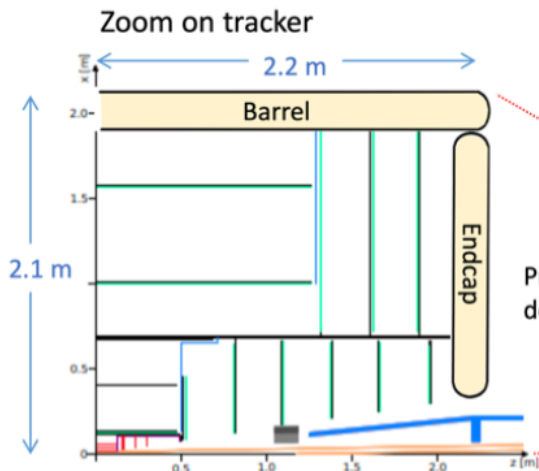
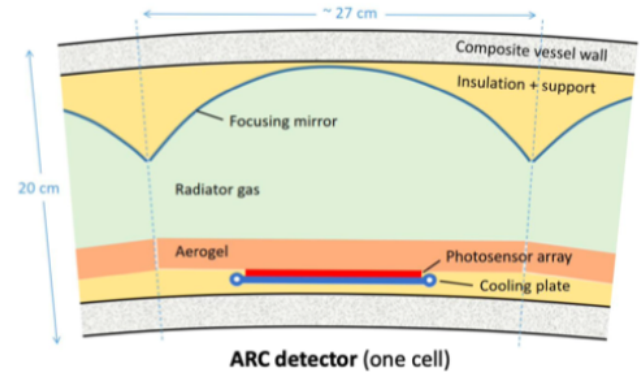


s-tagging

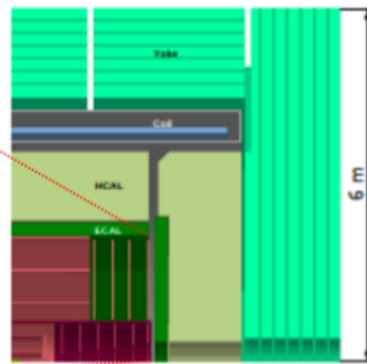


Compact RICH detector for FCC-ee

- ◆ Design goal: Compact design, max 20 cm depth, few % X_0
- ◆ Use spherical focussing mirrors, $r = 30$ cm, for radiator thickness of 15 cm
- ◆ Two radiators
 - Aerogel
 - Gas
 - ❖ Unpressurised C_4F_{10} gives good momentum range for $K-\pi$ separation, with acceptable photon yield
 - ❖ Pressurised Xenon may provide similar performance if fluorocarbons unacceptable



A quarter of CLD



Proposed ARC detector

R.Forty, 9th FCC Week, 2023

$K-\pi$ separation

