IFIC - SiWECAL activities: gluing and more

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COB paper



COB vs FEVs noise paper draft

- ▶ Draft since december...
 - Overleaf link (read only) https://www.overleaf.com/read/gwkswbsgcckj
- If you want edit rights, please let me know.
- ► Still missing few bites
 - Text and figures on COB design
 - Description of the production
 - Description of the process of chip bonding and encapsulation
 - Sensor gluing (? needed? Was it explained in previous papers?)
 - Few crosschecks of numbers (gains, etc)
 - Description of the injection system



Person power IFIC

- A. Irles
 - 50% CALICE, 50% ILD & pheno, 75% LUXE&ECAL-p, 50% preparing for position, 50% office work...
- Student starting this month (Melissa Almanza)
 - 100% LUXE and ECAL-p
- ≥ 20% mechanical engineer (César Blanch)
 - LUXE ECAL-p & ECAL-e: preparing tools for gluing R&D
- ▶ Postdoc... this was a failure. Our candidate dropped and went to industry.
 - Not clear if we will be able to maintaint the budget.
- Not long-term positions for enegineers/technicias associated to this project.

- Gluing R&D for CALICE is not in danger.
 - Large scale production is a different topic...





Gluing R&D

► Check slides discussed in LUXE ECAL-p meeting (in the indico).

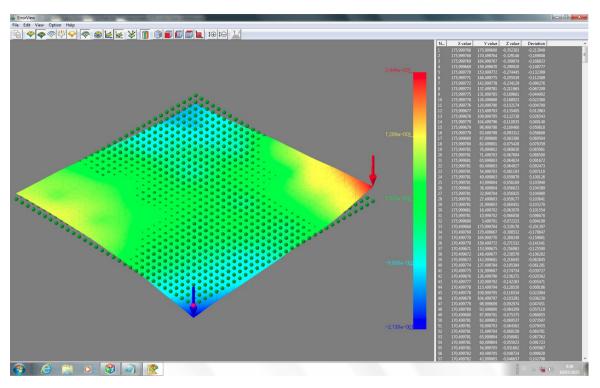
- ► FEV2 metrology
- ▶ Quick gluing tests / samples for Alice/Alex
 - Testing new and old glues
- ► R&D strength of the glue
 - Pull tests (Alice/Alex)
 - Underfill (Alice/Alex)
 - Double tape approach
 - Other solutions
 - Optimization of FEV2 assembly?
- ▶ Wafer characterization



FEV2 metrology

- IFIC A
- 5

- ► In production. Few samples per lab.
 - IFIC has one (fully equipped and functional...)
 - Not tested at IFIC yet
- ▶ Metrology at IFIC doesn't show great results...
 - to be compared with metrologies before and after component soldering







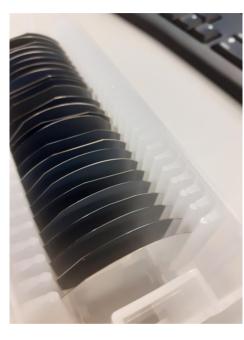
August 2023







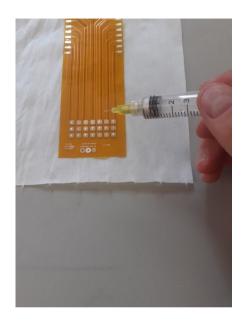


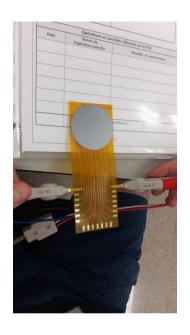














- New tests done in August 2023, using serynge with different glues (H20E, EJ4110)
- Measurement of resistivity being performed
- Different humidity storage conditions.
 - The lab (25degrees, ~20humidity) &
 - the workshop in my house Palomar (30 degrees... ~70-80% humidity)













August 2023 – two PCBs shipped to IJCLabFIC 🔨











August 2023 – two PCBs shipped to IJCLabFIC 🔨

- Curing at 80 degrees for half a day (15h)
- Full procedure in the aspiration plate
- Deformation of the PCB visible after the process...
 - Not seen when doing tests in climatic chamber without wafers / aluminum plate
 - Previous tests were done in an oven and/or climatic chamber... better controlled environment
 - Curing oven will be available for us at IFIC.





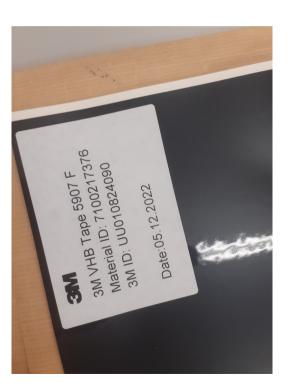


Testing the double tape approach





- ► Another approach is to use double tape
 - Dirk managed to get a sample of 150um which is perfectly okay for CALICE











Testing the double tape approach















Testing the double tape approach



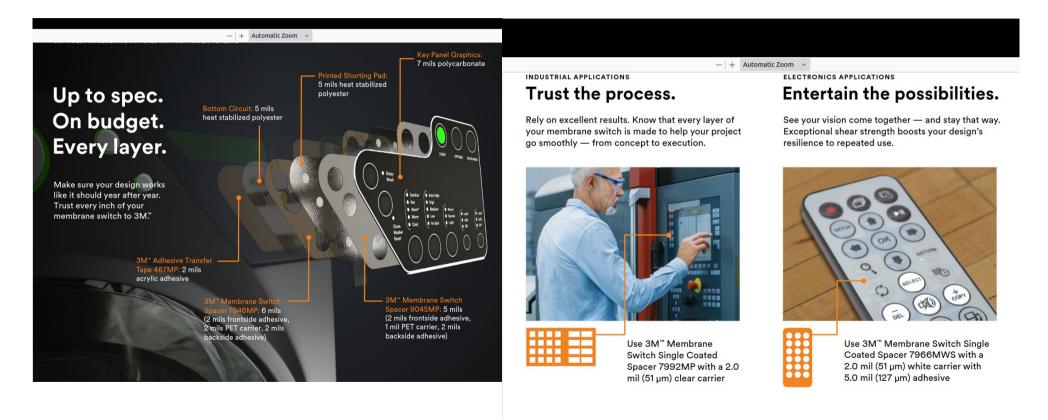


- As we speak
 - Stencil design in process (with holes of 3mm diameter)
 - Production will, hopefully, happen before CALICE meeting

- Questions (for underfill too)
 - What are the electrical properties of these solutions?



https://multimedia.3m.com/mws/media/4414400/3m-tapes-for-membrane-switch-brochure.pdf











https://multimedia.3m.com/mws/media/4414400/3m-tapes-for-membrane-switch-brochure.pdf

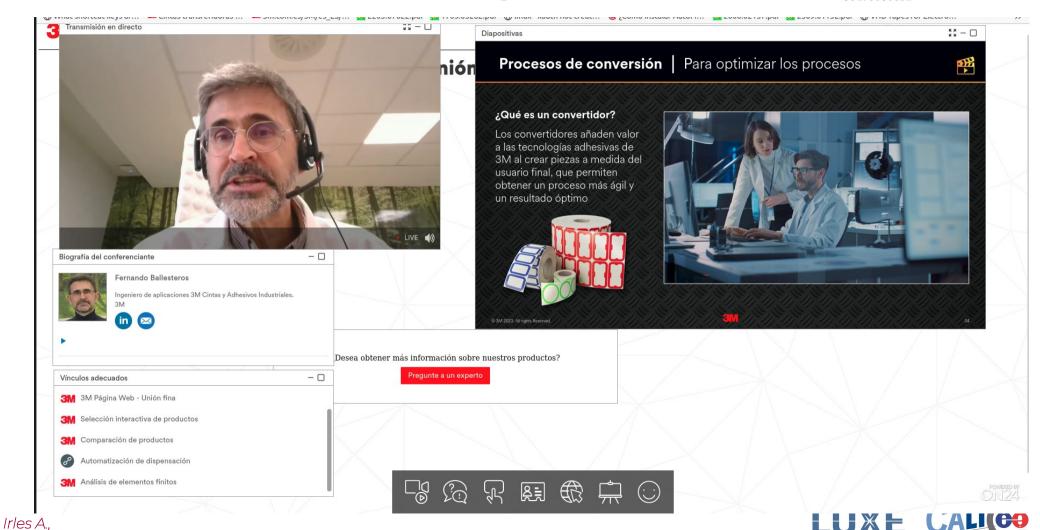
3M[™] Tapes for Metal and HSE Plastics

with 3M™ Acrylic Adhesive

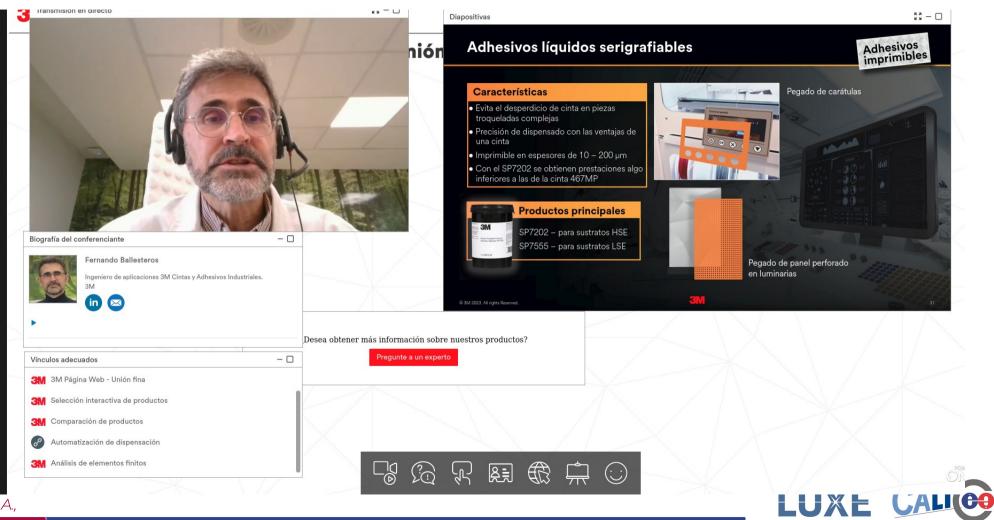
Product Number	Description	Total Caliper (mils)	Construction		Caliper (mils)
7945MP	Excellent temperature, chemical and UV resistance. High shear strength withstands repeated stresses of switch actuation. Designed to separate switch circuitry until actuation. Both liners are printed.	5	Top Liner	58# PCK	
			Adhesive Type I	200MP	2
			Carrier	Polyester	1
			Adhesive Type II	200MP	2
			Bottom Liner	58# PCK	
			Tanlinas	50# DOV	













The GB-matrix type of Inter-Connector







HOME

ABOUT ~

MARKETS

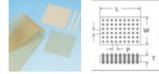
PRODUCTS



GB-Matrix type of Inter-Connector



The GB-U-type of Inter-Connector is used for permanent board to board and component to board connections.





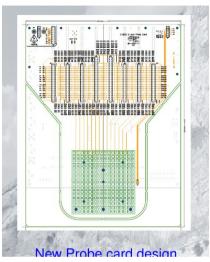


Wafer characterization

- IFIC A
- 19

- We are getting a switch + probe card for testing sensors
 - Coordinated with ECAL-p / Tel Aviv sensor testing
 - Electronics designed by CERN
- Two parts:
 - Switch card → designed by CERN
 - Probe card → designed and produced by CERN (~5000CHF)
- ▶ The switch card is to be produced by us
 - I contacted a spanish company, ROMPAL.
 - Will produce 1 for me...









20

Meeting with Rompal

- ▶ I haveve shown them our FEV2
- ➤ They have an industrial process for PCB assembly with large and long oven that facilitate the optimiziation of the heating/soldering with different phases
- ► They could do a detailed study of deformation and damage of the PCB (and/or components) during the assembly
 - It requires to destroy one or two naked FEV2.
 - The result of the process could be a special oven program for our cards
- ► They could also equip our PCBs, after they have done the tests
 - Or before.



