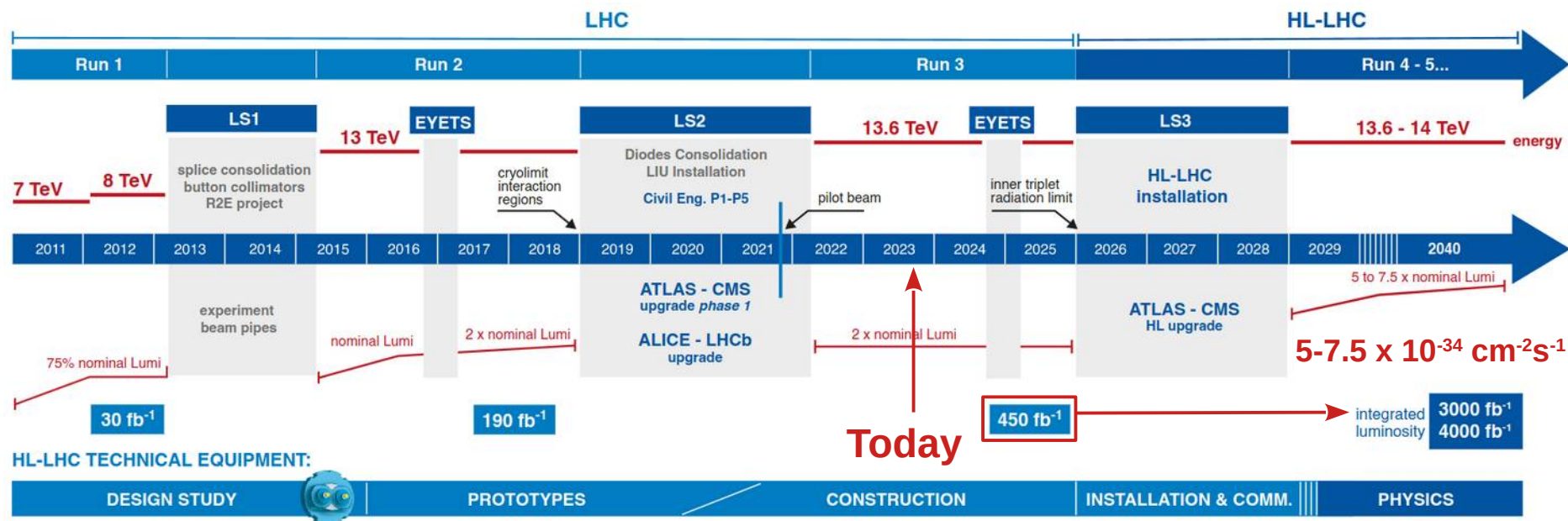


# The Inner Tracker of ATLAS

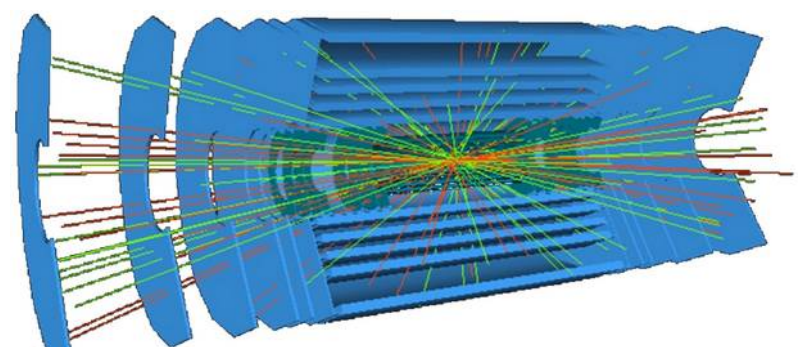
F. Costanza, on behalf of the ATLAS Collaboration

Intensity Frontier Workshop, GDR-InF  
CPPM, September 19<sup>th</sup>-20<sup>th</sup> 2023

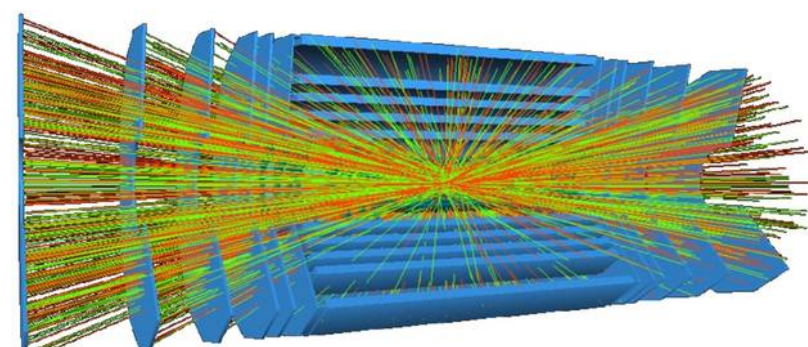


LHC: Inner Detector (ID), made of Pixels + Strips + TRT

HL-LHC: New all-silicon Inner Tracker (ITk), made of Pixels + Strips



19 – 55 pileup events



140 – 200 pileup events



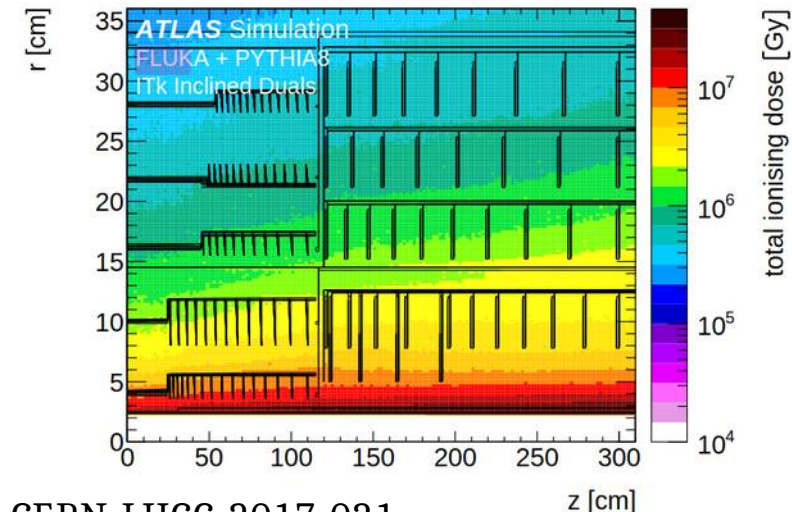
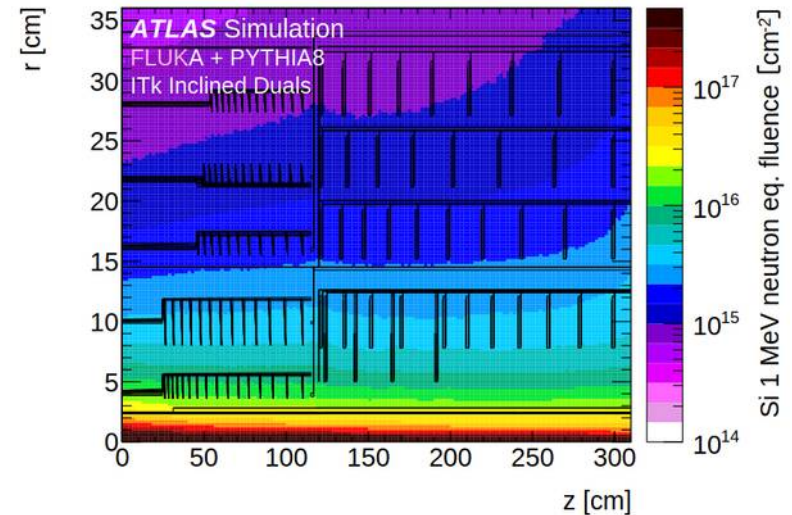
- ◆ **Goal:** maintain (or improve) current ID tracking performance in the harsher HL-LHC environment.

- ◆ **Instantaneous conditions:**

- ◆ Increased pileup, high event rate
- ◆ increased occupancy

- ◆ **Integrated effects:**

- ◆ x10 increase radiation damage.
- ◆ Particle fluence up to  $2 \times 10^{16} \text{ n}_{\text{eq}} \text{ cm}^{-2}$  and total ionizing dose (TID) up to **1MGy**.



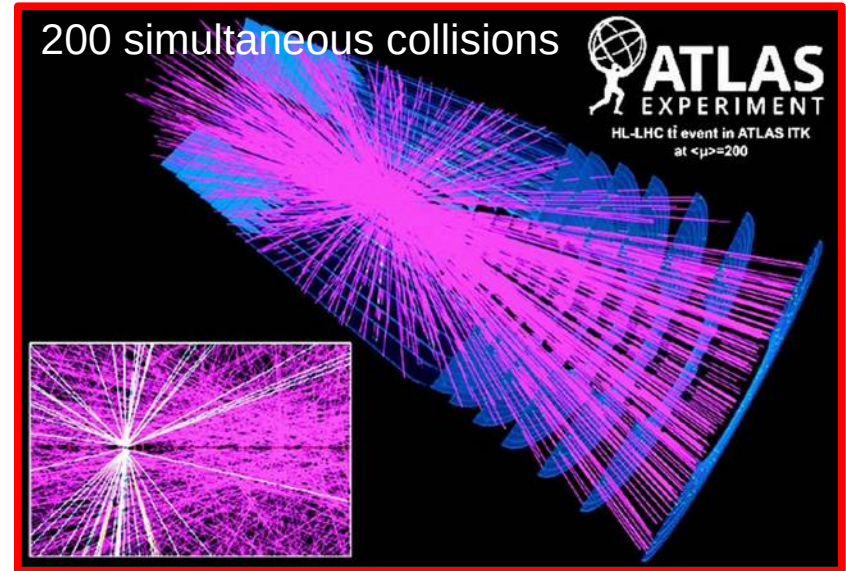
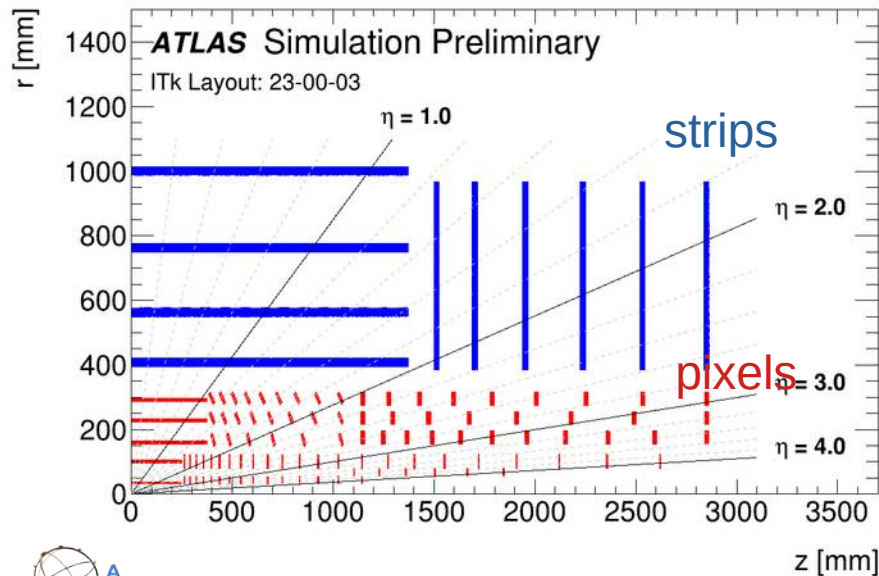
CERN-LHCC-2017-021



# The ATLAS Inner Tracker upgrade

- ◆ The Inner Tracker (ITk) @HL-LHC requires:
  - ◆ Increased radiation hardness.
  - ◆ Higher granularity (smaller pixels).
  - ◆ Faster readout.
  - ◆ Reduction of material.
  - ◆ Extended coverage up to  $|\eta| < 4.0$ .

ATL-PHYS-PUB-2021-024

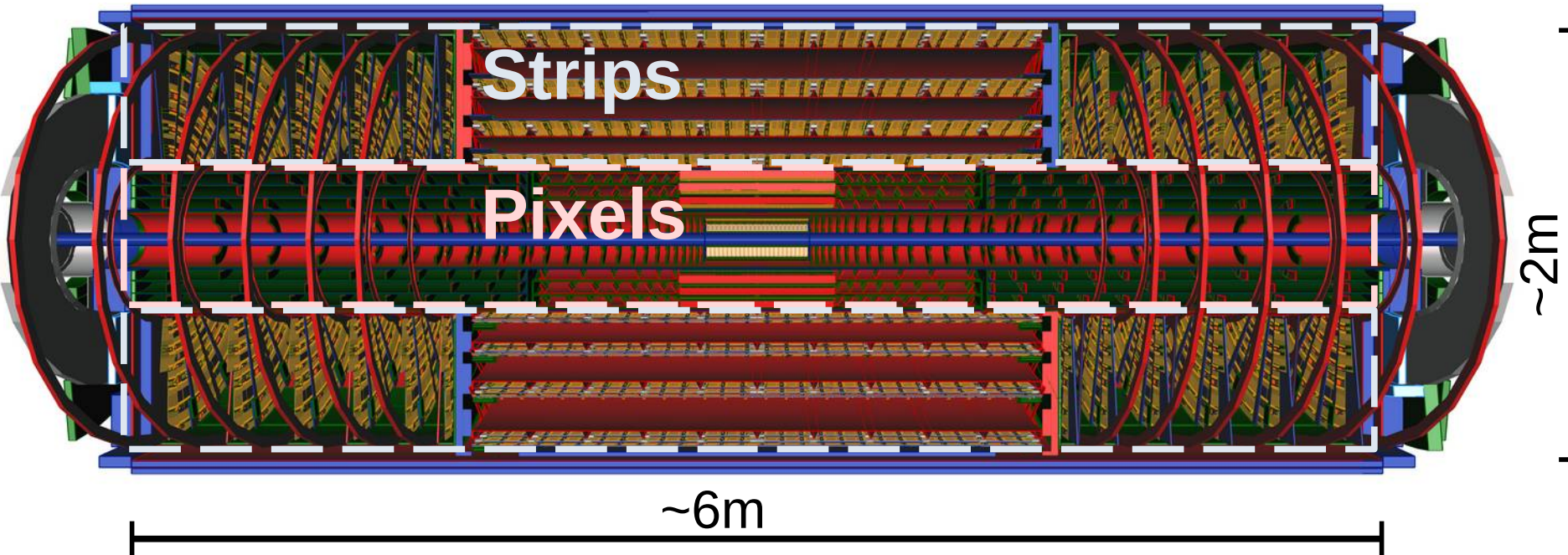


- ◆ Reduced pixel pitch:  $50 \times 50 \mu m^2$  **ITk-Pixel**
- ◆ Five barrel layers with inclined sensors from  $|\eta| > 1.4$  to reduce the material budget.
- ◆ Endcap rings to improve coverage. **ITk-Strip**
- ◆ Four strip barrel layers and six endcap disks



# The ATLAS Inner Tracker upgrade

ATL-PHYS-PUB-2021-024

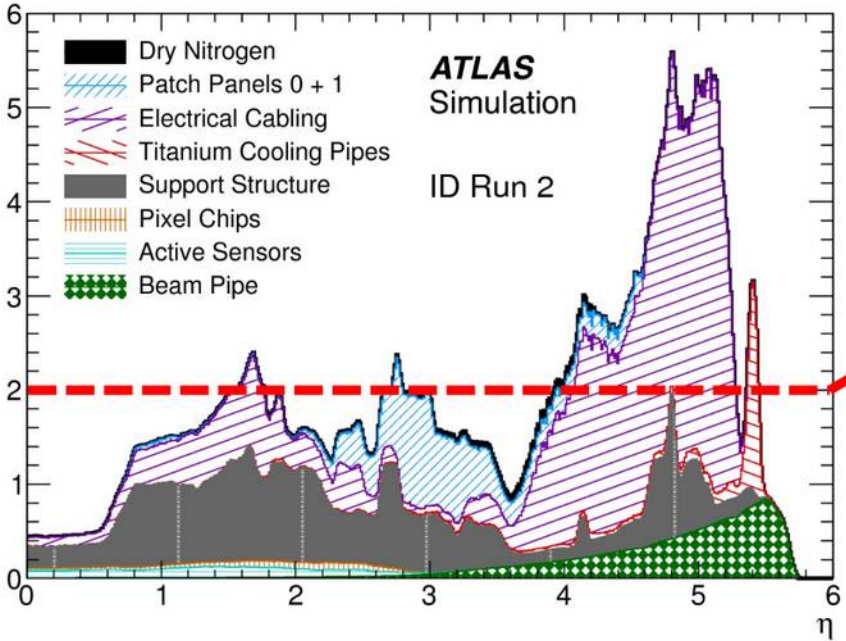


ITk (ID)	Area (m <sup>2</sup> )	# Modules	# Channels (M)
Pixels	13 (1.6)	9164 (2000)	5100 (92)
Strips	165 (61)	17888 (4088)	60 (6.3)

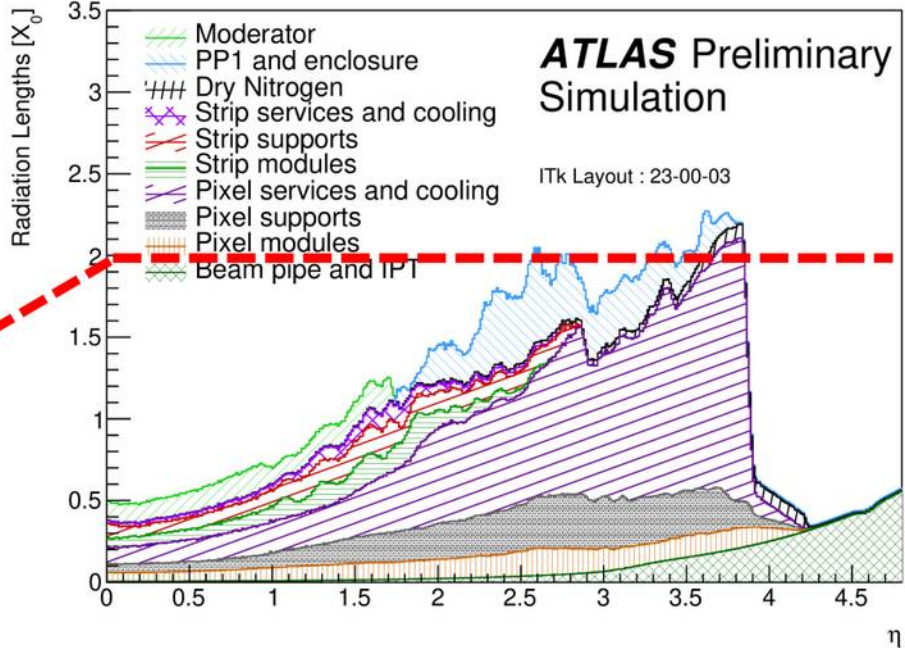


- ◆ Lower material budget than ATLAS ID
  - ◆ **Advanced cabling:** serial powering for Pixels, data link sharing.
  - ◆ **Thin Sensors** and FE-chips, **inclined modules** for Pixels  $|\eta| > 1.4$ .
  - ◆ CO<sub>2</sub> titanium pipes for cooling, low-mass carbon local supports.

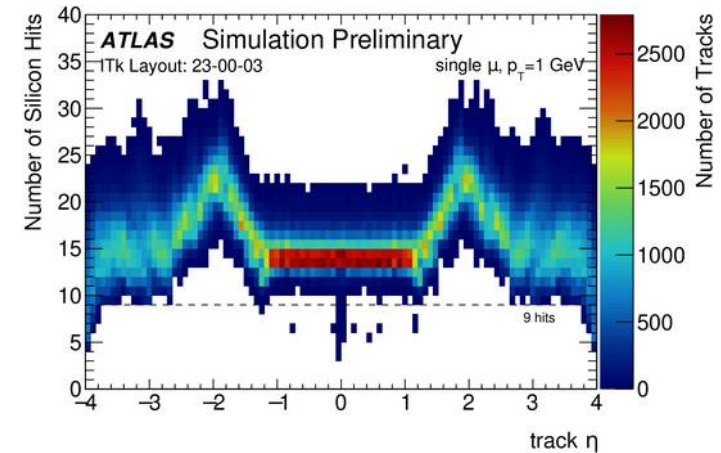
ATL-PHYS-PUB-2021-024



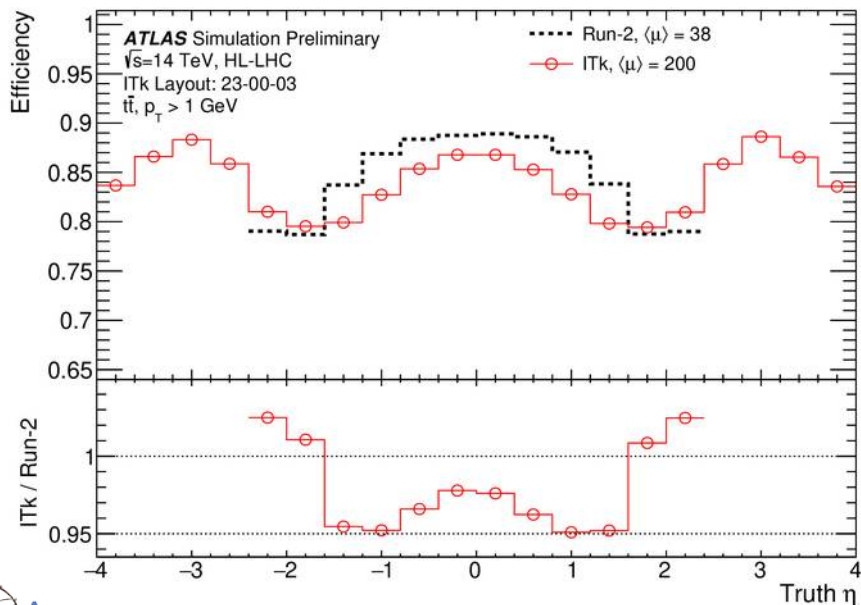
ATL-PHYS-PUB-2021-024



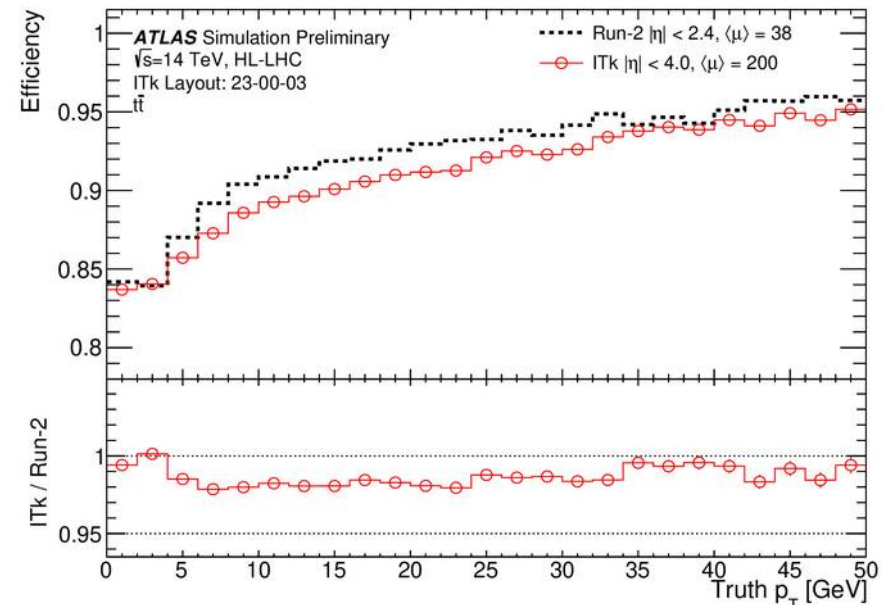
- ◆ Minimum 9 hits on the full pseudorapidity range.
- ◆ Tracking efficiency pileup = 200 for ITk vs 38 for ID
  - ◆ Similar performance to Run-2 in the barrel.
  - ◆ Efficiency **above 85 % at high pseudorapidity.**

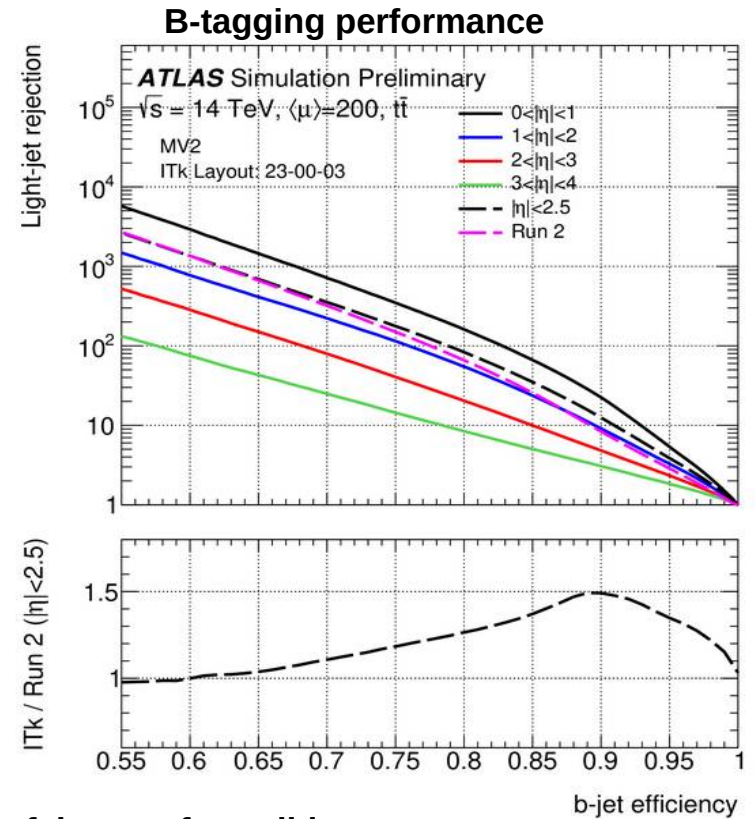
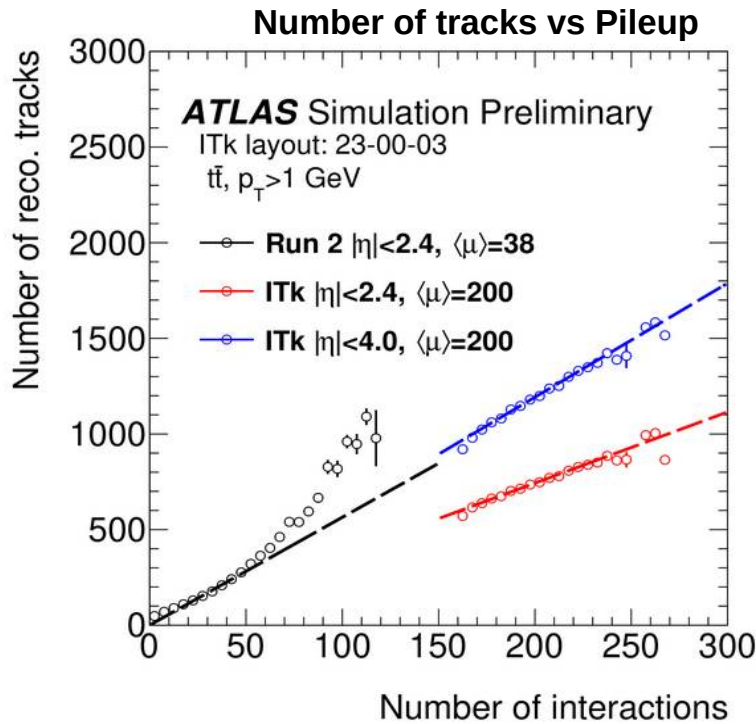


ATL-PHYS-PUB-2021-024

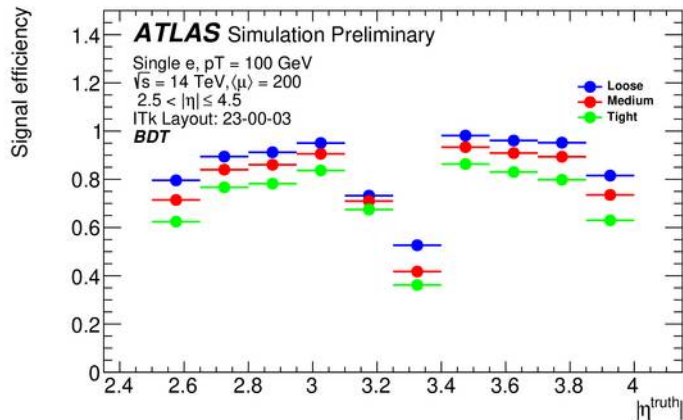


ATL-PHYS-PUB-2021-024

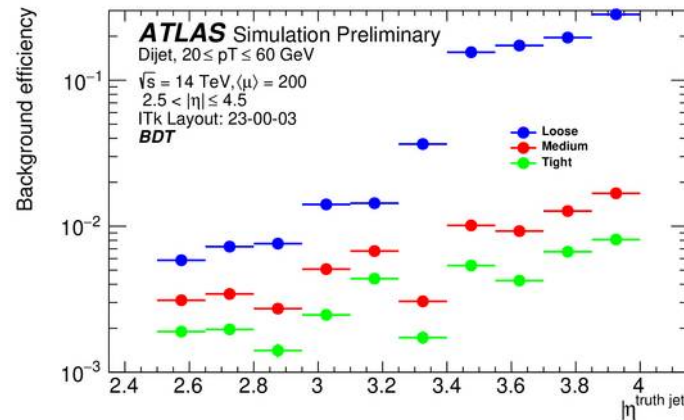




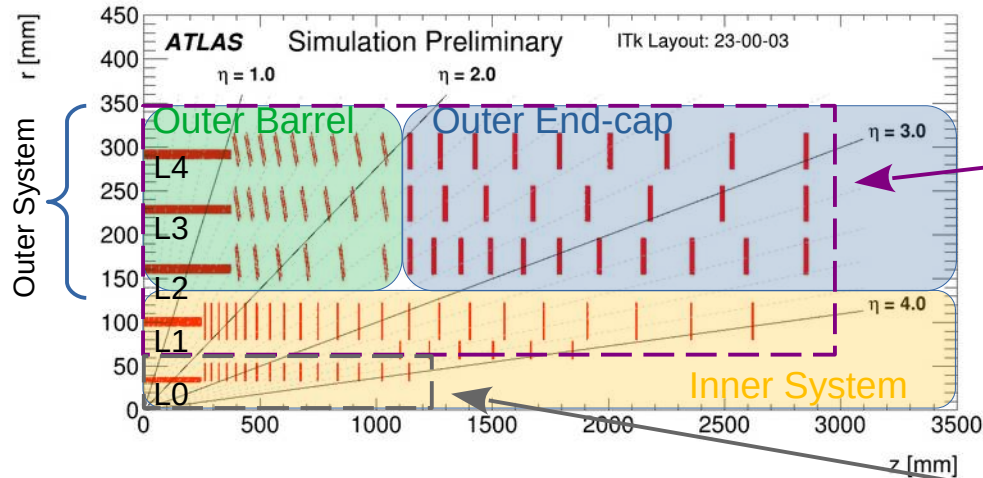
### Forward Electron reconstruction Efficiency



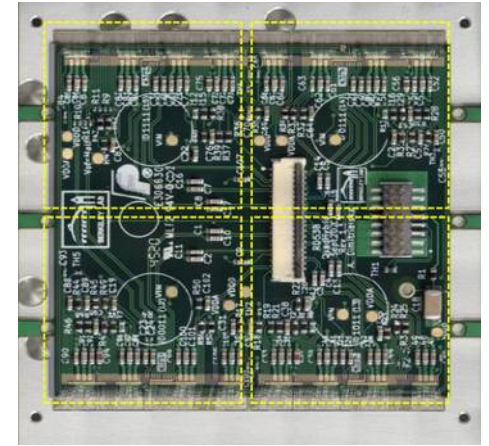
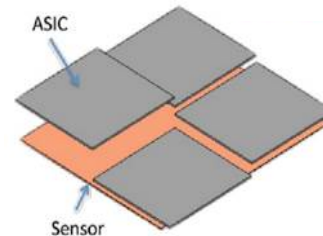
### Forward Electron fake-rate from di-jets







**L1-4 planar quad module**  
(single sensor)



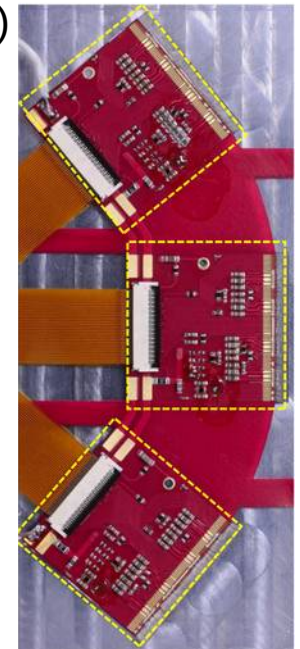
◆ Outer system

- ◆ **3 layers** of flat staves and inclined rings.
- ◆ **~7k planar quad modules** with 150 $\mu\text{m}$  thick sensor + 150 $\mu\text{m}$  thick ASIC, 50x50 $\mu\text{m}^2$  pixel size.

◆ Inner system

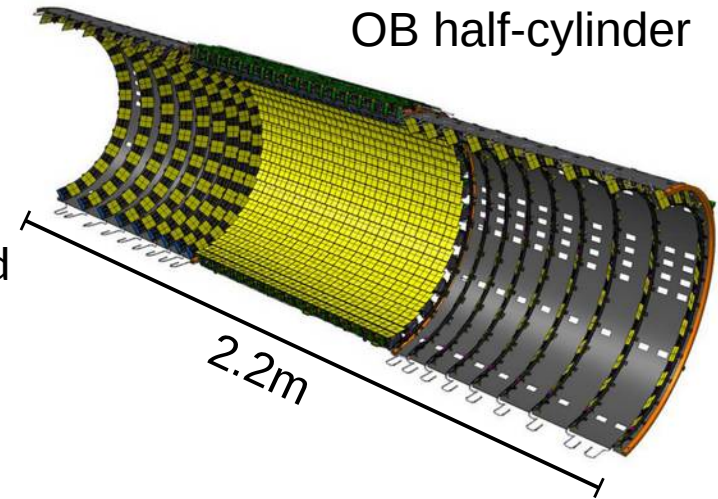
- ◆ **2 layers** of flat staves and rings.
- ◆ L0: **~1.2k 3D single modules** (25x100 $\mu\text{m}^2$  for flat, 50x50 $\mu\text{m}^2$  for EC).
- ◆ L1: **~1.2k planar quad modules** with 100 $\mu\text{m}$  thick sensor + 150 $\mu\text{m}$  thick ASIC, 50x50 $\mu\text{m}^2$  pixel size.

**L0 3D single module**  
(triplet PCB)



# The Pixel Outer Barrel

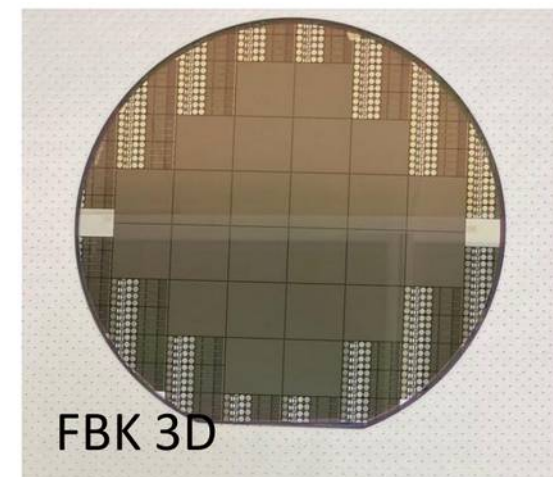
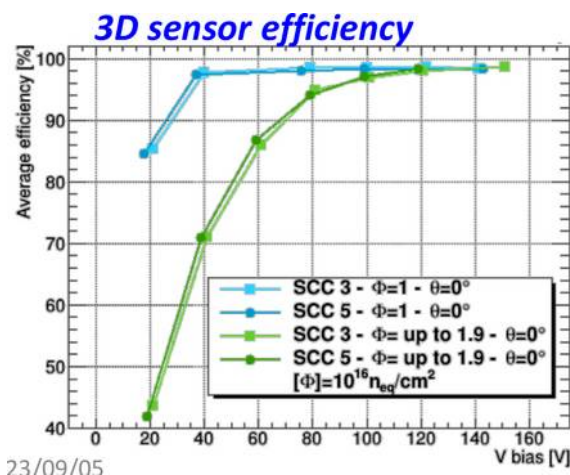
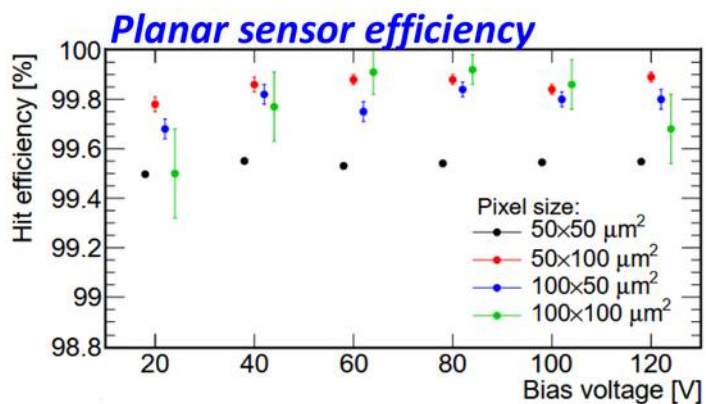
- ◆ **Pixel outer barrel (OB), 3 coaxial cylinders :**
  - ◆ ~50% of Itk-Pixel modules, ~5k modules.
  - ◆ International collaboration:  
CERN, Japon, **France**, Germany, Switzerland
- ◆ **PIX-ILE** cluster : IJCLab/IRFU/LPNHE  
Module assembly.
- ◆ **ALPACA** cluster : CPPM/LAPP/LPSC  
Cell loading and integration of 25% of local supports.



- ◆ Additionally, LAPP is responsible for the design and production of on-detector electrical services, aka type-0.
- ◆ And the tools needed for the integration of type-0 services.

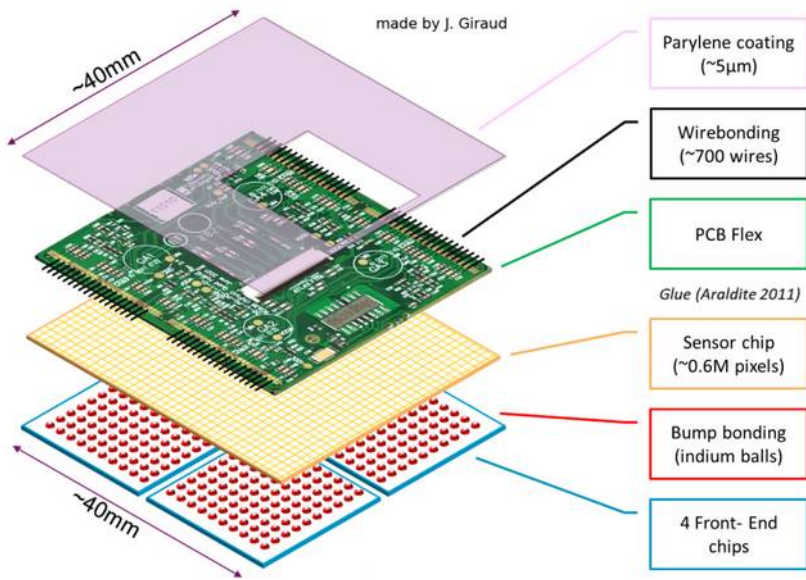
## ◆ Sensors:

- ◆ Pre-production (10% of required for installation) finished in '22.
- ◆ Qualification for final production order almost completed (some production sensors already received).



## ◆ FE ASIC:

- ◆ RD53A and ITkpix-v1 used as prototype program.
- ◆ **ATLAS approved final FE-chip (ITkpix-v2) submission on March 17th '23.**
  - ◆ First delivery of 20 wafers 26<sup>th</sup> June.
  - ◆ First checks show basic functions work as expected.

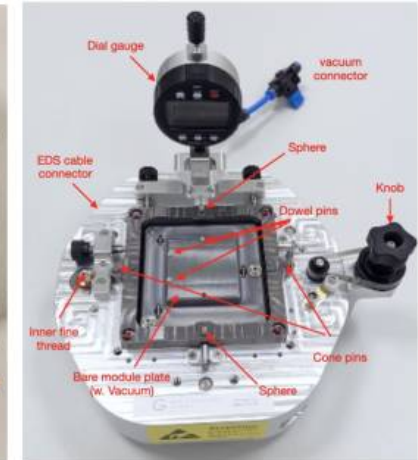
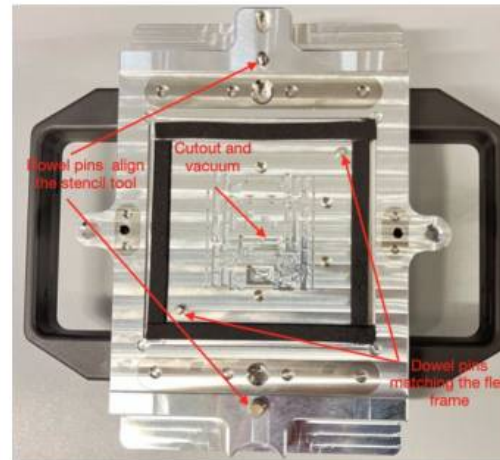


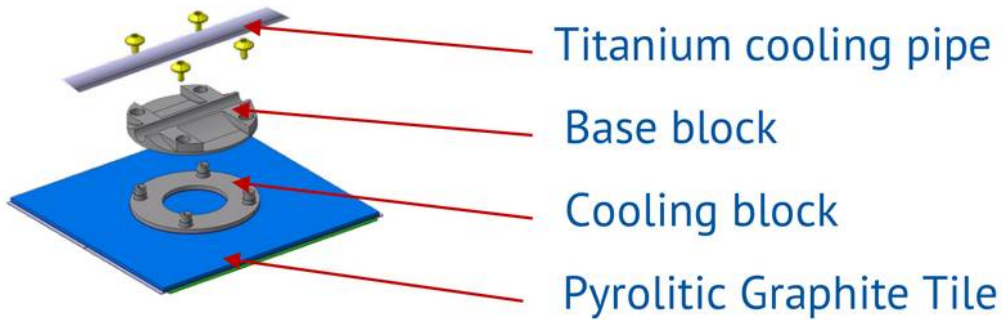
- ◆ Shared among over 20 institutes (CERN, France, Germany, Italy, Japan, UK, USA).
- ◆ 4 ITkpix flip-chip bump-bonded to one sensor tile
- ◆ Custom-designed flex PCB glued on sensor backside
  - ◆ Wirebonded to 4 ITkpix chips
- ◆ Wirebond protections

Quad module with wirebond mechanical protections



Common flex-attach tooling: flex PCB & bare module jig





- ◆ In Outer barrel, cell concept to ease exchange of damaged modules mounted on LLS.
- ◆ Tests for optimisation and selection of pre-production glue patterns underway.
- ◆ QC test setup under commissioning.

Automatic glue dispenser



QC setup to test 4 cells in parallel with cold cycles down to  $-45^{\circ}\text{C}$



Flat section: Longerons

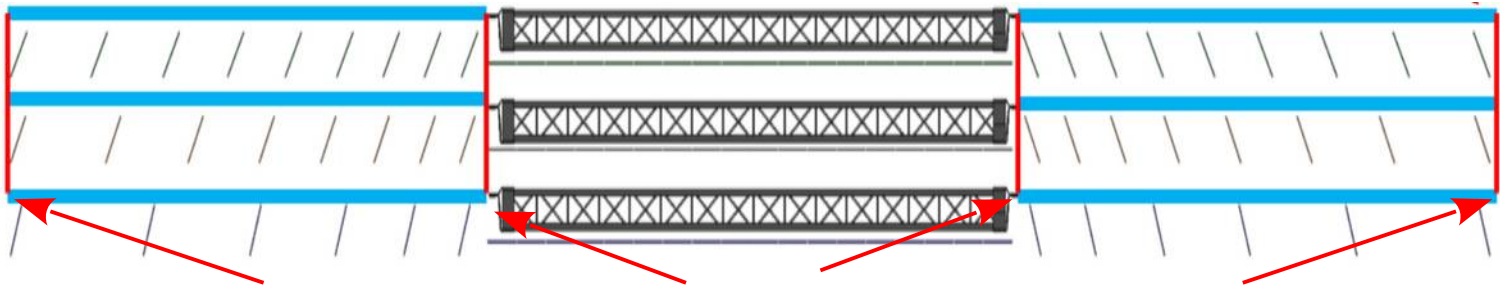


1 red square  
= 1 module

Inclined Half rings



Local supports: mechanical resistance with lowest possible mass → carbon-fiber.



Global Mechanics: **Support points and intermediate supports**

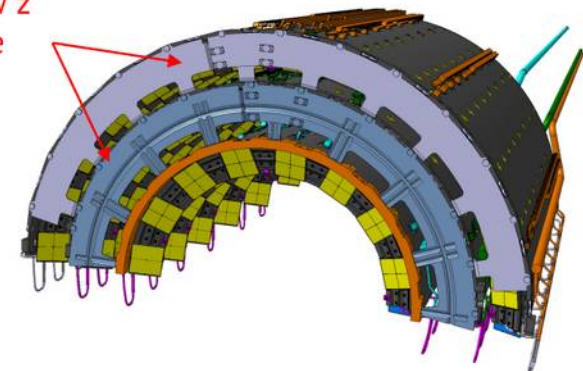
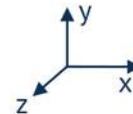


Pixel Support Tube (PST)

PST rails

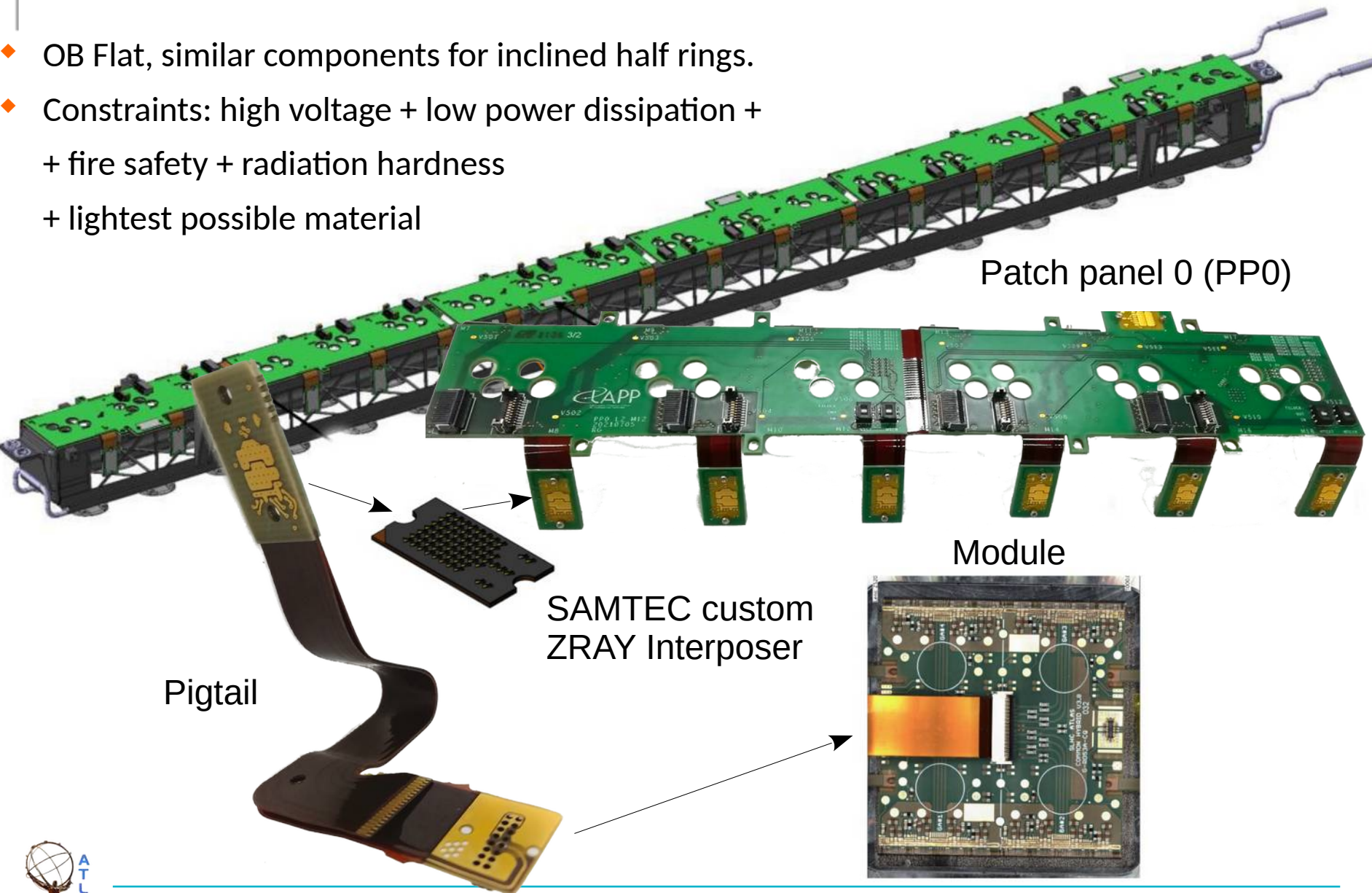
Support points

High z  
Low z  
intermediate  
supports





- ◆ OB Flat, similar components for inclined half rings.
- ◆ Constraints: high voltage + low power dissipation +  
+ fire safety + radiation hardness  
+ lightest possible material



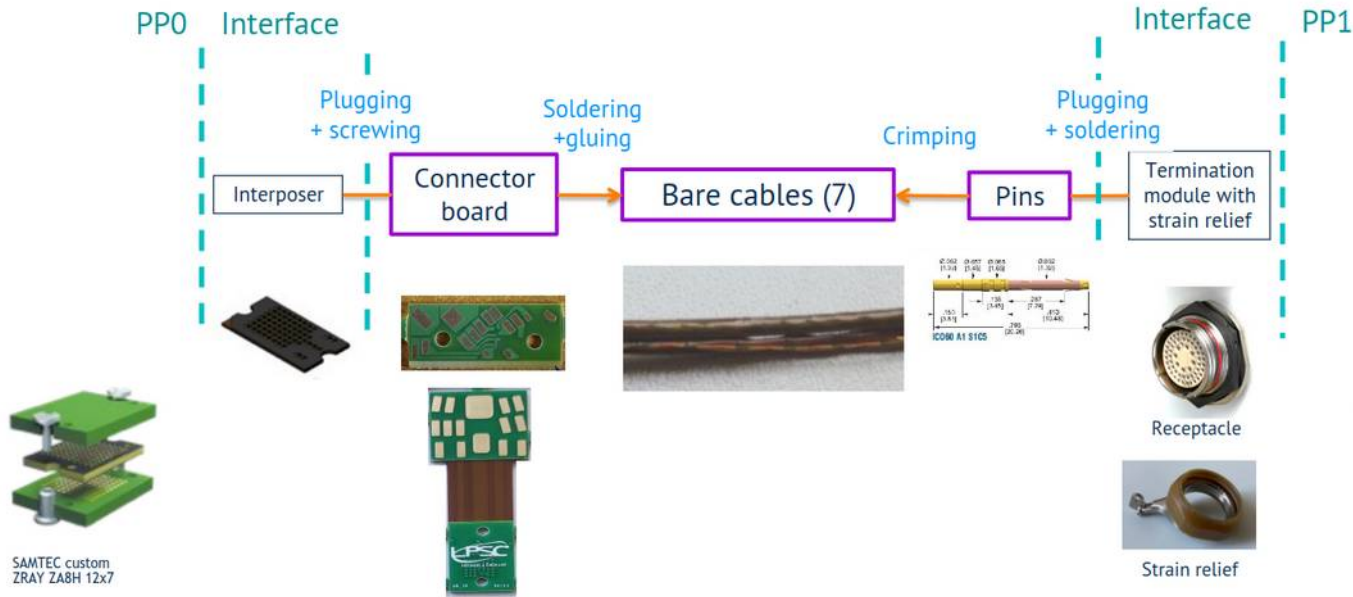
Patch panel 0 (PP0)

Module

SAMTEC custom ZRAY Interposer

Pigtail

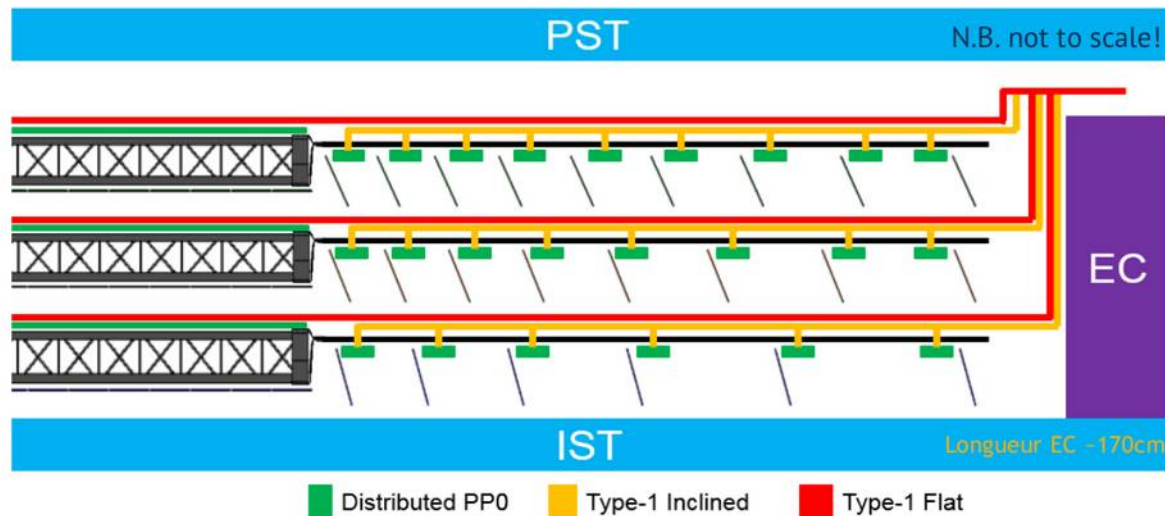




Type-1 Power bundles.  
From distributed PP0  
to the end of ITk.

Similar requirements  
for type-0 services.

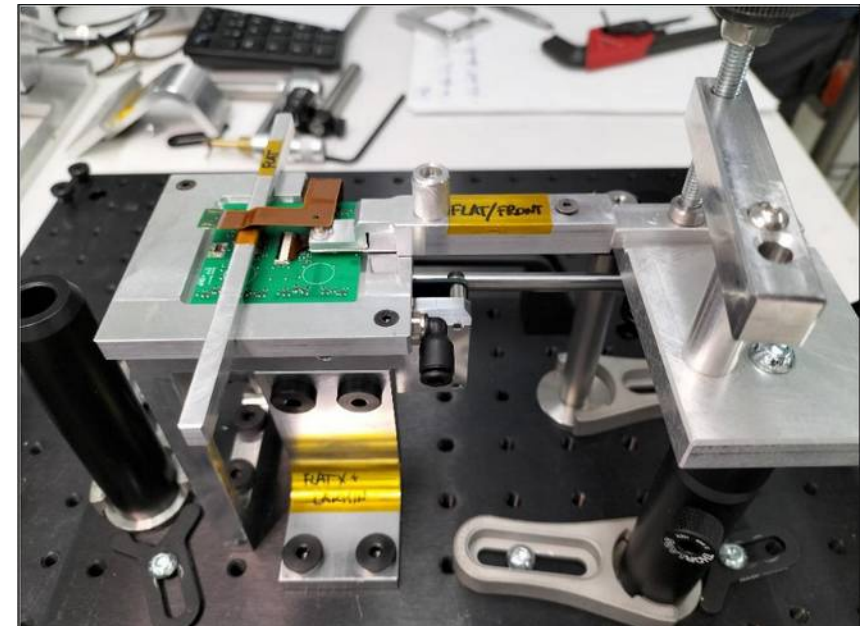
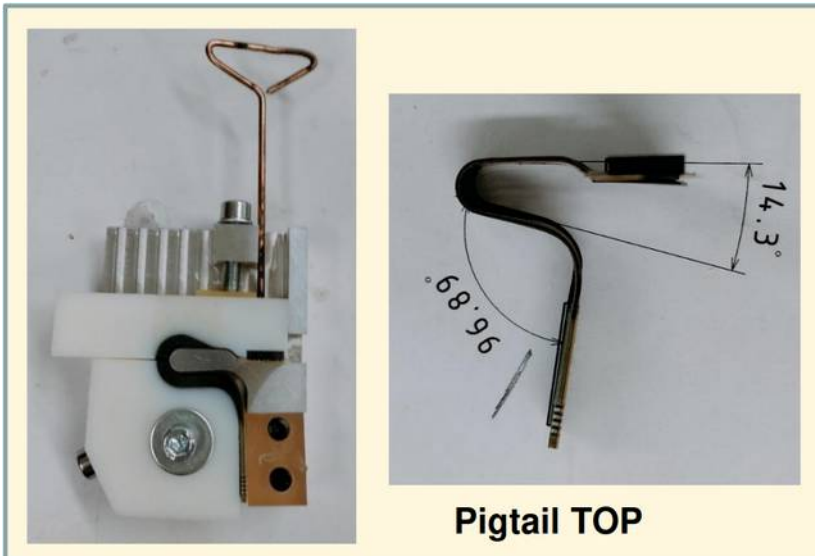
Very challenging space  
constraints.







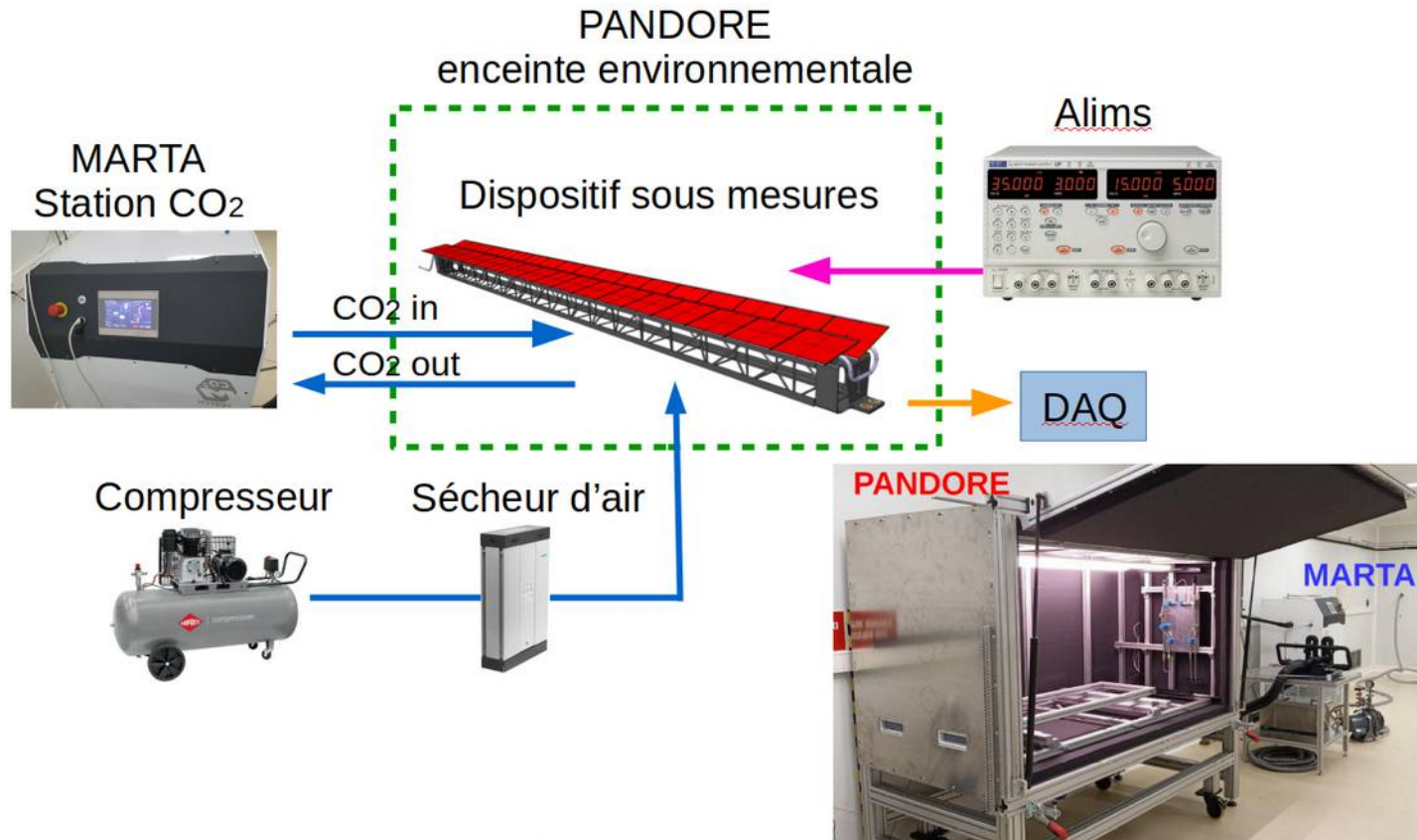
- ◆ Tools for **folding pigtails**: 4 flavors (2 for longerons and 2 for inclined half rings).
- ◆ Tools for **folding wings on PP0s**.
  - ◆ Folding requires **many prototypes** and **iterative process**, not easy to predict how the fold will relax. Heating while folded helps to preserve shape.
- ◆ Tools for **mounting pigtails on modules**, to be replicated for each integration site.





# Quality control of loaded local supports

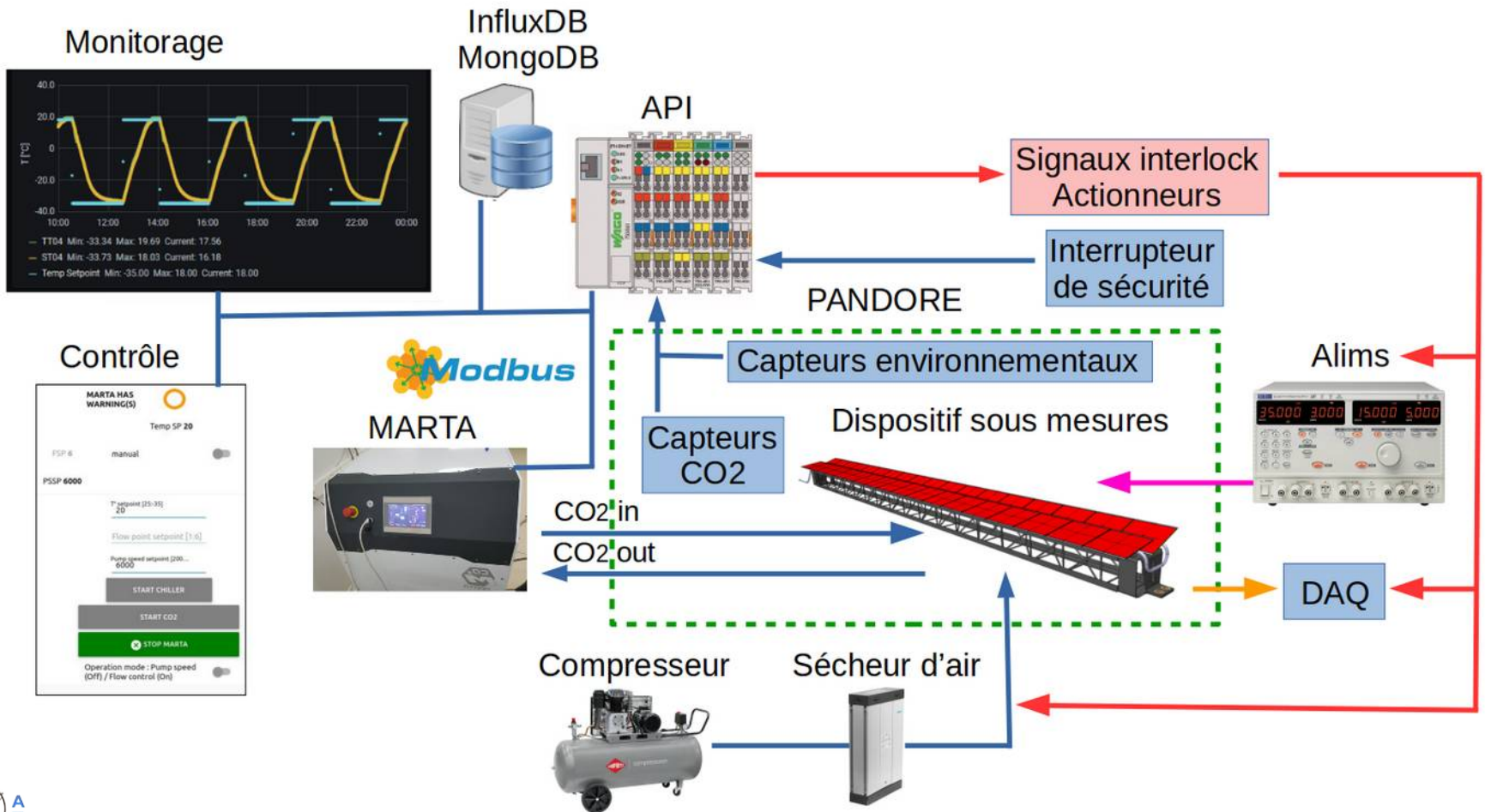
- ◆ Loading of local supports in 6 sites: Bonn Uni, CERN, CPPM, LAPP, LPSC, UniGe
- ◆ After integration, LLS will be tested in a controlled environment (temp, dew point, no light).
  - ◆ Thermal cycles with diphasic CO<sub>2</sub> plant MARTA: 300W cooling power @ -30°C.
  - ◆ Electrical tests: current/voltage curve, module readout, ...





# Quality control of loaded local supports

- ◆ Automatic interlock system to ensure safe operation for users and equipment.
- ◆ Human-machine interface for controlling and monitoring (Node-RED + Grafana).
- ◆ Environmental data stored on local InfluxDB.



- ◆ ITk is a very challenging project and is crucial for successful Physics runs of ATLAS at HL-LHC.
- ◆ France institutes have a **primary role** in the design and construction of several key components of the ITk-Pixels Outer Barrel.
- ◆ **Coordination** among different parts of the project is essential.

