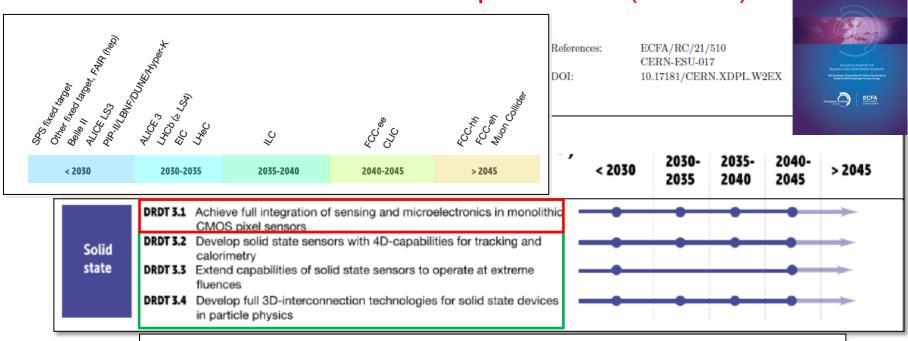
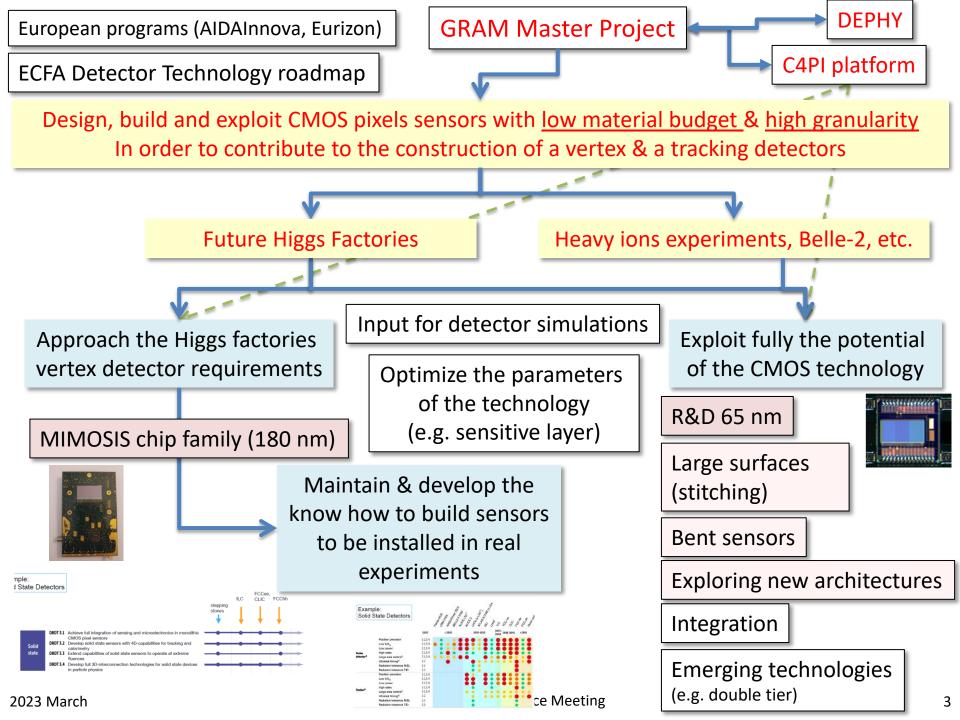
R&D CMOS for FCCee & plans within the ECFA DRDs

Detector R&D Roadmap: themes (DRDTs)



DRDT 3.1 - Achieve full integration of sensing and microelectronics in monolithic CMOS pixel sensors.

Developments of Monolithic Active Pixel Sensors (MAPS) should achieve very high spatial resolution and very low mass aiming to also perform in high fluence environments. To achieve low mass in vertex and tracking detectors, thin and large area sensors will be crucial. For tracking and calorimetry applications MAPS arrays of very large areas, but reduced granularity are required for which cost and power aspects are critical R&D drivers. Passive CMOS designs are to be explored, as a complement to standard sensors fabricated in dedicated clean room facilities, towards hybrid detector modules where the sensors is bonded to an independent ASIC circuit. Passive CMOS sensors are good candidates for calorimetry applications where position precision and lightness are not major constraints (see Chapter 6). State-of-the-art commercial CMOS imaging sensor (CIS) technology should be explored for suitability in tracking and vertex detectors.



DRD organisation

• DRD3 (Solid State Detector)

✓ CERN workshop : March 22-23rd 2023

• DRD7 (Electronics)

✓ CERN workshop : March 14-15th 2023

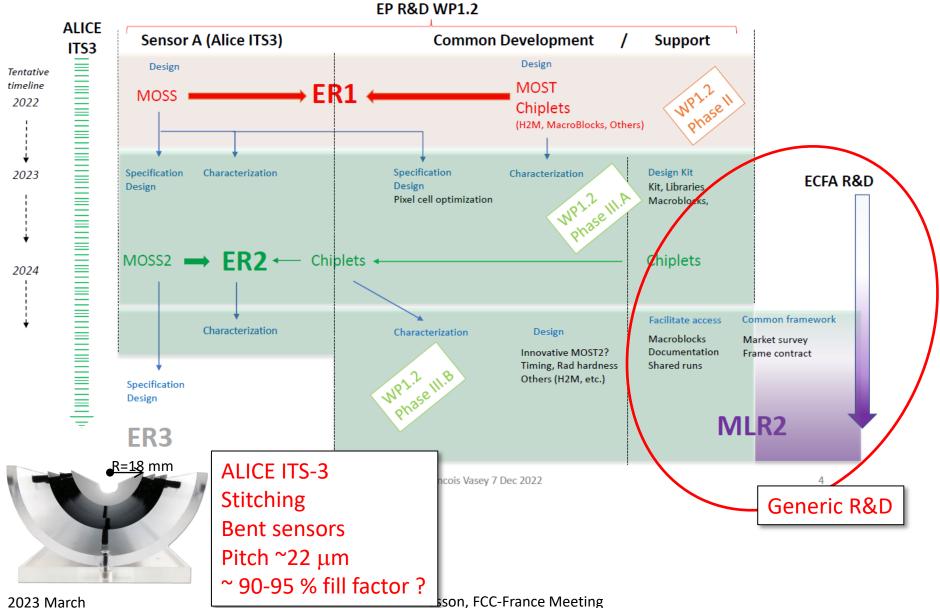
- CMOS activities mainly in DRD3 but both will contribute
 - ✓ 1h30 session dedicated to CMOS in each DRD.
 - \checkmark Discussions ongoing on the final organization
 - $\checkmark\,$ Both DRD will cover also other technologies
- CMOS technologies
 - ✓ Smaller feature sizes allow to improve performances
 - Spatial Resolution, time resolution, Power, etc.
 - ✓ Tower 180 nm: MIMOSIS, ALPIDE, OBELIX, MALTA, etc.

Technology possibly driven by C4Pi in the future

- ✓ TPSco 65 nm: ALICE ITS-3 ⇒ Technology endorsed by CERN
- ✓ Others (e.g. Lfoundry).
- R&D 65 nm: x 3 costs w.r.t. 180 nm

✓ R&D strategy based on common submissions

Submission plans for Tower-65 nm calendar (F. Vasey CERN)



Project proposal for lepton colliders to DRD3

- « Fine-pitch CMOS pixel sensors with precision timing for vertex detectors at future Lepton-Collider experiments »
- Framework: 65 nm technology
 - Targets 3 μm spatial resolution, improved time resolution (5-500 ns), controlled Power (< 50 mW/cm²), data flow (10-100 MHz/cm²) and low material budget (50 μm thickness)
- Proposing Institutes: CERN, DESY, IPHC, APC, etc.
 - ✓ Open to other participations
 - ✓ Includes all Higgs factory concepts
- Goal: gather groups to reach a critical size allowing the submission of demonstrators dedicated to Higgs factories
- Deliverables
 - ✓ Proof of concept demonstrator (MLR2)
 - ✓ Large size demonstrator
 - Allowing to equip new generation of beam telescopes

DRD project: Fine-pitch CMOS pixel sensors with
precision timing for vertex detectors at future
Lepton-Collider experiments

DRD technology area

DRDT 3.1 - Achieve full integration of sensing and microelectronics in monolithic CMOS pixel sensors.

Proposing institutes

Institute	Contact	Foreseen main areas of contribution
APC Paris	M. Bomben	Simulations, testing
CERN	D. Dannheim	Testing, DAQ, ASIC design support
DESY	S. Spannagel	ASIC design, testing, DAQ, simulations
IPHC Strasbourg	J. Baudot	ASIC design, testing
Oxford University	D. Hynds	Testing, simulations
Zurich University	A. Macchiolo	Testing, DAQ, simulations

The proposal is open to additional collaborators and contributions

Development targets and strategy

The physics goals and experimental conditions at high-energy Lepton Colliders (LC) result in stingent, requirements for the alicon vertex detector. High spatial and temporal measurement accuracy needs to be combined with very low mass and power consumption, and the readjust steme needs to be optimized for the optimized dudy cycle and background particle risks at the different accelerators. This proposal concerns the development and particle risks at the different accelerators. This proposal concerns the development and particle risks at the different accelerators. The proposal concerns the development processes, targeting the LC proglements as cultiend in the ECFA detector readmaps. Key development targets include 3 µm angle-point resolution, down to -5 ne time resolution accelator to LL proglement, timming to being with UD µm, an exercise power consumption below 30 mW/cm², a minimal nactive periphery area, and a sensor architecture accelator to LL proglement.

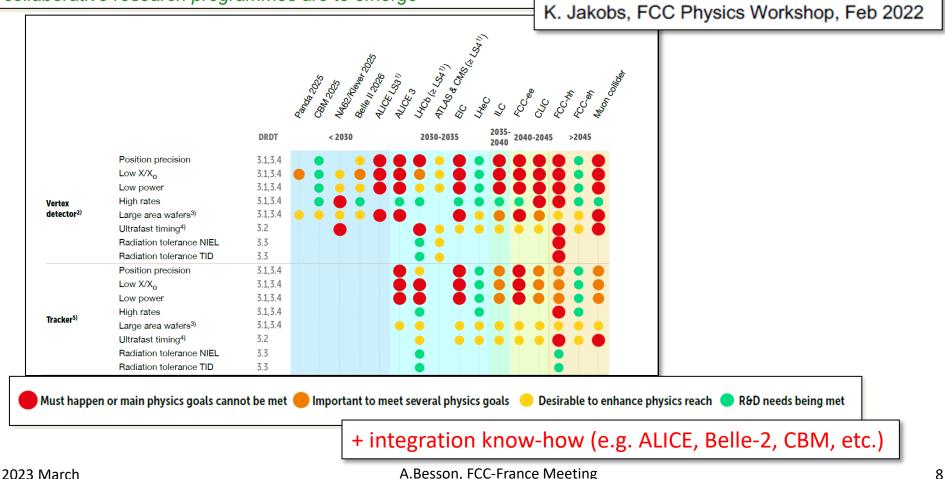
A new generation of low-mass high-resolution beam-telescope sensors is needed to support the various ECFA detector-roadmap developments and to provide accurate reference measurements. The precision requirements for these instruments are similar to the ones for



Synergies

ECFA recognizes the need for the experimental and theoretical communities involved in physics studies. experiment designs and detector technologies at future Higgs factories to gather. ECFA supports a series of workshops with the aim to share challenges and expertise, to explore synergies in their efforts and to respond coherently to this priority in the European Strategy for Particle Physics (ESPP).

Goal: bring the entire e^+e^- Higgs factory effort together, foster cooperation across various projects; collaborative research programmes are to emerge



CMOS Pixel Sensors @ IN2P3

- New IN2P3 internal organisation for CMOS R&D
 - ✓ 2 national master projects (provides R&D funding)
 - GRAM (Higgs factories, Belle-II, ALICE, heavy ions)
 - DEPHY (high rates & high doses)
 - ✓ 1 Platform C4PI @ IPHC
- IPHC reviewed completely in fall 2022 (twice)
 - ✓ CNRS commission ⇒ very positive outcome for CMOS activities @ IPHC
 ✓ HCERES
- Scientific council of IN2P3 recently reviewed 2 projects carried by IPHC:
 - ✓ Belle-II upgrade (presented by J.B.)
 - ✓ ALICE ITS-3 upgrade Accepted by IN2P3 !
 - ⇒ Expects positive outcome
- Strasbourg prospectives for all CMOS activities @ IPHC
 - ✓ Involves C4PI and physics teams (PICSEL, Belle-2, ALICE, etc.)
 - Ongoing Q1-Q2 2023