

List of configurations taken in May 2022

Process	Pitch	Chip	High voltage	PSUB (relevant for C4 only)	Analyzed
Standard	15	A4	1	0	
Standard	15	A4	2	0	
Standard	15	A4	3	0	
Standard	15	A4	4	0	
Standard	15	A4	10	0	YES
Modified with gap	15	B4	1	0	
Modified with gap	15	B4	2	0	
Modified with gap	15	B4	3	0	
Modified with gap	15	B4	4	0	
Modified with gap	15	B4	10	0	YES
Modified	15	C4	1	0	
Modified	15	C4	2	0	
Modified	15	C4	3	0	
Modified	15	C4	4	0	
Modified	15	C4	10	0	
Modified	15	C4	0	-3	
Modified	15	C4	1	-3	
Modified	15	C4	2	-3	
Modified	15	C4	3	-3	
Modified	15	C4	4	-3	
Modified	15	C4	10	-3	
Standard	25	D4	1	0	
Standard	25	D4	2	0	
Standard	25	D4	3	0	
Standard	25	D4	4	0	
Standard	25	D4	10	0	
Standard	25	D4	15	0	

- Available results for analyzed runs:

1. Seed charge distribution
2. Cluster charge distribution
3. Noise distribution

Missing results:

1. Efficiency (due to RTS pixels)
2. Spatial resolution