

Time synchronization and clock distribution for Hyper Kamiokande Mathieu Guigue for the HK Synchronization Working Group



e HK Synchronization Working Group April 11th 2022





Hyper Kamiokande in a nutshell





Hyper-Kamiokande

Time and clock for HK — April 11th 2022









Clock and timing requirements



Correlation with external expts (J-PARC beam, TOF, SNs...) \rightarrow event time-tagging < 100 ns using GNSS antennas



Time and clock for HK — April 11th 2022



Rings reconstruction by coincidence \rightarrow time difference between PMTs < 100 ps \rightarrow constant skew after reset





PARIS







Hyper-Kamiokande

Overall scheme















Time synchronization: GNSS

Correlation local time \leftrightarrow UTC using data stream coming from Global Navigation Satellite System (GNSS)

Short-term: Rb clock more stable than GNSS Long-term: frequency of Rb clock changes (random walk) correctable using GNSS

 \rightarrow Combine free-running atomic clock and offline time correction issued by GNSS receiver \rightarrow Corrections every ~3 hours







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Time synchronization: GNSS

Correlation local time \leftrightarrow UTC using data stream coming from Global Navigation Satellite System (GNSS)

Calibration of antenna, cables & electronics in collaboration with SYRTE (Obs. Paris) against time standard (precision sub-ns)

- Rb clock+GNSS measurements at LPNHE
- Impact of the number of visible satellites
- Synchronization GNNS-Rb clock via White-Rabbit
- Common view technique performances
- Usage of multi-constellations for better precision







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Clock distribution scheme







Conditioning/fanout:

- serves as time reference point

Based on Clock-in-Data-Recovery (CDR)

First-stage clock distributors (FSD):

- exchange control and status with DAQ PC
- receive synchronous signals
- send synchronous messages to second-stage
- distributors (TDM)
- Second-stage clock distributors:
- receive and transmit clock/data from FSD to FE
- send slow control info to DAQ PC



Clock distribution prototypes

First-stage distributor prototype



First- and second-stage distributors based on similar design - Xilinx Zynq UltraScale+ (2 multi-core processors + FPGA) - 48 or 32 SFP optical transceivers (half for redundancy) - 2 GbEth links for external control and display

First prototypes being built/received and characterized









Clock distribution performances

Time domain

Received clock time



Transmitted clock time









Physics impact

Better precision for event time-tagging (~27 ns for T2K) Better jitter (<100 ps) Increased detector volume (x8 wrt SK) More intense neutrino beam from J-PARC (x3 wrt T2K)

Improved rings reconstruction

Time-Of-Flight measurements during J-PARC ν_{μ} beam

 \rightarrow improvement by up to one order of magnitude (sub-MeV limit)

Time-Of-Flight using SuperNovae explosions \rightarrow mostly improved by statistics

 \rightarrow SN1987A with 20x stats $\rightarrow m_{\nu} < 0.4 \text{ eV}$ (competitive with Katrin)

Hyper-Kamiokande

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PRD 93, 012006 (2016)



Conclusions

- Hyper-Kamiokande has a vast and rich physics program including: Long-baseline neutrino detection
- Multi-messenger astrophysics (transient and diffuse SN detection) \rightarrow Need a precise timing determination of individual events \rightarrow Need low jitter between PMTs all across the detector
- HK proposes:
- a time synchronisation system based on atomic clock and calibrated GNSS receivers
- a two-stage clock distribution system using CDR technology

Coordinated solution between France (IRFU & LPNHE) and Italy (INFN) with good synergies between boards Excellent knowledge transfer between LPNHE and SYRTE Time and clock for HK — April 11th 2022 Hyper-Kamiokande

