

# Clock distribution and time synchronization system for HK

LPNHE Neutrino group WG4 meeting — April 8th 2021

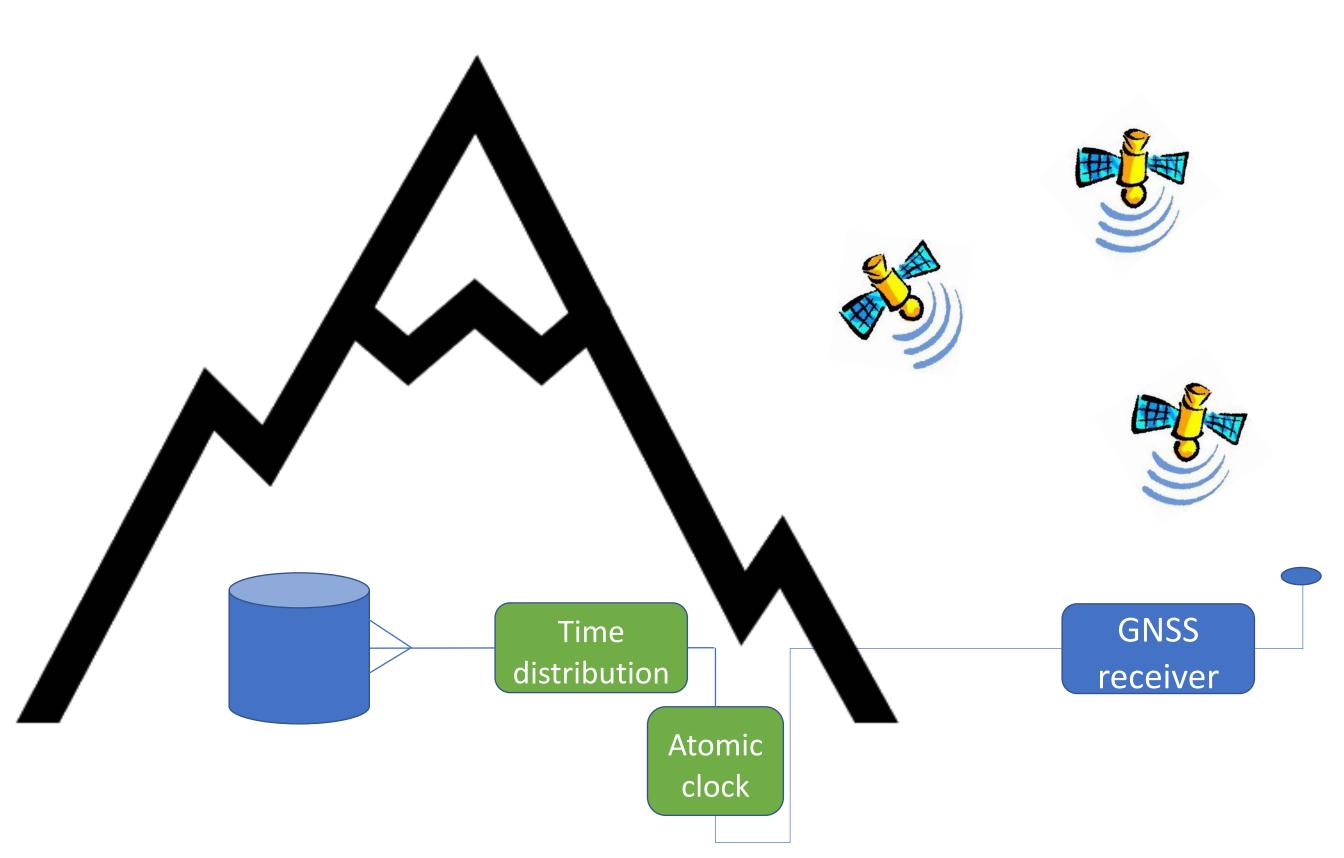






## Clock distribution system overview LPNHE





#### Major components:

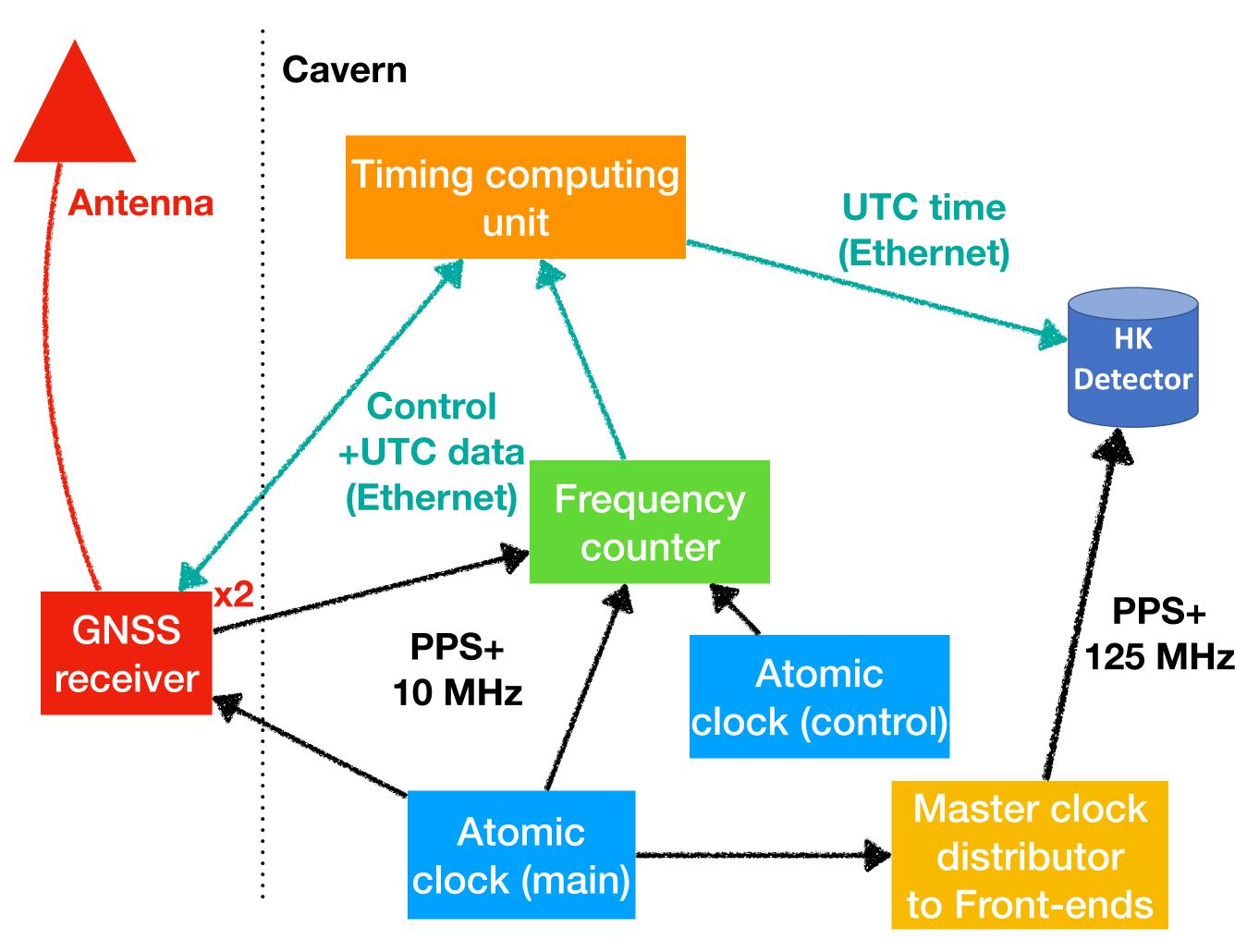
- Global Time synchronization to UTC using GNSS receiver (correction of received times)
- Synchronization with local atomic clock
- Distribution of PPS & 125 MHz frequency and commands to PMT front-ends
- Reception/treatment/storage of slow control data from PMTs





## Overall clock generation for HK





Discussions with SYRTE colleagues

Local time base defined by main clock

- Delivers PPS + 10MHz
- Control clock monitor PPS stability
- Results stored and receiver control via slow control computer

Distributor converts 10 MHz into 125 MHz before distributing to FE

- Implements triggers

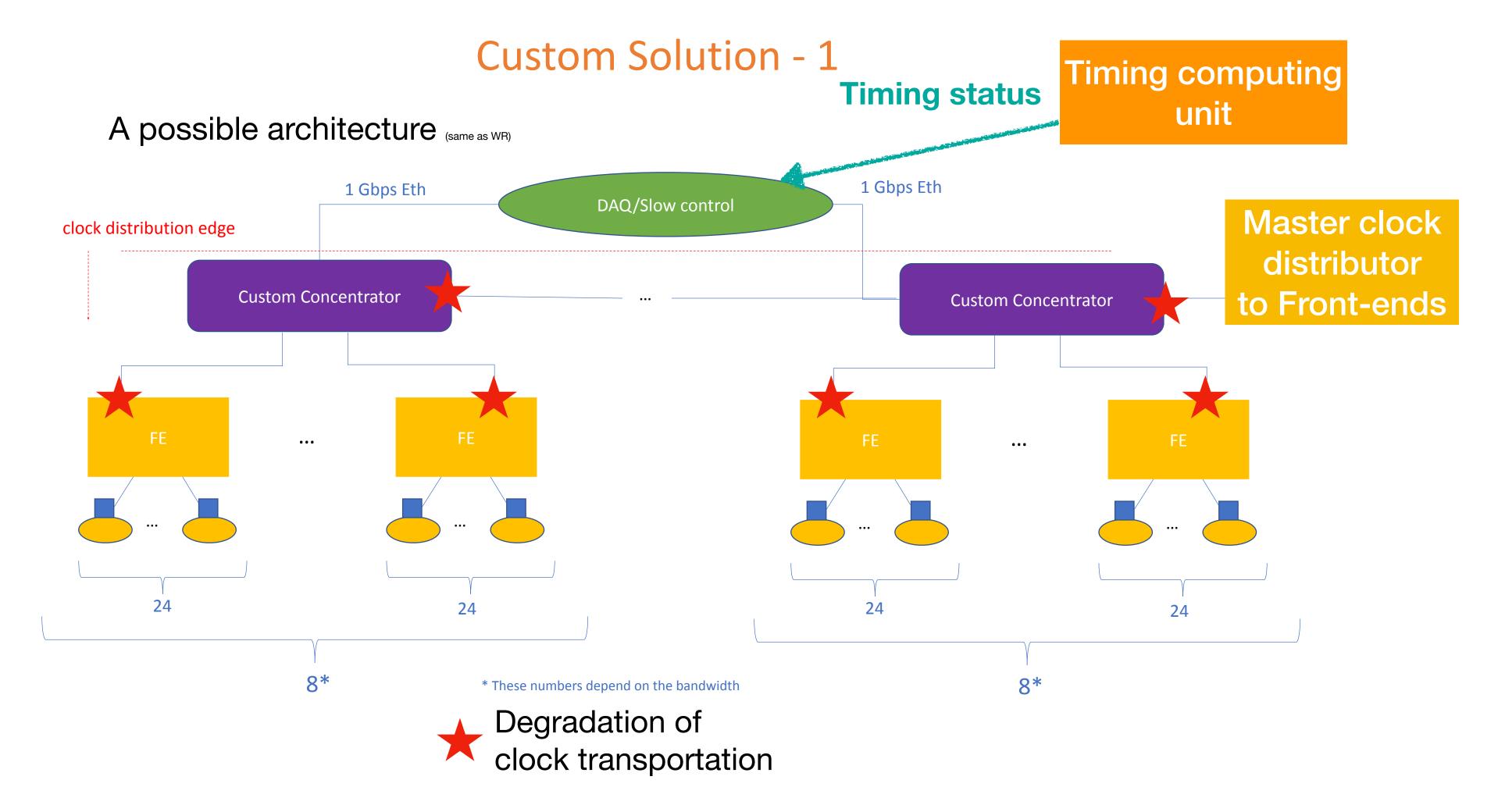
GNSS receiver generates PPS compared with main clock's

- Tag each PPS from clock with UTC time
- Backup GNSS antenna and receiver



#### Overall clock distribution for HK





- → Clock worsening as elements are daisy-chained
- → Need measuring individual and daisy-chained elements



### Clocks survey



Broad survey provided by SYRTE colleagues

- SRS FS725 (Rb Clock)
- → Off-the-shelf system used in T2K/SK
- PHM1008 (Passive Hydrogen Maser)
- → More stable at short and long times
- -> More expensive and sensitive to environment fluctuations





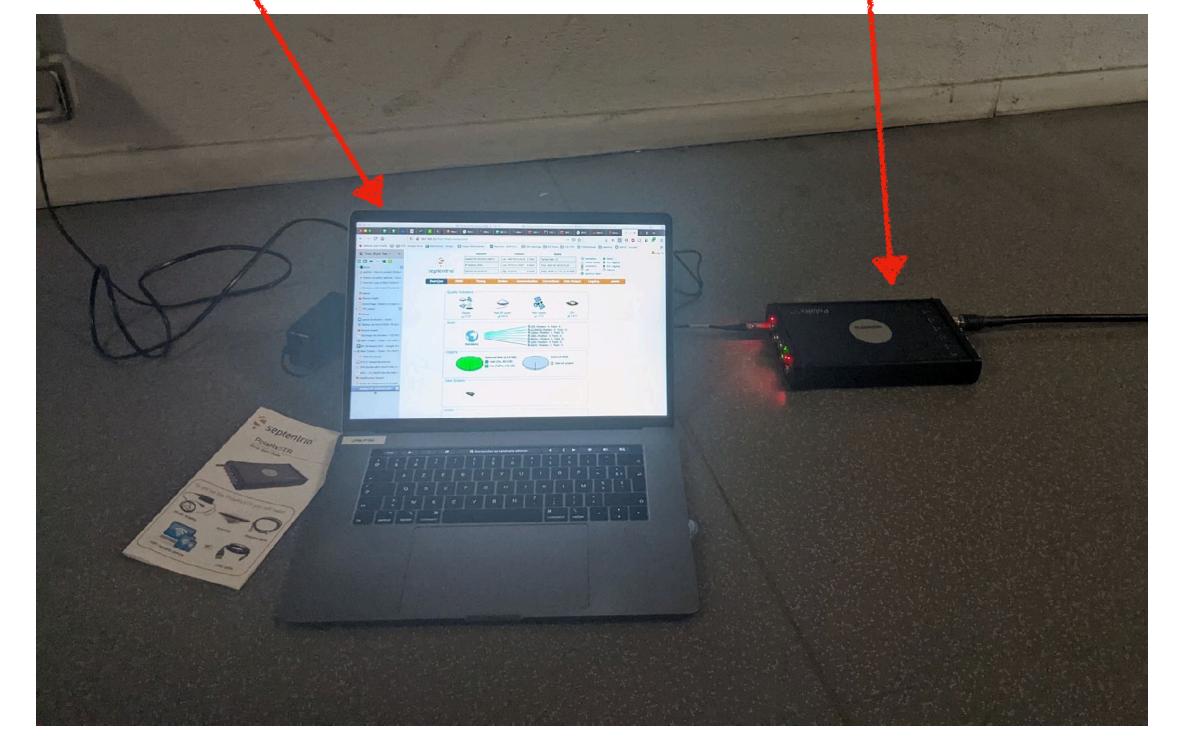
#### First test of GNSS antenna



Purchased GNSS receiver Septentrio 5<sub>Antenna</sub>

Computer connected by Wifi

Septentrio module



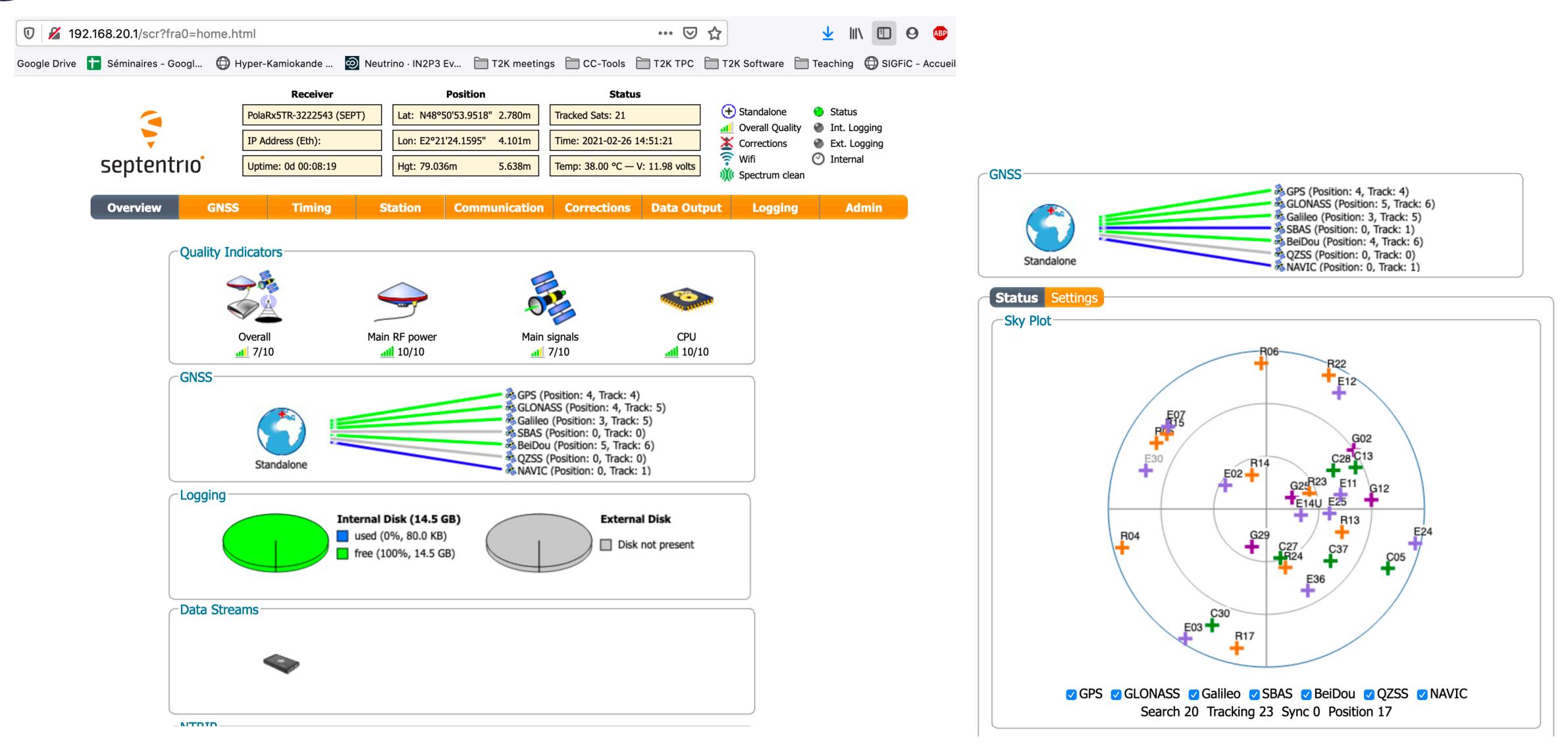






#### First results of GNSS antenna





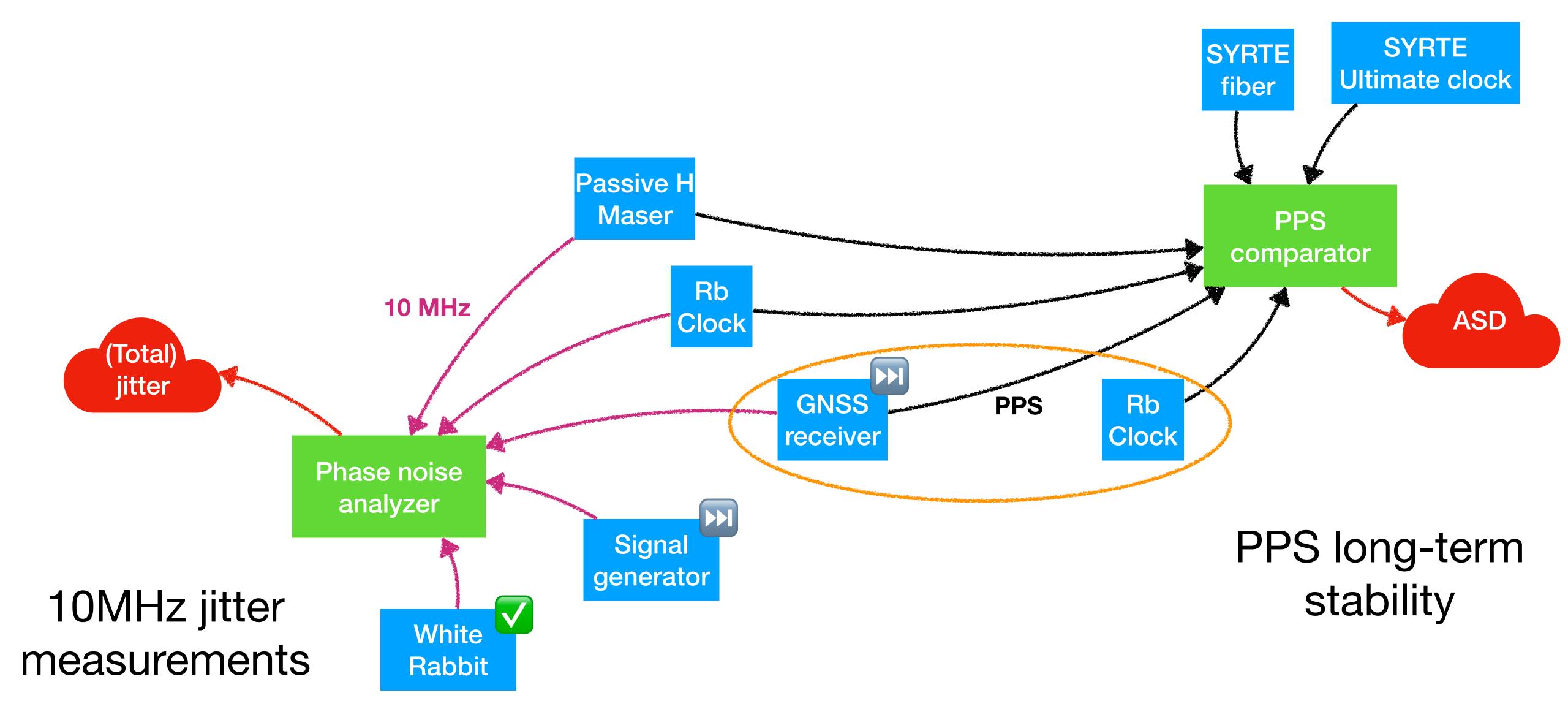






#### Next steps: measurements







### Next steps and conclusions



Jitter measured for key components using phase noise analyzer

→ well within specs (2.4 ps); ideas on how to improve it some more Measurement of entire chain in progress with UoT/INFN

Development time base generation scheme in progress Purchase GNSS + clocks

→ Preparing first tests of performances





# Backup







## Quantify time stability



Signal y(t) (usually discrete measurements  $y_i = y(t_i)$ )
How to compute stability?

→ Frequency domain: power spectral density (PSD)

$$S_{y}(\omega) = \frac{|\tilde{y}(\omega)|^{2}}{T} = \frac{\left|\int_{0}^{T} y(t) \exp(i\omega t) dt\right|^{2}}{T}$$

→ Time domain: Allan variance (AV)

$$\sigma_{\text{Allan}}(n) = \sqrt{\frac{1}{2\left(\frac{N}{n} - 1\right)} \sum_{l=1}^{\frac{N}{n} - 1} \left(y_{l+1}^{(n)} - y_{l}^{(n)}\right)^{2}} \text{ with } y_{l}^{(n)} = \frac{1}{n} \sum_{i=1}^{n} y_{ln+i}$$
(1,5)

Many variations around this formula like modified AV:

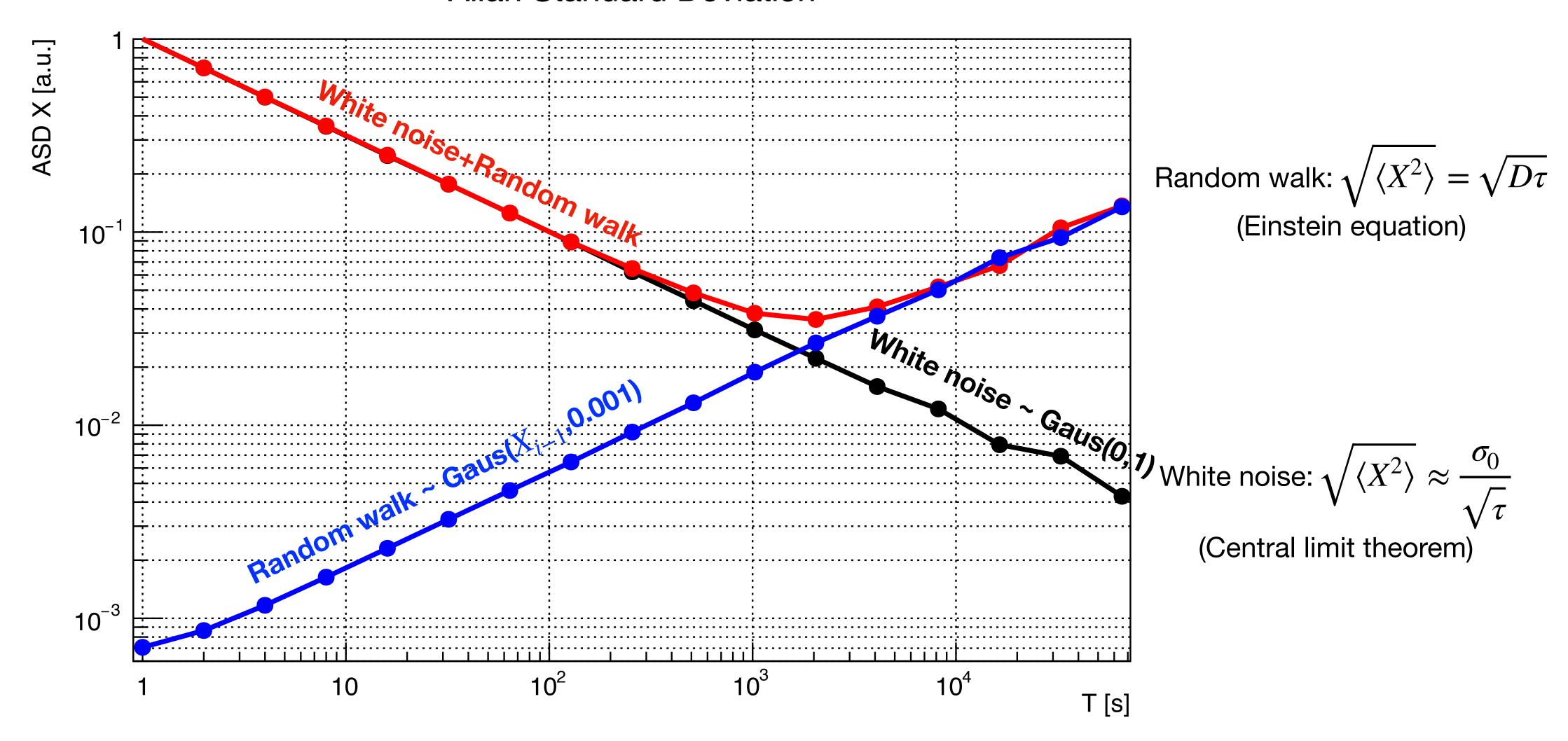
$$\sigma_{\text{Mod}}(\tau) = \sqrt{\frac{1}{2n^2(N-2n+1)}} \sum_{j=1}^{N-2n+1} \left( \sum_{i=j}^{j+n-1} y_{i+n}^{(n)} - y_i^{(n)} \right)^2$$
(3,5)



# Generating data and ASD



#### Allan Standard Deviation





**Hyper-Kamiokande** 

# Short-term stability: jitter definition LPNHE



PRS10 Single Side Band Phase Noise

Jitter: fluctuations on both amplitude, frequency and phase

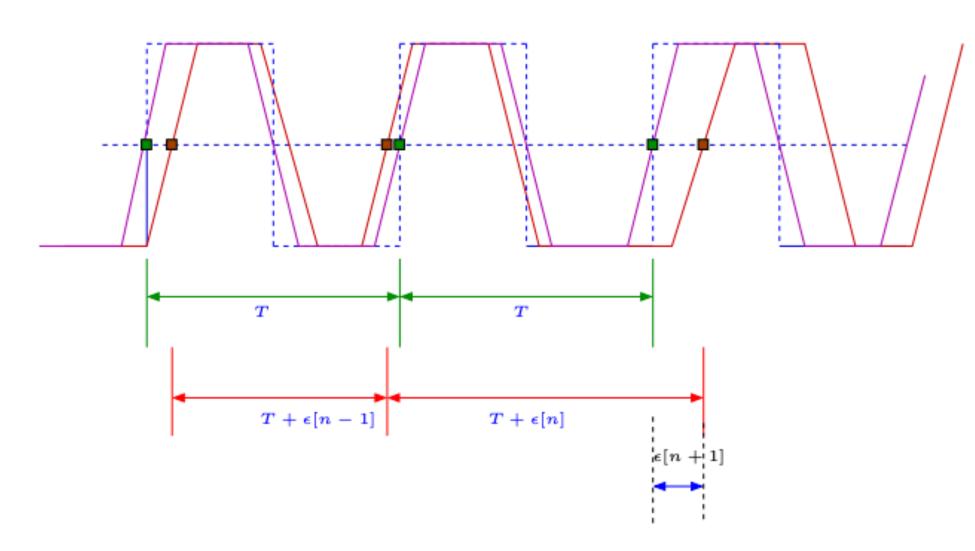


Figure 8. Sampling Clock Jitter: The time duration (period) between successive triggers vary as a result of the phase noise. Ideal clocks preserve constant period T, whereas, practical clocks vary the value randomly, leding to jitter

#### -110 -120 -150 -160 100 k Frequency Offset from Carrier (Hz)

#### Phase noise:

$$\sigma_{\phi}^{2}(\tau) = \left\langle \bar{\phi}^{2} \right\rangle = \frac{1}{\tau^{2}} \left\langle \left[ \int_{t_{k}-\tau}^{t_{k}} \phi(t) dt \right]^{2} \right\rangle$$

(RMS phase) jitter (@1 s):

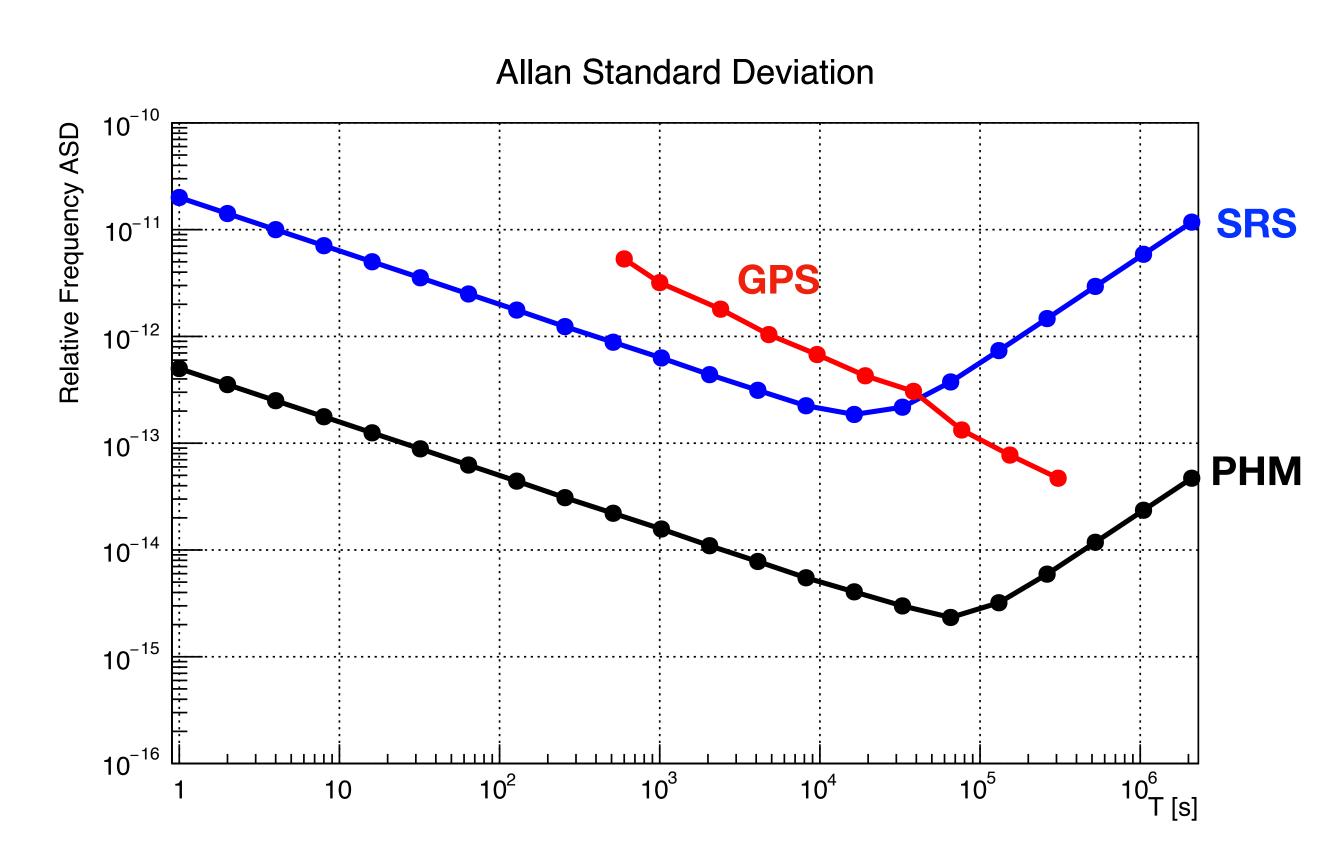
$$J = \frac{\sigma_{\phi}}{2\pi f_0} = \frac{1}{2\pi f_0} \sqrt{\int_0^{\infty} S(f) \left(\frac{\sin \pi \tau f}{\pi \tau f}\right)^2} df$$

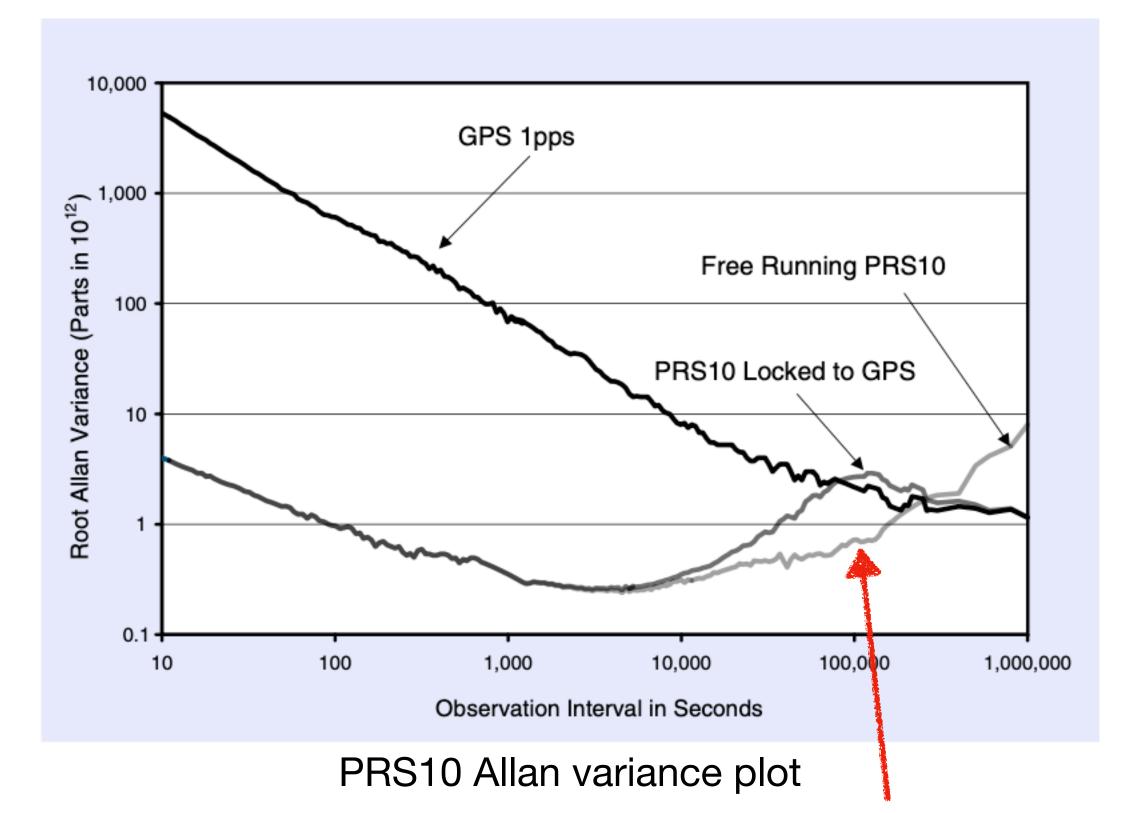
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## Locking clock PPS on GPS







Locking doesn't seem to be the best of both → why?

SYRTE recommended post-treatment time corrections





#### Biblio



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