

On-line 21th AGATA Week Virtually hosted by IJCLab Orsay

vendredi 26 mars 2021

Electronics and Data Flow session (09:00 - 14:00)

time	[id] title	presenter
09:00	[49] Electronics Introduction	GADEA, Andres LAZARUS, Ian
09:05	[50] Phase 2 electronics : introduction, mecanics and PS	GONZALEZ, Vicente
09:20	[51] Status of the new DIGIOPT12 production	PULLIA, Alberto
09:25	[52] Status of the PACE Hardware	COLLADO, Javier
09:40	[53] Status of the STARE Hardware and Firmware	KARKOUR, Nabil
09:55	[55] Report on the status of the SMART development	WITTMER, Gilles
10:10	[61] Report on the status of the GANIL Trigger Processor	BOUJRAD, Abderrahman
10:25	[63] AGATA-EDAQ V2 Preprocessing and Firrmware	CHARLES, Laurent
10:35	[62] Discussion on present and future Firmware	LAZARUS, Ian
10:50	Coffee break	
11:00	[56] Introduction to the Data Flow	KORICHI, Amel
11:05	[57] Software status	DOSME, Nicolas
11:20	[58] Hardware satus	JACOB, Jérémie LE JEANNIC, Patrick
11:35	[60] PSA computing performance	GAURON, Philippe LAFAGE, Vincent
11:50	[59] EDAQ general status and phase 2 discussion	LEGAY, Eric