

# EPDET: an SiW-ECAL optimized for $e^+e^-$ collisions

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NUCLÉAIRE  
& PARTICULES

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This project has received funding from the European Union's Horizon 2020 research and innovation programme under grant agreement No 101004761.



- 1) Technological with the production and testing of prototypes in beam (DESY and CERN)**  
Status of prototype. CALICE → DRD6
- 2) Design of a complete detector including all the constraints (performance, readout, power and cooling, and costing);**  
CaloFlux: a new tool for estimation for fluxes in calorimeters
- 3) Reconstruction software and methods adapted to this new type of devices;**
  - 1) “Cluster time measurement with CEPC calorimeter”, Eur. Phys. J. C (2023) 83:93
  - 2) ParticleFlow optimized ARBOR Covered in CEPC session by M.R.
- 4) Estimation of the physics potential using all the above (prototypes, design, and methods).**  
Covered in CEPC session by M.R.

# Highly-Granular ECAL at Higgs Factories for Particle Flow Approach based detectors

## Full Reconstruction of single particles

- Charged measured mostly from trackers
- Neutrals only measured from calorimeters

## → Large Tracker

- Precision and low  $X_0$  budget
- Pattern recognition

## → High precision on Si trackers

- Tagging of beauty and charm

## Large acceptance

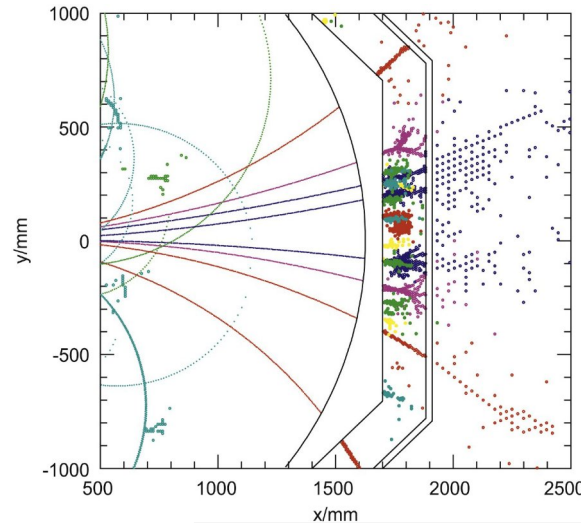
→ **Highly Granular Imaging Calorimetry + Particle Flow software**

Particle Flow Algorithms :

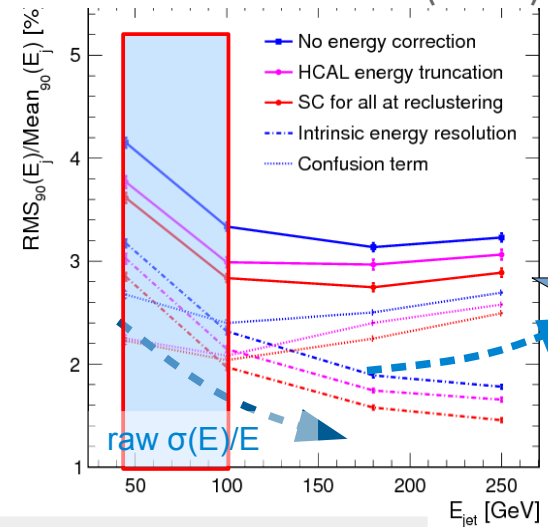
- **Jets** = 65% charged Tracks + 25%  $\gamma$  ECAL + 10%  $h^0$  ECAL+HCAL
- TPC  $\delta p/p \sim 5 \cdot 10^{-5}$ ; VTX  $\sigma_{x,y,z} \sim 10 \mu\text{m}$

ECAL

H. Videau and J. C. Brient, "Calorimetry optimised for jets," (CALOR 2002)



Pandora PFA: EPJ C77 (2017) 10, 698



Software Weighing

Confusion (cluster misattrib, merging)

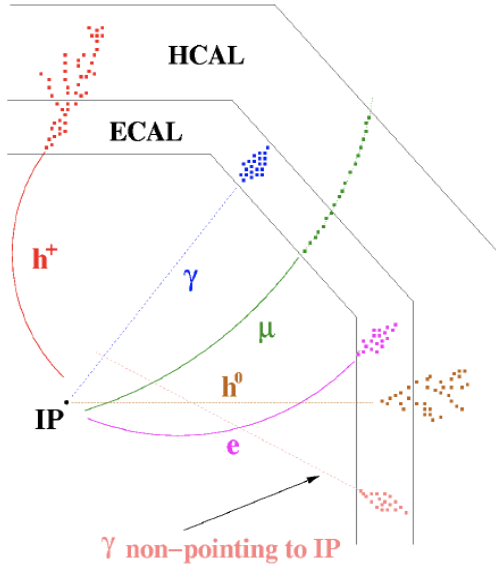
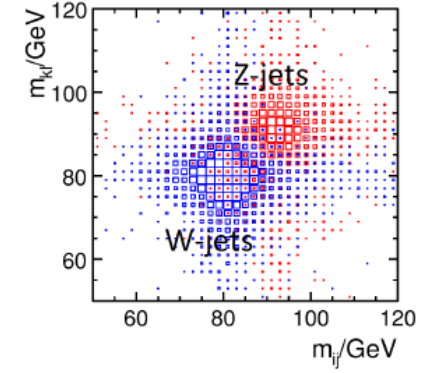
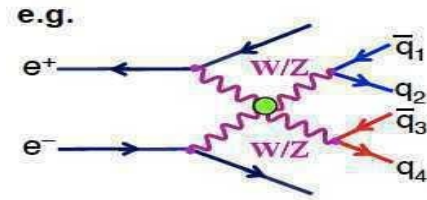
Low E jets  $\Rightarrow$  where PFA brings most

# Particle Flow Detectors at Higgs Factories

Basis: sep of  $H \rightarrow WW/ZZ \rightarrow 4j$

$$- \sigma_z/M_z \sim \sigma_w/M_w \sim 2.7\% \sigma \sim 2.75 \sigma_{sep}$$

$$\Rightarrow \sigma_E/E \text{ (jets)} < 3.8\%$$



Particle Flow ECAL should :

spot tracks & showers from charged ( $h^\pm, e^\pm$ )

→ Dynamic range from 1/3 MIP

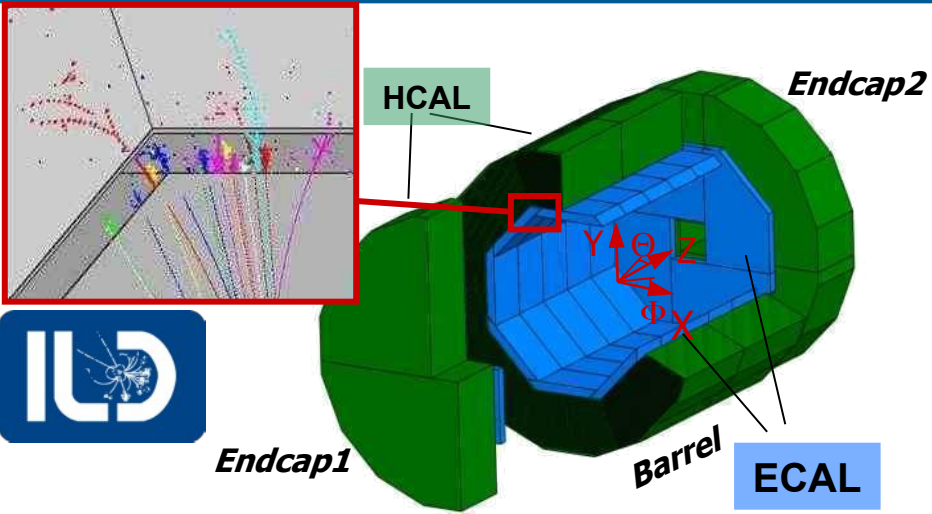
measure Photons in jets & Tau physics ( $\gamma$  vs  $\pi_0$ )

up to 3000 MIPs

measure 2/3 of neutral hadrons interacting in the ECAL

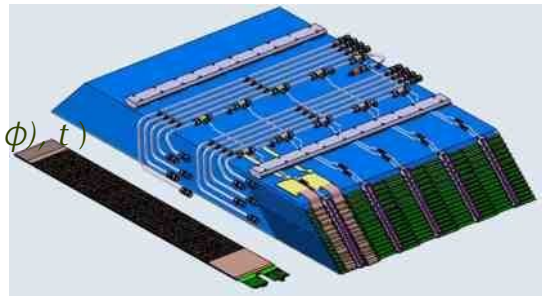
measure Time-of-Flight (10's ps)

# An Ultra-Granular SiW-ECAL for experiments



## Particle Flow optimised calorimetry

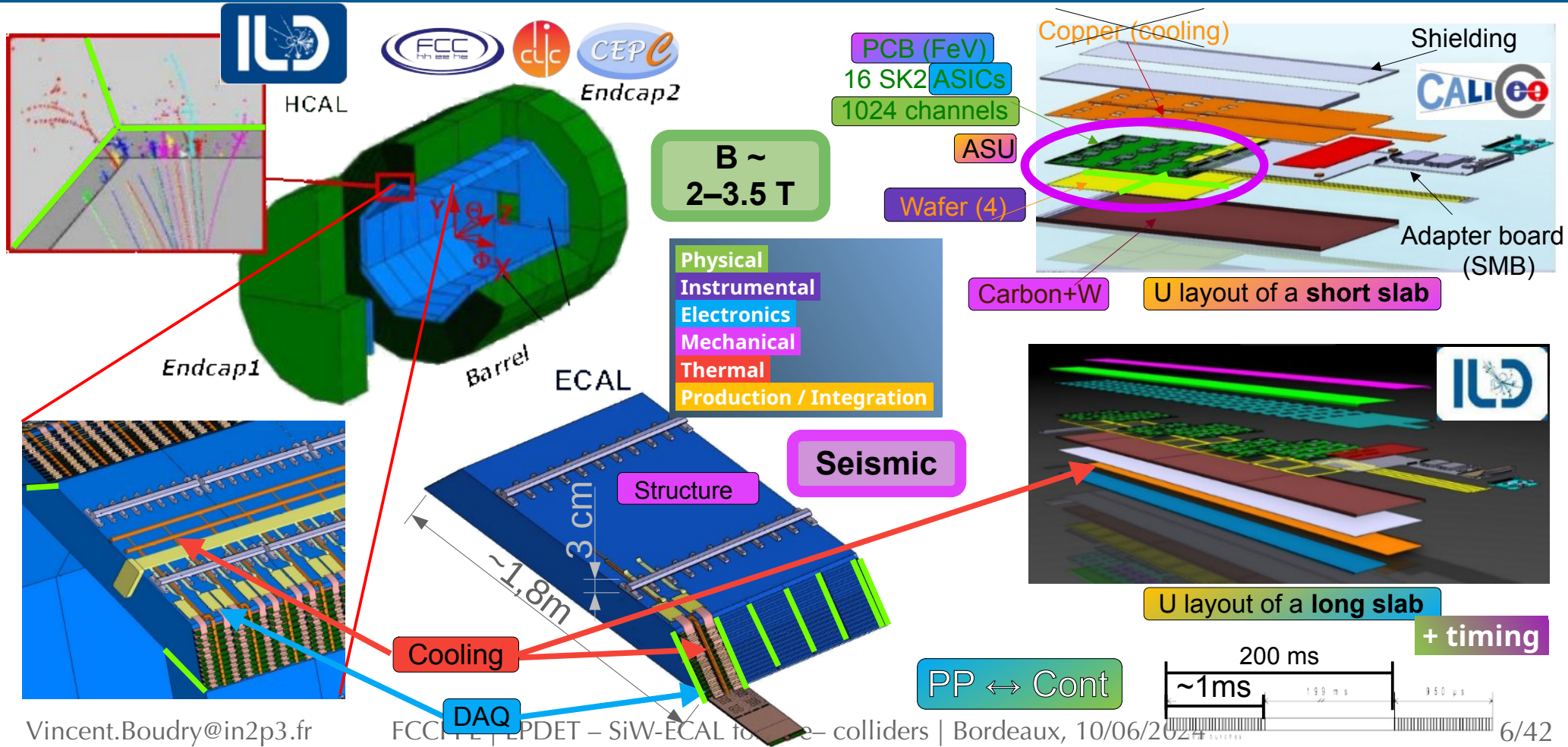
- Standard requirements
  - Hermeticity, Resolution, Uniformity & Stability ( $E, (\theta, \phi), t$ )
- PFlow requirements:
  - Extremely high granularity
  - Compacity (density)



## SiW+CFRC baseline choice for future Lepton Colliders:

- Tungsten as absorber material
  - $X_0 = 3.5 \text{ mm}, R_M = 9 \text{ mm}, \lambda_1 = 96 \text{ mm}$  To be assessed by prototypes
  - Narrow showers
  - Assures compact design
- Silicon as active material
  - Support compact design: Sensor+RO  $\leq 2\text{mm}$**  ←
  - Allows for ~any pixelisation
  - Robust technology
  - Excellent signal/noise ratio:  $\geq 10$**  ←
  - Intrinsic stability (vs environment, aging)** ←
  - Albeit expensive...**
- Tungsten–Carbon alveolar structure
  - Minimal structural dead-spaces** ←
  - Scalability** ←

# Modular & Transverse Constraints

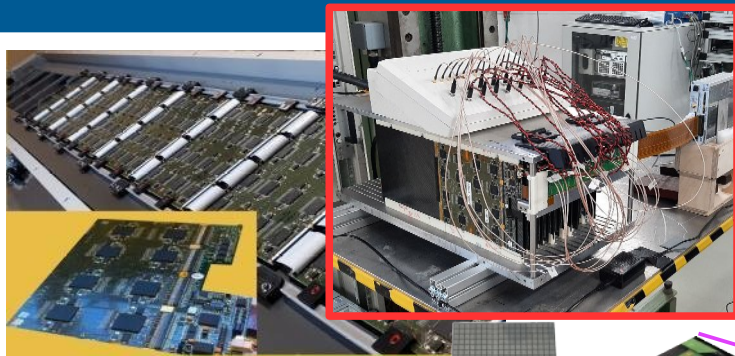
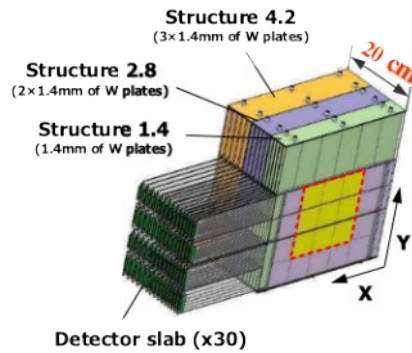


# SiW-ECAL

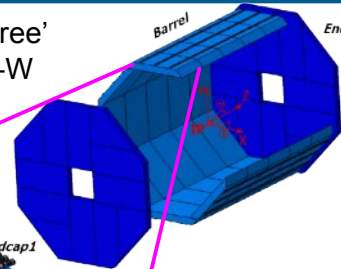


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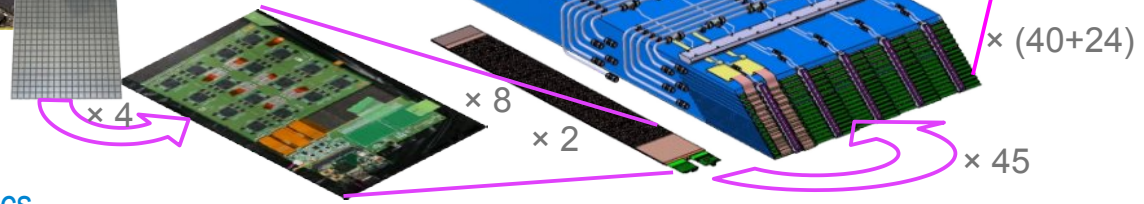
'dead space free'  
Carbon Fibre-W  
Structure



## Technological (now)

- Embedded electronics
  - Power-Pulsed, Auto-Trig, delayed RO
  - $S/N = (MPV/\sigma_{Noise}) \geq \sim 12$  (trig)
- Compatible w/ 8+ modules-slab
- $5 \times 5 \text{ mm}^2$  on  $320\text{--}650\mu\text{m}$   $9 \times 9 \text{ cm}^2$   $\times 26\text{--}30$  layers
  - 8k (slab)  $\sim$  30k (calo) channels

We are here



## Pilote

- 1M
- on  $750\mu\text{m}$   $12 \times 12 \text{ cm}^2$  8" Wafers ?
- Pre-industrial building
- Full integration ( $\supset$  cooling)
- Final ASIC

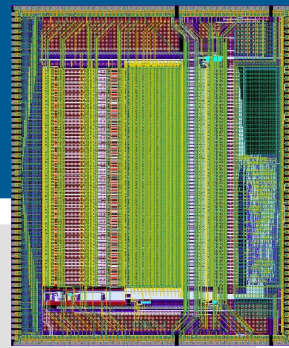
## Full Detector

70M channels

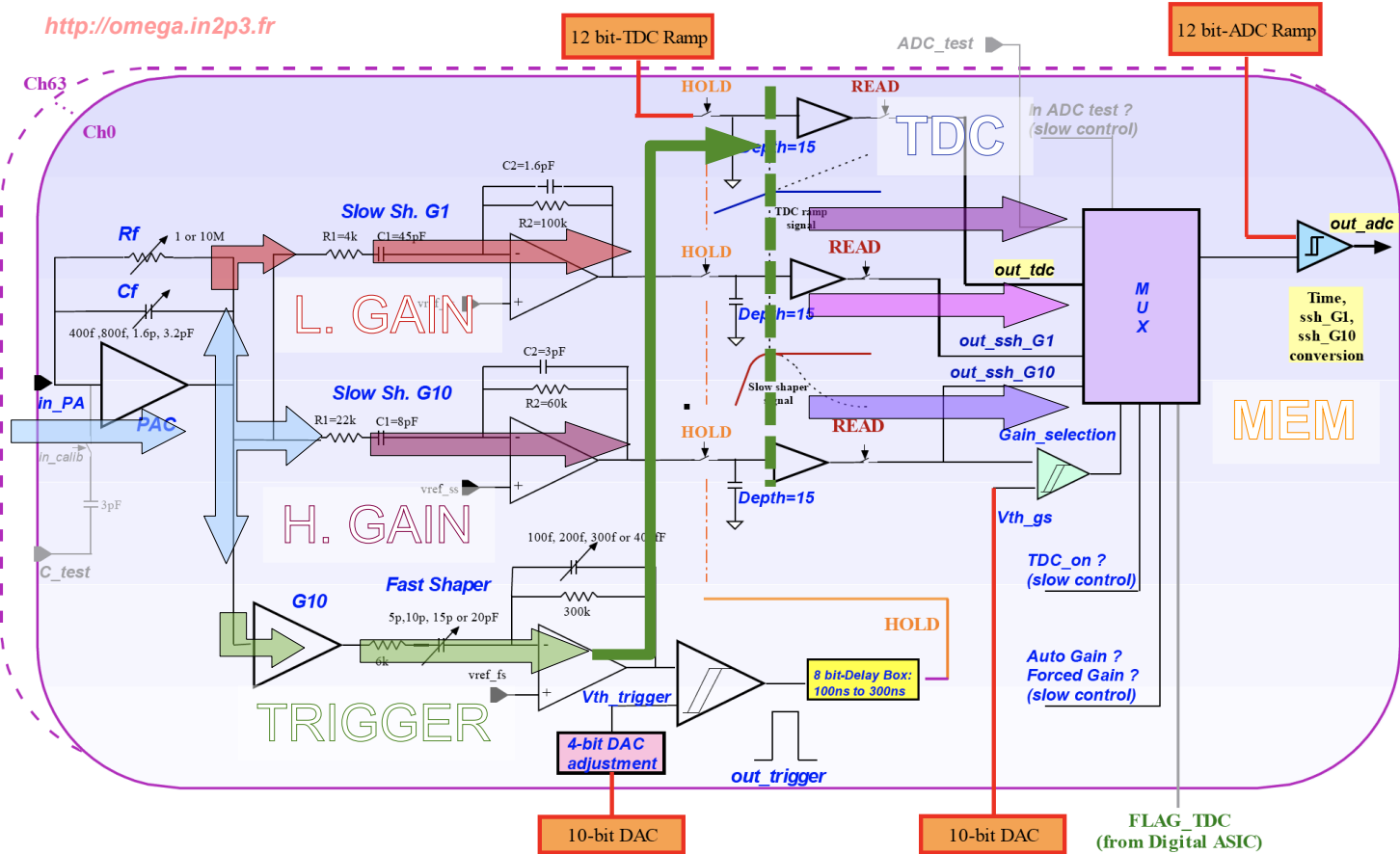
## Physical (2005-11)

- $1 \times 1 \text{ cm}^2$  on  $500\mu\text{m}$   $6 \times 6 \text{ cm}^2$  Pad glued on PCB Floating GR
- $\times 30$  layers (10k chan).
- External readout
- Proof of principle





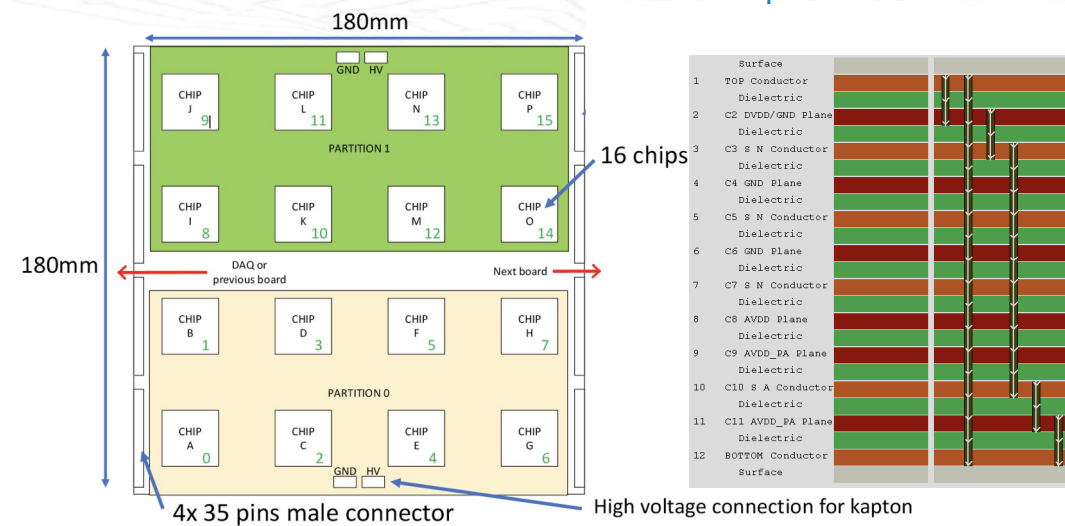
<http://omega.in2p3.fr>



- 64 channels
- Auto-triggered
  - per cell adj.
  - 1 cell triggers all
- Preamp + 2 Gains + Auto-select + TDC (~1.4ns)
- 15 (x2) analogue memories
- Dyn range 0.1 ~ 2500 mips
  - mip in 320 μm (4 fC)
  - 12 bits ADC's
- 616 config bits
- Low consumption
  - 25 μW/ch with 0.5% ILC-like duty cycle
- Power-Pulsed

## Most complex element: electrical-mechanical integration

- Powering, Distrib / Collect signals from ASICs, Analog & Digital with dyn. range  $\geq 7500$ 
  - Single End operation  $\rightarrow$  Chaining for 8–10 boards
- **Mechanical** placer & holder for Wafers  $\rightarrow \leq 50\mu\text{m}$  lateral precision, flatness
- **Thickness** constraints  $\rightarrow$  Calorimeter Compactness



Milestone	Date	Object	Details	REM
1 <sup>st</sup> ASIC proto	2007	SK1 on FEV4	36 ch, 5 SCA	proto, $\leq 2000$ mips
1 <sup>st</sup> ASIC	2009	SK2	64ch, 15 SCA	3000 mips
1 <sup>st</sup> PCB proto	2010	FEV7	8 SK2	COB
1 <sup>st</sup> working PCB	2011	FEV8	16 SK2 (1024 ch)	CIP (QGFP)
1 <sup>st</sup> working ASU in BT	2012	FEV8	4 SK2 readout (256ch)	S/N $\leq \sim 14$ (H Gain), no Power Pulsing retriggerers 50–75%
1 <sup>st</sup> run in PP	2013	FEV8-CIP		BGA, Power Pulsing
1 <sup>st</sup> full ASU	2015	FEV10	4 units on test board 1024 channel	S/N $\sim 17-18$ (H Gain) retrigger $\sim 50\%$
1 <sup>st</sup> SLABs	2016	FEV11	10 units	Noise issues
pre-calo	2017	FEV 11	7 units	S/N $\sim 20$ (12) <sub>Trig</sub> , 6–8 % masked
1 <sup>st</sup> technological ECAL	2018	FEV11, 12 13 Compact Calo Long Slab	SK2 & SK2a ( $\rightarrow$ timing) 8 ASUs	Improved S/N Timing enabling
1 <sup>st</sup> working COB, new DAQ	2019	FEV-COB	2x1/4 ASUs Cont. power.	Technical
2 <sup>nd</sup> tech ECAL	20–22	5 types FEV's	H. Gain, Cont. Power	320, 500, 650 $\mu\text{m}$

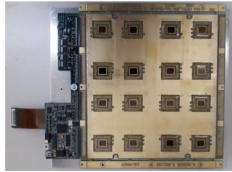
# Technological Prototype Beam test at DESY & CERN

2022 BT



**FEV10, 11, 12**

- BGA packaging
- Incremental modifications
- From v10 -> v12
- Main "Working horses" since 2014



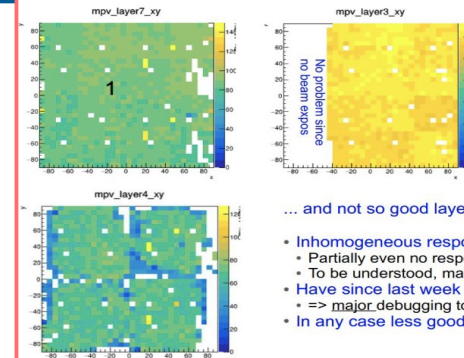
**FEV-COB**

- Chip-On-Board : ASICs wirebonded in cavities
  - Thinner than FEV with BGA
- Based on FEV11
  - External connectivity compatible



**FEV13**

- BGA packaging
  - Improved routing
  - Local power storage
  - Different external connectivity

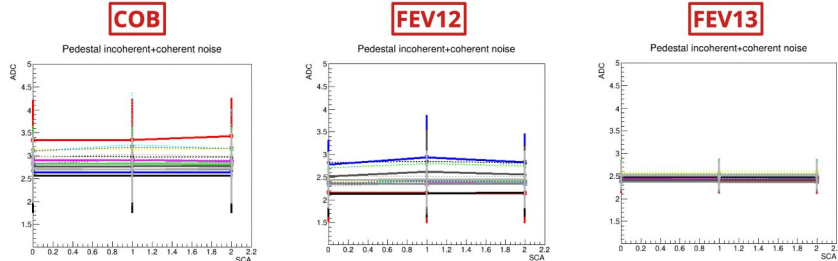


- We have good layers ...
  - Homogeneous response to MIPs over layer surface
  - Here white cells are masked cells due to PCB routing
  - Understood and will be corrected

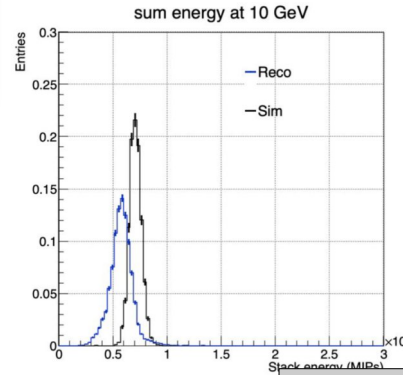
... and not so good layers

- Inhomogeneous response to MIPs
  - Partially even no response at all, in particular at the wafer boundaries
  - To be understood, may require dedicated aging studies
  - Have since last week access to the different stages of the ASICs => major debugging tool
  - In any case less good layers will be replaced in coming months

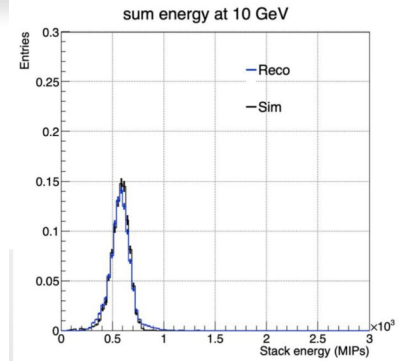
## Pedestal widths, 1<sup>st</sup> memory cells, per asic



- (Average  $\pm$  Standard Deviation) of Sigmas for all 64 channels in the same chip
- Latest PCBs, with optimized routing of power distribution shows better behavior
- Slightly larger spread on COB due to a near lack of decoupling capacitors



Masking Beam profiling



Yuichi Okugawa (PhD in Feb.)

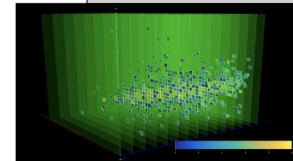


Fig. Simulation e- 100 GeV

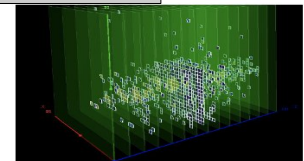


Fig. Reconstructed e- 100 GeV

# Compact DAQ readout

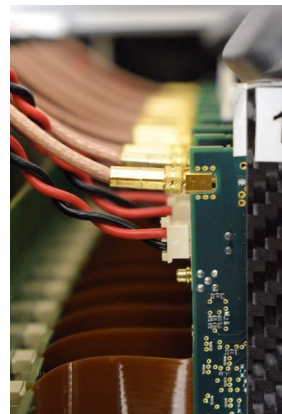
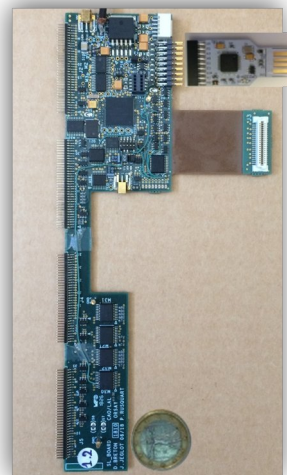
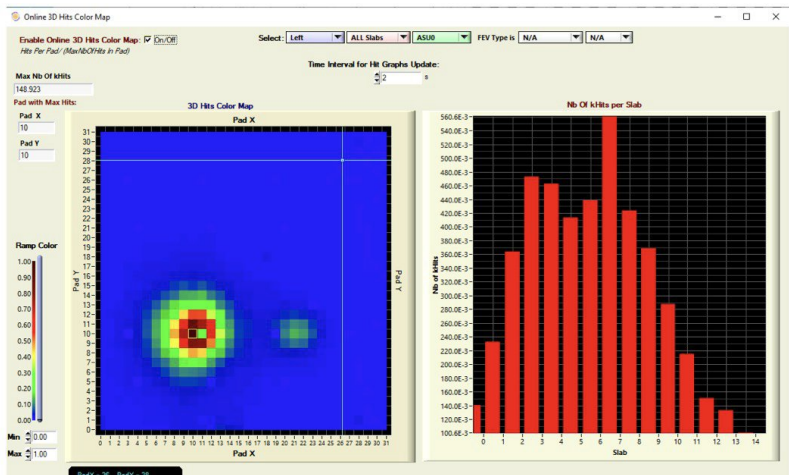
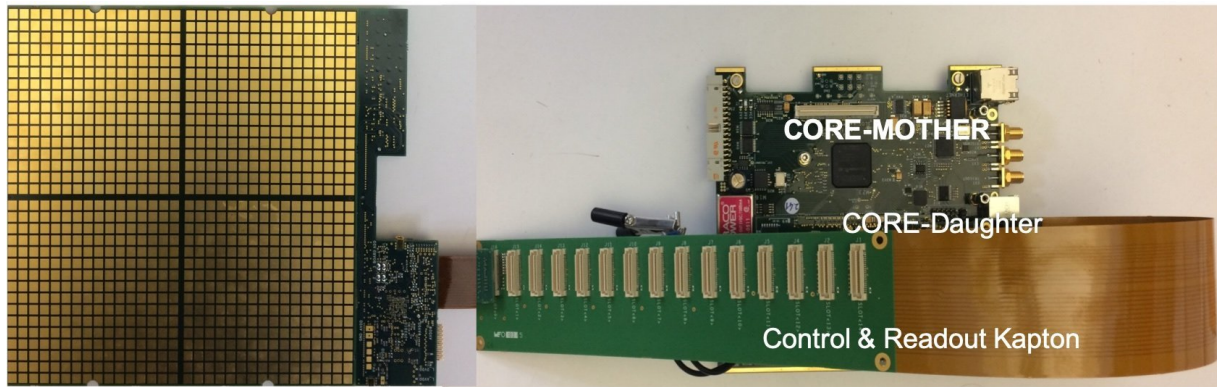
“Dead space free” granular calorimeters

→ ~ 30 mm space ECAL–HCAL

- Compact DAQ
- in use in BT since 2019

LabWindows + scriptings

- Full debug system
- ↔ EUDAQ
  - Combined running



# Acquisition software

## Written in C under Labwindows CVI

- Handle whole detector
- Two sides with 15 SLABs
- 5 ASU per SLAB

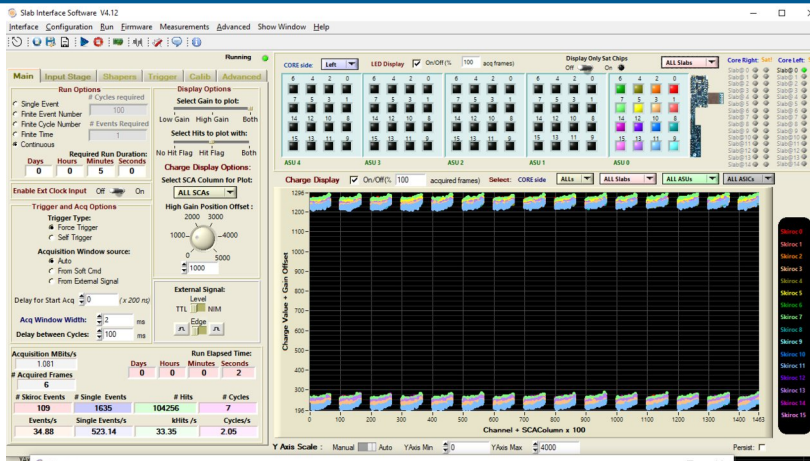
## Make advanced measurements

## Hardware automatically detected

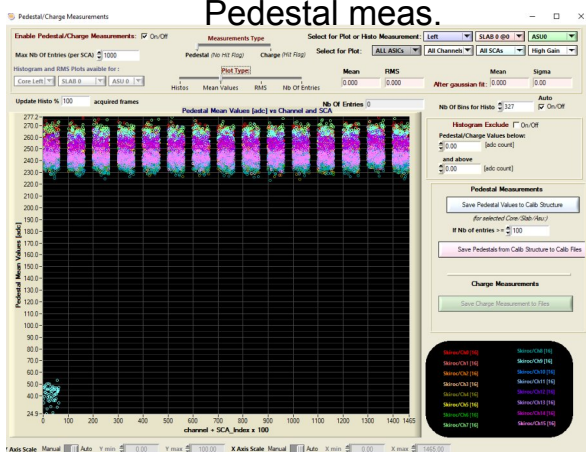
- Number of SLAB
- FEV type + number of ASU

## Slowcontrol:

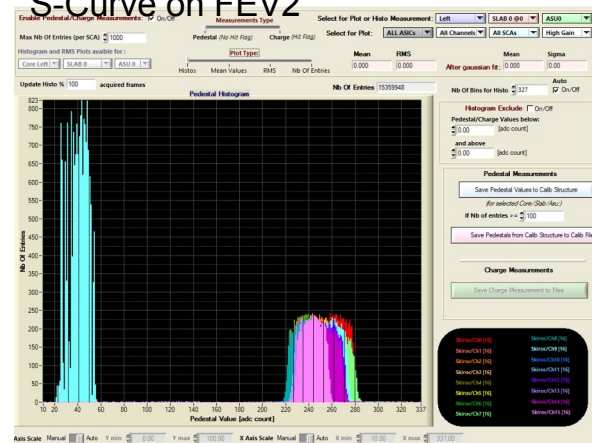
- All parameters programmable
- Integrated analysis



## Pedestal meas.

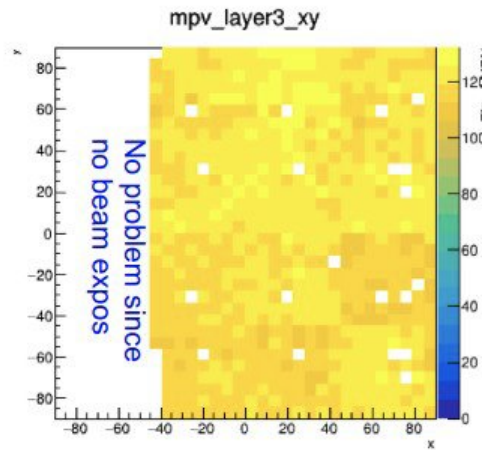
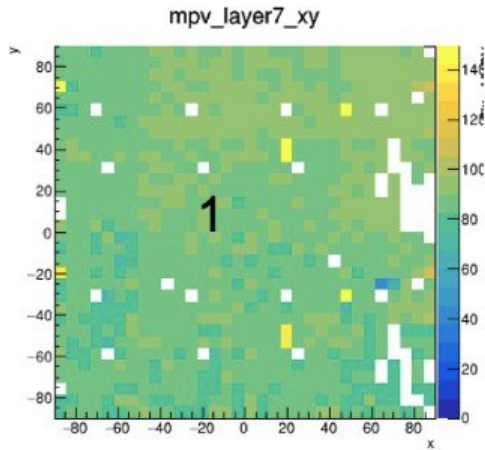


## S-Curve on FEV2

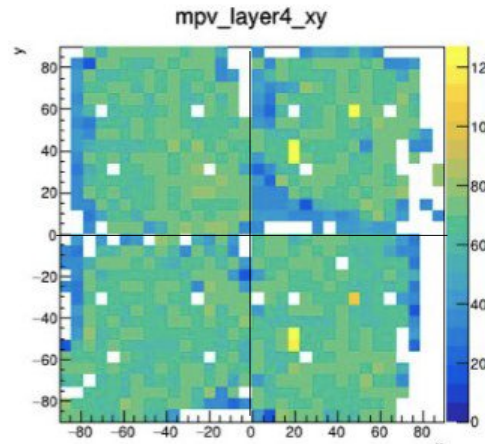


# MIP calibration

© 2019 CERN



- We have good layers ...
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  - Here white cells are masked cells due to PCB routing
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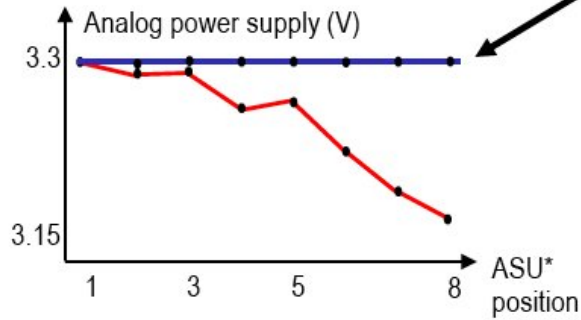


... and not so good layers

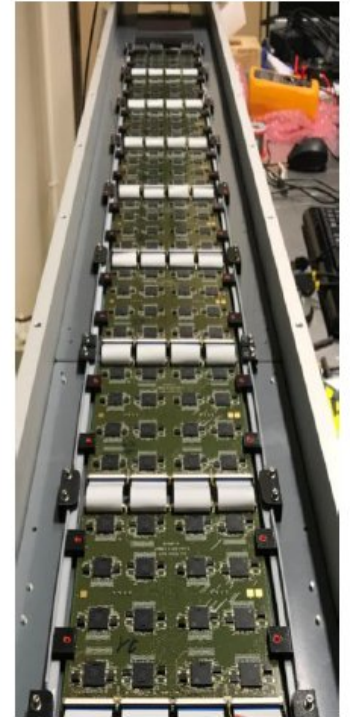
- Inhomogeneous response to MIPs
  - Partially even no response at all, in particular at the wafer boundaries
  - To be understood, may require dedicated aging studies
- Have since last week access to the different stages of the ASICs
  - => major debugging tool
- In any case less good layers will be replaced in coming months

# Power distribution dedicated for LONG SLAB

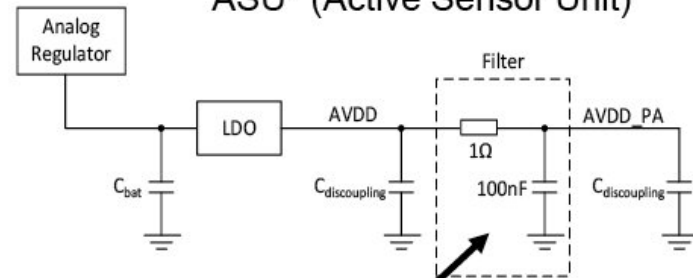
Expected results



In the electrical long SLAB, 8 boards are chained and due to resistivity of layer per board on analog 3.3V, we measure voltage drop along the long SLAB coupled with bandgap distribution.



ASU\* (Active Sensor Unit)



Add filter to generate local preamplifier power supply

→ We decide to generate local power supply with LDO (Low Drop Out) to cancel voltage drop and reduce common noise.

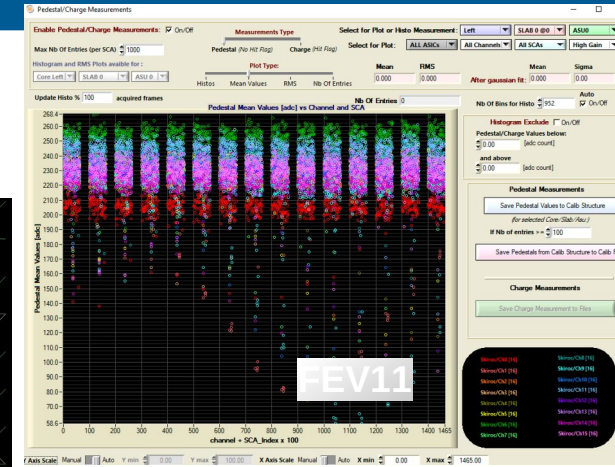
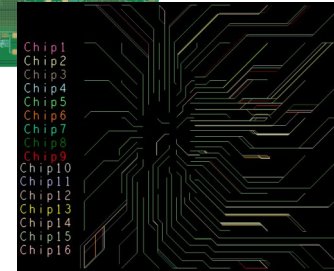
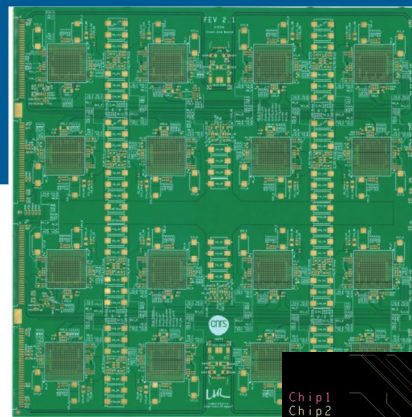
# New FE boards

## Improvements:

- Power distributions
  - Local power regulation: LDO's
  - Local High Voltage filtering & Supply
- Signal distribution (buffering), data paths
- Monitoring (single ID, temp, probe analogue line)
- ASIC shielding/routing

## Status:

- pre-version 2.0 tested, minor corrections needed
  - Noise uniformity dramatically improved (ex: outliers in thr. / 20)
- version 2.1 produced
  - before cabling, 2<sup>nd</sup> metrology, gluing, ...



Pedestal measurements vs. Ch# + Mem# x 100

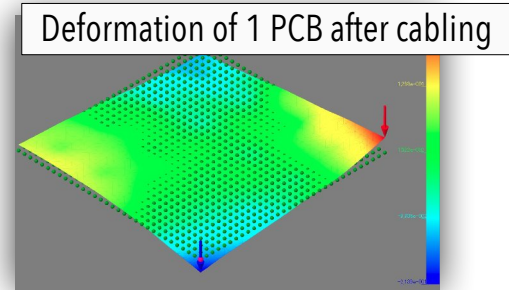




# New hardware for the SiW-ECAL

## 30 PCB of new type FEV2.1 have been produced

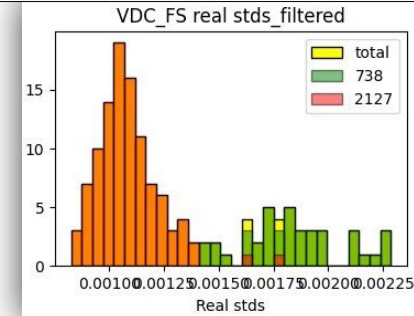
- 1<sup>st</sup> batch of 4 cabled for tests
- 1 equipped with 4 babywafers for HV test
  - Still needs adaptation of SIboard for HV supply (on-going)
- Mechanical test made at IFIC Valencia
  - Not all satisfactory (flatness  $\pm 200\mu\text{m}$  in the corners after the cabling involving heating at  $300^\circ\text{C}$ ); further investigation on the cabling process foreseen



## Testing of Skiroc2 ASICs:

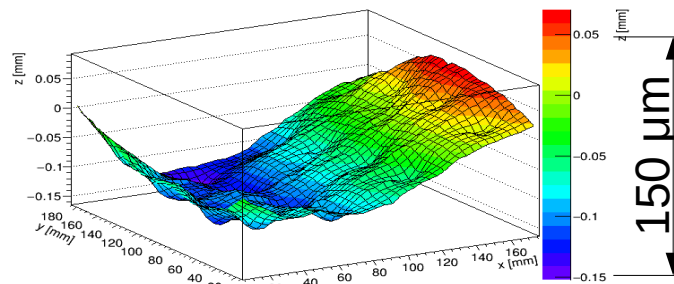
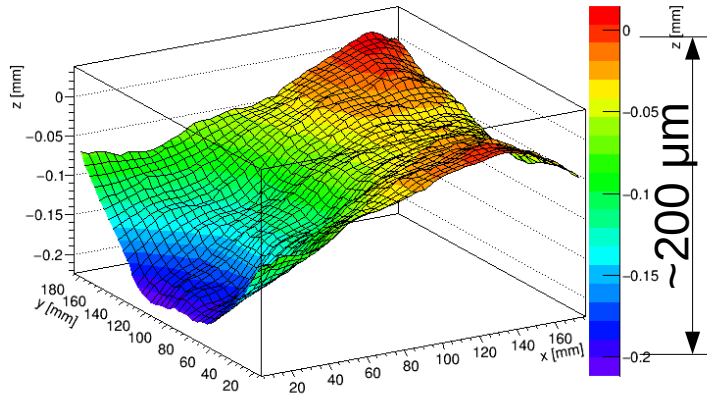
- ~ 1/3 of ASICs tested thoroughly on dedicated bench at  $\Omega$ mega lab prior to soldering on PCB's
  - Statistical analysis on-going; testing of the rest will resume soon.
- 64 (4x16) mounted on the FEV2.1
  - Performances (noises, thresholds, ... ) will be compared with bench results.

RMS of Fast Shaper for 2 type of packaging

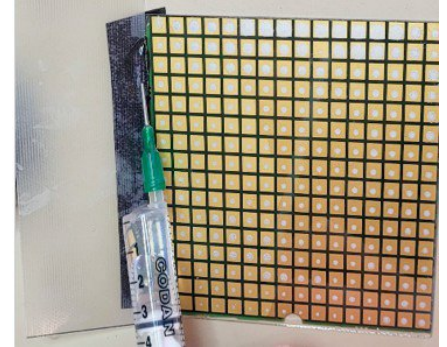


# PCB – Sensor gluing studies (on-going at Valencia & IJClab)

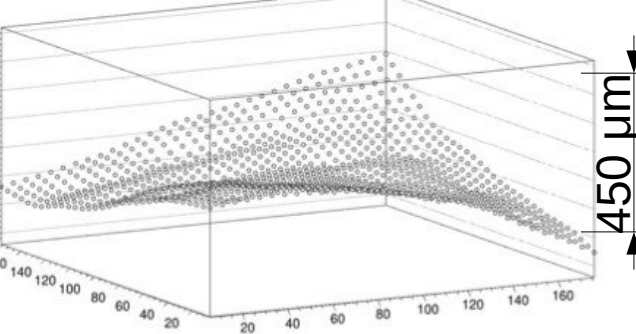
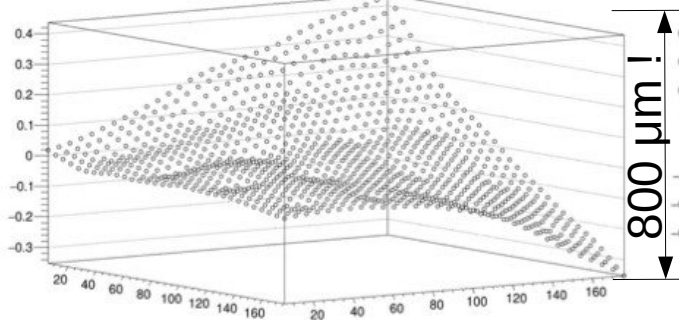
## PCB planarity @Orsay before / after component soldering



## Conductive glue dots + underfill mechanical glue



## PCB planarity Valencia before / after drying-out



## 3M conductive glue



# Beam Tests and Planning for 2024

## First CALICE/DRD6 beam tests

- Initially scheduled for June at DESY
- To be moved in Fall 2024

## Reason: careful revisitation of the gluing (hybridization) procedure:

- Deformation of the FEV under
  - Heat : expected
  - Humidity ? Not expected
- Need to understand before gluing expensive sensors on them

DRD 6: Calorimetry

Proposal Team for DRD-on-Calorimetry

November 15, 2023

Due date

### Task 1.1: Highly pixelised electromagnetic section

	Milestone	Deliverable	Description	Due date
Subtask 1.1.1: SiW ECAL		D1.1	Revised <del>15</del> layer stack	2024
	M1.1		Specifications for timing and cooling	2025
		D1.2	Engineering module for Higgs factory	>2026

# SiW-ECAL for circular EW/Higgs Factories

# Linear → Circular Collider's Conditions

## Linear (ILC, HL-ILC...)

- 250 GeV (ZH), 365 GeV (tt), 500 GeV (ZHH) + [1000 GeV],  $\mathcal{L} \sim \text{cst.}$
- Power pulsing: 5 [10–15]Hz × 1 [2] ms Power  $\sim \mathcal{L}$ .

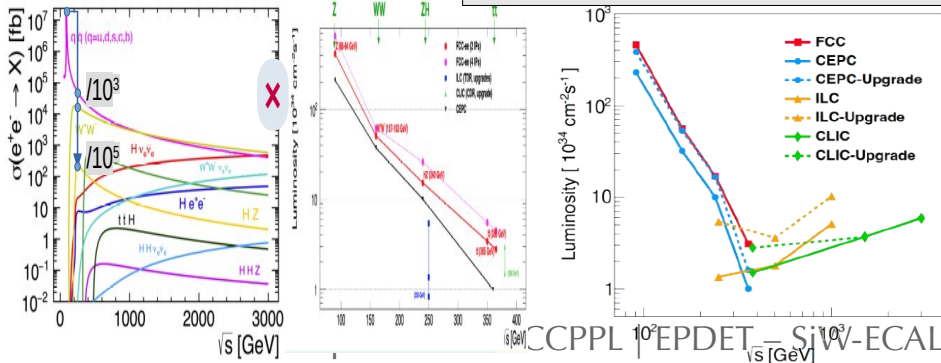
## More diverse et stringent conditions:

- 90GeV × 10<sup>7</sup> fb × 5·10<sup>36</sup> cm<sup>-2</sup> s<sup>-1</sup> (qq × 20,000 ILC @ 250)
- 150 GeV (WW) + 250 GeV (ZH)+ 365 GeV (tt)  
~10<sup>4</sup> fb × 5·10<sup>35</sup> cm<sup>-2</sup> s<sup>-1</sup> (qq × 5–10 ILC @ 250)

## From Pulsed to Continuous operation

- Power = cst + conversion+RO × local rates ( $P_{\text{Conv}}+P_{\text{RO}} \sim 40\% P_{\text{ACQ}}$ )
- ASIC, Power/Cooling, DAQ, Granularity, Precisions (E, t), New ideas...

Status of the CEPC, October 2022 J. Guimarães da Costa



## HL-ILC:

- $\mathcal{L} \times 4(6)$
- $N_{\text{bunches}} \times 2$ :  $\tau_{\text{Train}}: 1 \rightarrow 2$  ms
- $f_{\text{rep}} \times 2(3)$ : 5 → 15 Hz

Dominated by ACQ time:

$$P(\sim 25\mu\text{W}/\text{ch}) \times 6$$

## HL-CLIC:

- $\mathcal{L} \times 2$
- $N_{\text{bunches}} \rightarrow$ :  $\tau_{\text{Train}}: 176$  ns
- $f_{\text{rep}} \times 2$ : 50 → 100 Hz

Dominated by Set-up &

$$\text{Conversion time: } P(\sim 82\mu\text{W}/\text{ch}) \times 2$$

FCC-ee parameters		Z	W*W'	ZH	ttbar
$\sqrt{s}$	GeV	91.2	160	240	350-365
Luminosity / IP	10 <sup>34</sup> cm <sup>-2</sup> s <sup>-1</sup>	230	28	8.5	1.7
Bunch spacing	ns	19.6	163	994	3000
"Physics" cross section	pb	35,000	10	0.2	0.5
Total cross section (Z)	pb	40,000	30	10	8
Event rate	Hz	92,000	8.4	1	0.1
"Pile up" parameter [μ]	10 <sup>-6</sup>	1,800	1	1	1

## Experimentally, Z pole most challenging

- Extremely large statistics
- Physics event rates up to 100 kHz
- Bunch spacing at 20 ns
  - "Continuous" beams, no bunch trains, no power pulsing
- No pileup, no underlying event ...
  - ...well, pileup of 2 × 10<sup>-3</sup> at Z pole

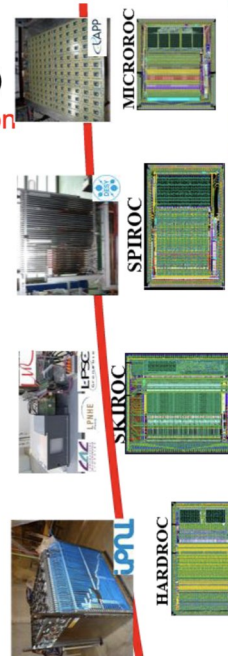
<https://indico.cern.ch/event/1064327/contributions/4893208/>  
Mogens Dam @ FCC Week, 10/06/2022

# New ASIC:

## DRD6 Common readout ASICs proposal [AGH, Omega, Saclay]



- Develop readout ASIC family for DRD6 prototype characterization
  - Inspired from CALICE SKIROC/SPIROC/HARDROC/MICROROC family
  - Targeting future experiments as mentioned in ICFA document (EIC, FCC, ILC, CEPC...)
  - Addressing **embedded electronics** and detector/electronics coexistence + **joint optimization**
  - Detector specific front-end but **common backend**
  - ⇒ allows common DAQ and facilitates combined testbeam
- Start from HGCROC / HKROC : Si and SiPM
  - **Reduce power** from 15 mW/ch to few mW/ch
  - Allows better granularity or LAr operation
  - Extend to LAr (cryogenic operation) and MCPs (PID)
  - Remove HL-LHC-specific digital part and provide flexible **auto-triggered** data payload
  - Several improvements foreseen in the VFE and digitization parts
- Several other ASICs R/Os also developed in DRD6 and it is good !
  - FLAME/FLAXE, FATIC...
  - Waveform samplers : commercial or specific (e.g. SPIDER)
  - DECAL



CdLT : future chips DRD1 10 jul 23

8

## Low Power

- Enough ?
- If Timing ?

## Low occupancy

- Self-trigger
- Less memory
  - if continuous readout

## Optimized dynamic range (silicon)

See Christophe's presentation & Disc on Thursday

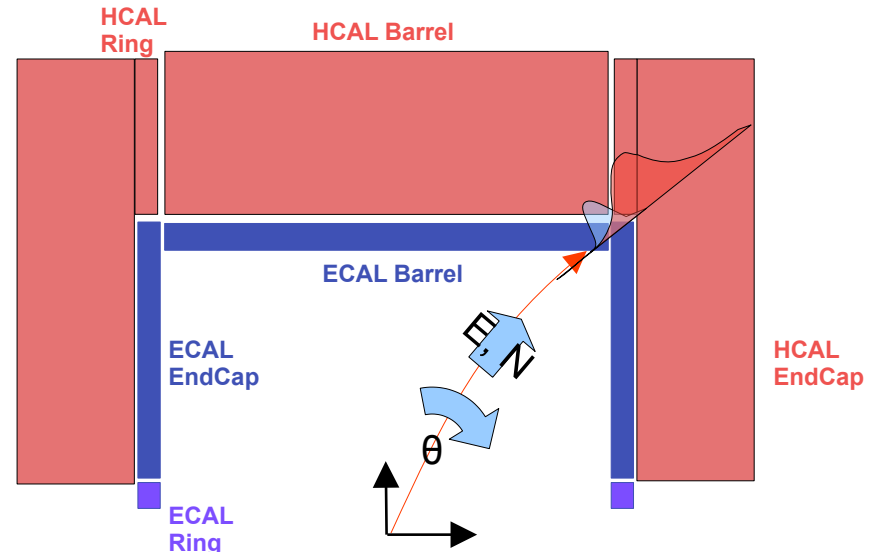
# Calorimeter Fluxes from Full Simulations: CaloFlux

## Quantities useful for self-triggering, low occupancy, Front-End electronics & Design

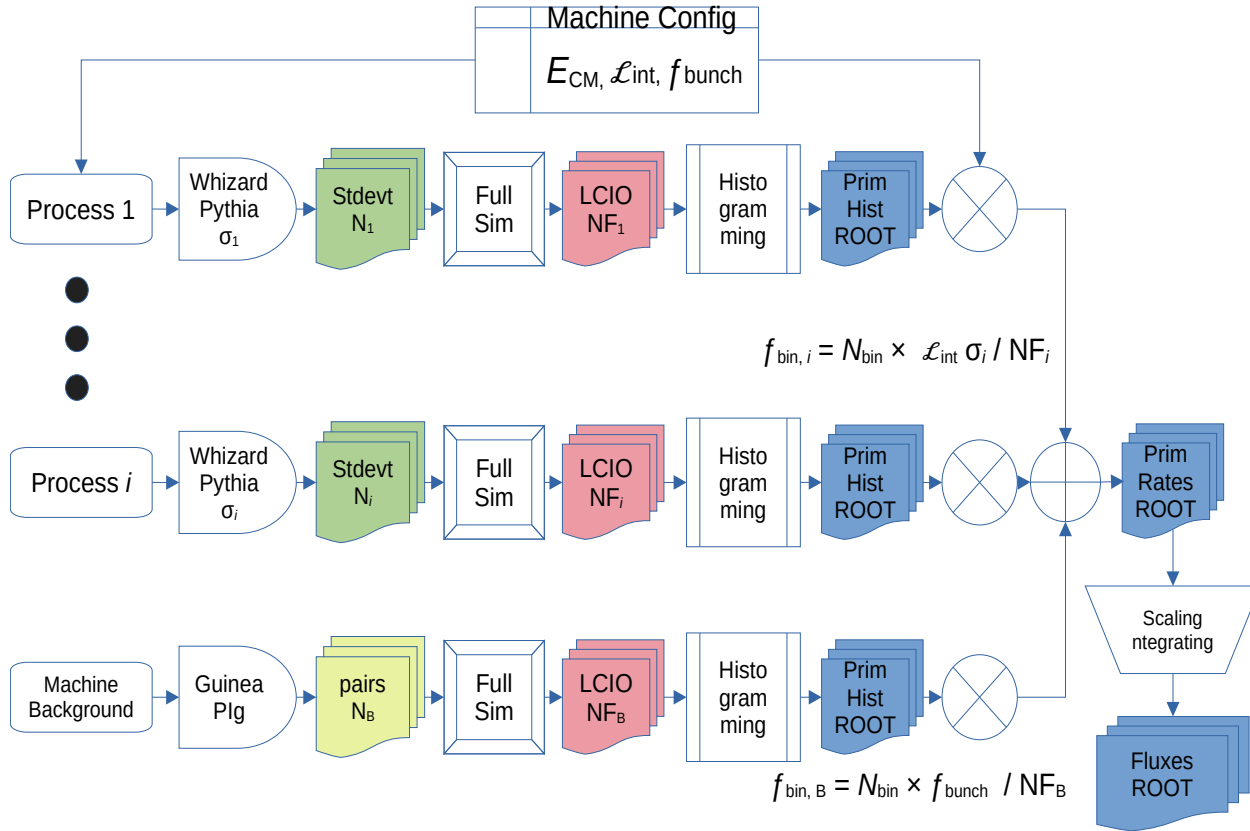
- Number of hits/s per ASICs
  - Power (Energy per conversion)
  - Memory size
- Distribution of Energy & Time
  - Dynamic ranges
  - Power per conversion
  - Double hits
- Data output
  - Data Flux per readout partition (DAQ)
  - DAQ scheme (Calo trigger to other parts ?)

## Other quantities

- Deposited energies
  - Radiation



# Processes to Fluxes



**Table 2:** 91.2 GeV  
( $N = 10000, \mathcal{L}_{int.} = 1.4 \times 10^{-3} \text{ fb}^{-1} \text{ s}^{-1}$ )

Channels	$\sigma$ ( $10^5 \text{ fb}$ )	$\sigma \times \mathcal{L}_{int.}/N$ ( $\text{s}^{-1}$ )
$e^+e^- \rightarrow q\bar{q}$	344	4.82
$e^+e^- \rightarrow \ell\bar{\ell}$	34.6	0.484
$e^+e^- \rightarrow \ell\bar{\ell}$	1.01	0.0141
( $M_{ee} < 30 \text{ GeV}$ )		
$e^+e^- \rightarrow \ell\bar{\ell}$	57.8	0.809
( $M_{ee} > 30 \text{ GeV}$ )		

**Table 3:** 162.5 GeV  
( $N = 10000, \mathcal{L}_{int.} = 2.14 \times 10^{-4} \text{ fb}^{-1} \text{ s}^{-1}$ )

Channels	$\sigma$ ( $10^5 \text{ fb}$ )	$\sigma \times \mathcal{L}_{int.}/N$ ( $\text{s}^{-1}$ )
$e^+e^- \rightarrow q\bar{q}$	1.55	$3.32 \times 10^{-3}$
$e^+e^- \rightarrow \ell\bar{\ell}$	0.241	$5.16 \times 10^{-4}$
$e^+e^- \rightarrow W^+W^-$	0.0504	$1.08 \times 10^{-4}$
$e^+e^- \rightarrow \ell\bar{\ell}$	0.240	$5.14 \times 10^{-4}$
( $M_{ee} < 30 \text{ GeV}$ )		
$e^+e^- \rightarrow \ell\bar{\ell}$	12.9	$2.76 \times 10^{-2}$
( $M_{ee} > 30 \text{ GeV}$ )		

**Table 4:** 240 GeV  
( $N = 10000, \mathcal{L}_{int.} = 6.9 \times 10^{-5} \text{ fb}^{-1} \text{ s}^{-1}$ )

Channels	$\sigma$ ( $10^5 \text{ fb}$ )	$\sigma \times \mathcal{L}_{int.}/N$ ( $\text{s}^{-1}$ )
$e^+e^- \rightarrow q\bar{q}$	0.550	$3.80 \times 10^{-4}$
$e^+e^- \rightarrow \ell\bar{\ell}$	0.100	$6.88 \times 10^{-5}$
$e^+e^- \rightarrow W^+W^-$	0.167	$1.15 \times 10^{-4}$
$e^+e^- \rightarrow ZH$	0.00204	$1.41 \times 10^{-6}$
$e^+e^- \rightarrow \ell\bar{\ell}$	0.120	$8.29 \times 10^{-5}$
( $M_{ee} < 30 \text{ GeV}$ )		
$e^+e^- \rightarrow \ell\bar{\ell}$	5.92	$4.09 \times 10^{-3}$
( $M_{ee} > 30 \text{ GeV}$ )		

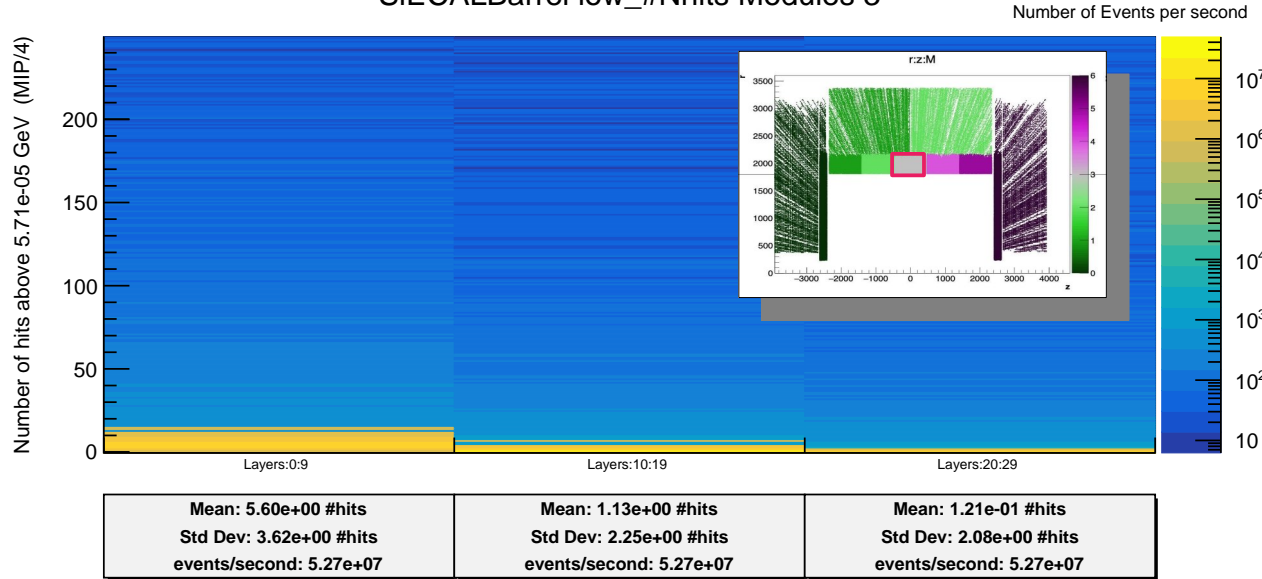
**Table 5:** 365 GeV  
( $N = 10000, \mathcal{L}_{int.} = 1.2 \times 10^{-5} \text{ fb}^{-1} \text{ s}^{-1}$ )

Channels	$\sigma$ ( $10^5 \text{ fb}$ )	$\sigma \times \mathcal{L}_{int.}/N$ ( $\text{s}^{-1}$ )
$e^+e^- \rightarrow q\bar{q}$	0.228	$2.74 \times 10^{-5}$
$e^+e^- \rightarrow \ell\bar{\ell}$	0.0430	$5.16 \times 10^{-6}$
$e^+e^- \rightarrow W^+W^-$	0.111	$1.33 \times 10^{-5}$
$e^+e^- \rightarrow ZH$	0.00123	$1.47 \times 10^{-7}$
$e^+e^- \rightarrow t\bar{t}$	0.00372	$4.46 \times 10^{-7}$
$e^+e^- \rightarrow \ell\bar{\ell}$	0.0499	$5.99 \times 10^{-6}$
( $M_{ee} < 30 \text{ GeV}$ )		
$e^+e^- \rightarrow \ell\bar{\ell}$	2.58	$4.65 \times 10^{-6}$
( $M_{ee} > 30 \text{ GeV}$ )		



# Results : Silicon ECAL Barrel, Central Module vs depth

SiECALBarrel low\_#Nhits Modules 3



Distributions of the number of hits crossing (MIP/4) energy threshold of all the physics processes and machine background at 91.2 GeV (FCC-Z4)

The  $z$  scale is the number of event/s

- Most of the hits are in the first 2 thirds of the calorimeter.
- Highest average rates L0:9
- Highest max rates in L10:19

From the  $\langle f_{Nhits} \rangle$  in one region one can extract :

- The data rate, knowing the number of bytes per hits (here 6 as a landmark)
- The occupancy, knowing the number of cell in the region.

Average	302E+6 hits/s	65E+6 hits/s	8E+6 hits/s
Max	2000 hits/event	2500 hits/event	1000 hits/event
for 6B/hits	2,111E+9 B/s	458E+6 B/s	54E+6 B/s
Ncells	4 026 764	3 767 273	3 378 036
Occupancy/BX	1,4E-06	3,3E-07	4,3E-08

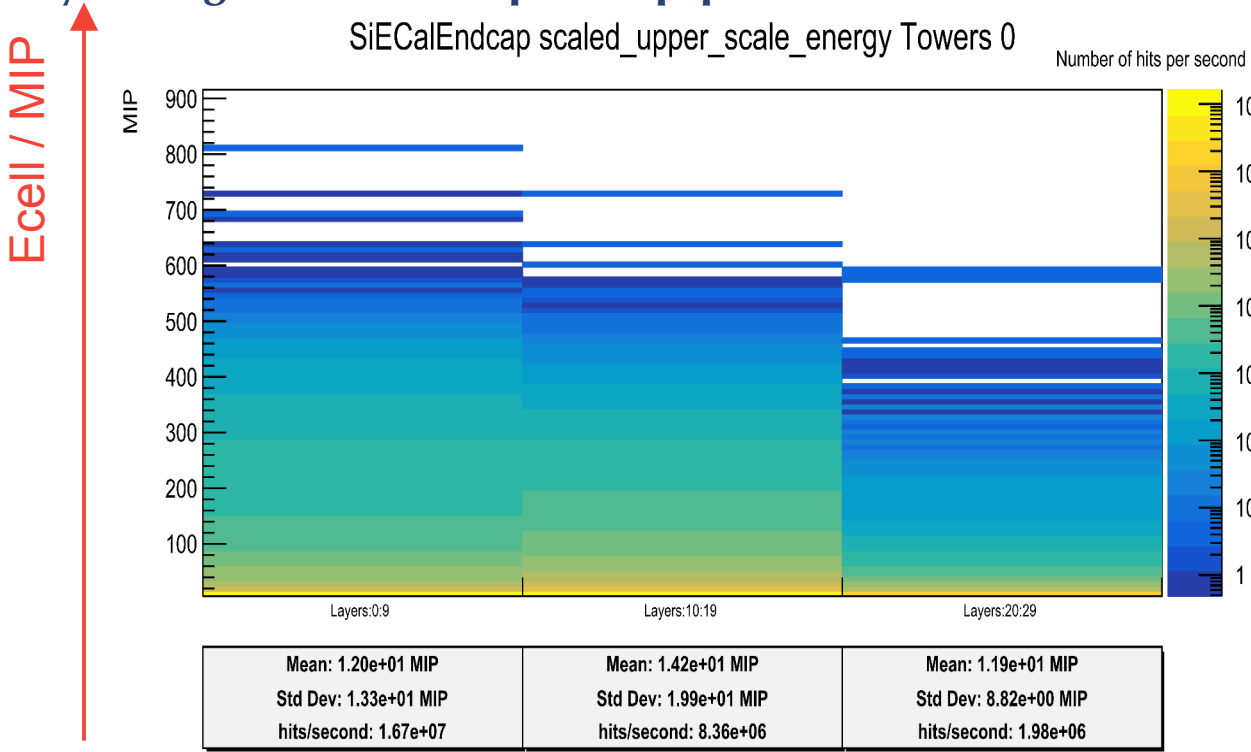
**Note 1 : Preliminary**

**Note 2 : Rates for all M3 modules → /8 per module, /10 per layer**

# Dynamic range

Dyn Range ~  $(E_{max}/Mip) / (Mip/precision)$

SiECalEndcap scaled\_upper\_scale\_energy Towers 0



Similar distributions for

- Time, Nhits, Energy
- SiW-ECAL, ScECAL, AHCAL, SDHCAL
- All Barrel and Endcaps modules/towers and “Rings”
  - 11 “systems” in total
- 3 section in depth

Now for ILD

Main processes  
@ 91\*, 162, 240\*, 365 GeV

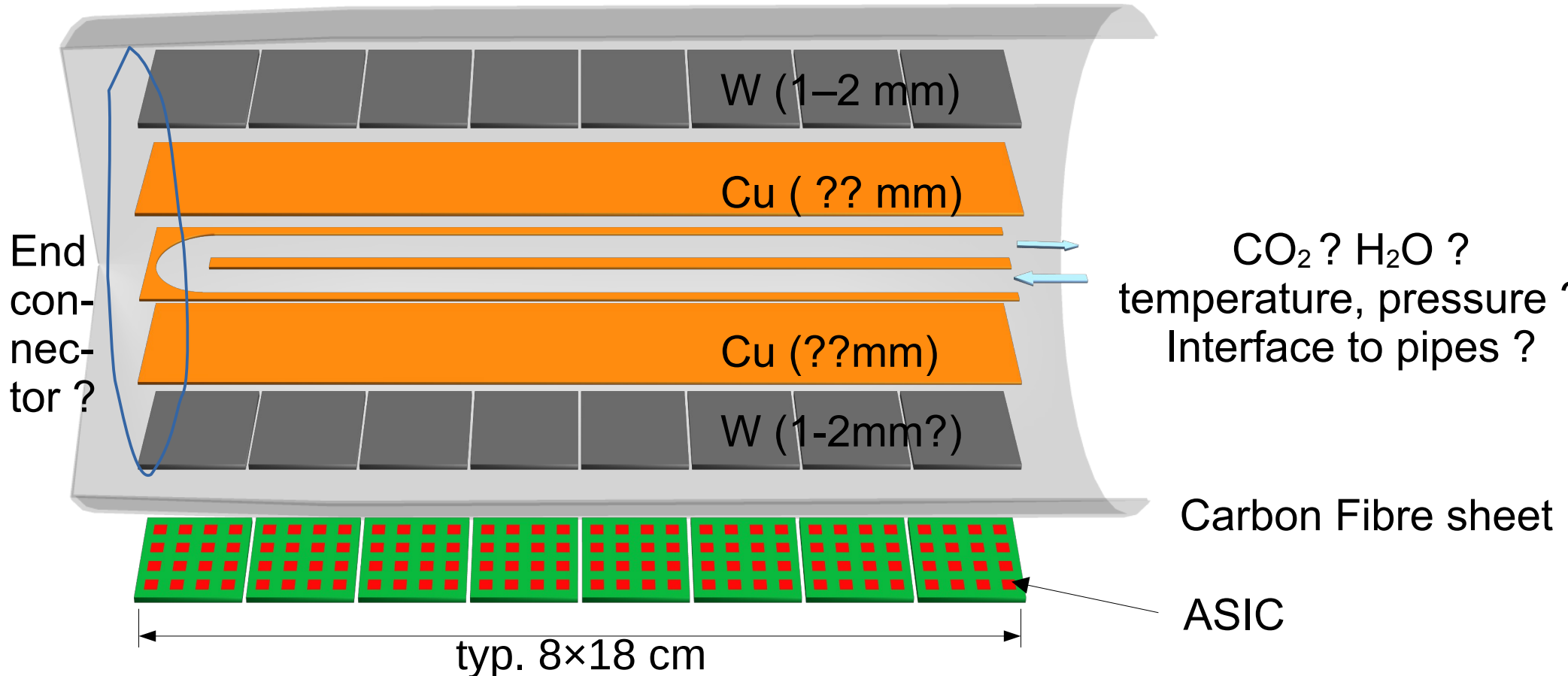
- With machine background

Variant with prelim MDI for FCC

Missing:

- Digitization (esp. for SDHCAL)
- Consolidation
- Play with granularity / electronics / cooling

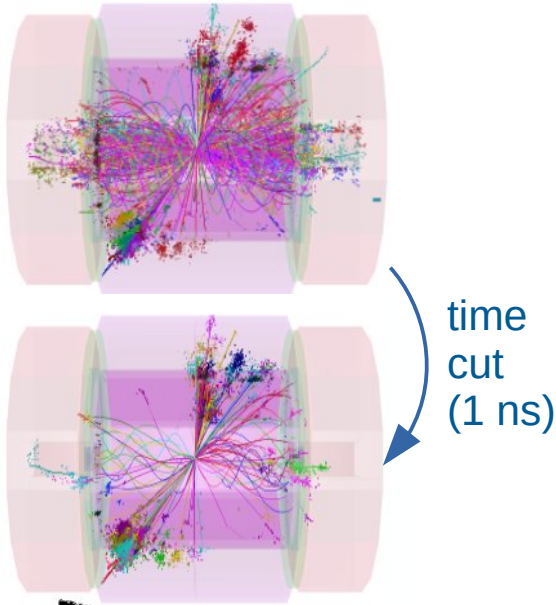
# How to Active cool a slab, without degrading uniformity ?



# Reconstruction Methods : timing

# Timing in Calorimeters: 0.02–1 ns range

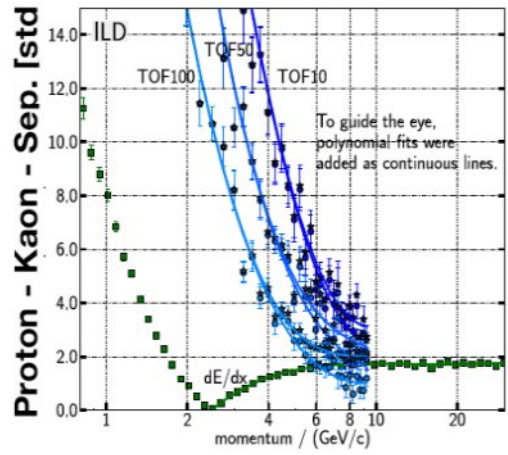
## Cleaning of Events



[CLIC CDR: 1202.5940]  
adapted from L. Emberger

## Particle ID by Time-of-Flight

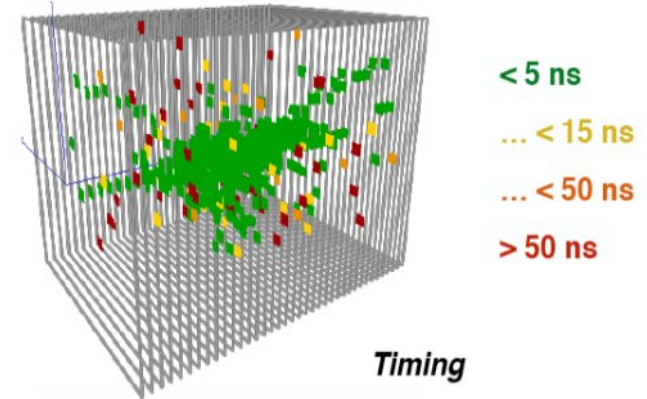
- Complementary to  $dE/dx$ 
  - Here with 100 ps on 10 ECAL hits



S. Dharani, U. Einhaus, J. List

## Ease Particle Flow with ps ?

- Cleaning of late neutrons & back scattering (ns)
- Identify primers in showers
- Help against confusion *better separation of showers*
  - Requires '4D clustering'



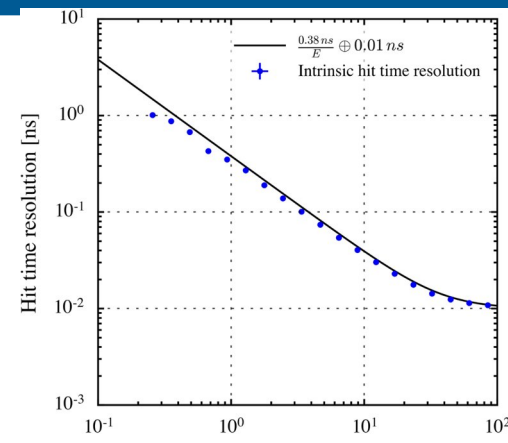
# Cluster time measurement with CEPC calorimeter,

Eur. Phys. J. C (2023) 83:93

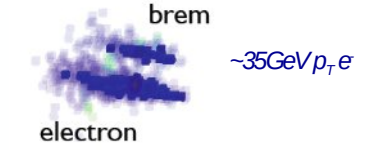
Yuzhi Che<sup>1</sup>, Vincent Boudry<sup>2</sup>, Henri Videau<sup>2</sup>, Muchen He<sup>1</sup>, Manqi Ruan<sup>1,a</sup>

## Optimal use of timing from a given cluster to estimate the particle Time-of-Flight in ECAL

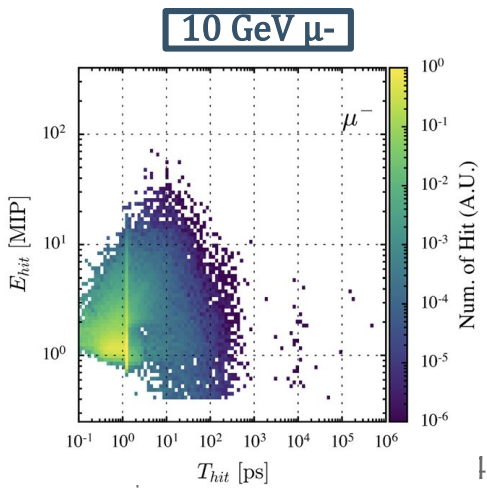
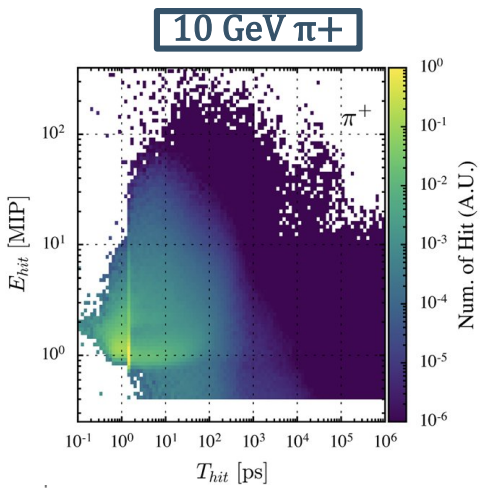
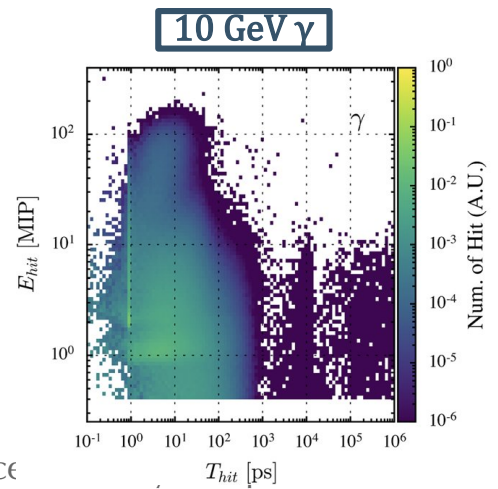
- "Intrinsic" cell-time resolution vs cell-energy  
 CMS resolution type  $\sim 10$  ps for high E  $\rightarrow$  Digitization
- Low-E cells degrades performances
  - Bad time resolution & Shower-halo  $\rightarrow$  Multiple scattering



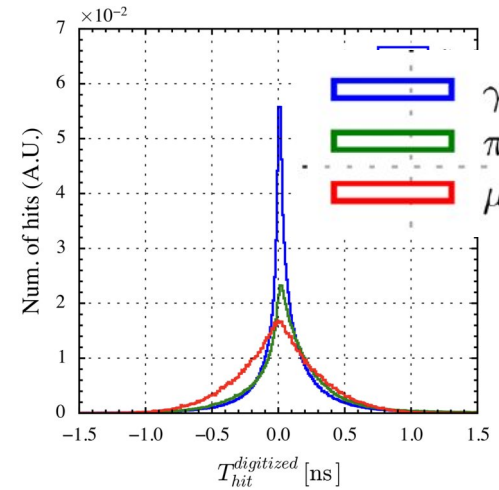
CMS Experiment at LHC, CERN  
 Data recorded: Thu Jan 1 01:00:00 1970 CEST  
 Run/Event: 1 / 1  
 Lumi section: 1



Transparent cells  $\Rightarrow$  no timing  
 Solid cells  $\Rightarrow$  timing information  $\sim 50$ ps

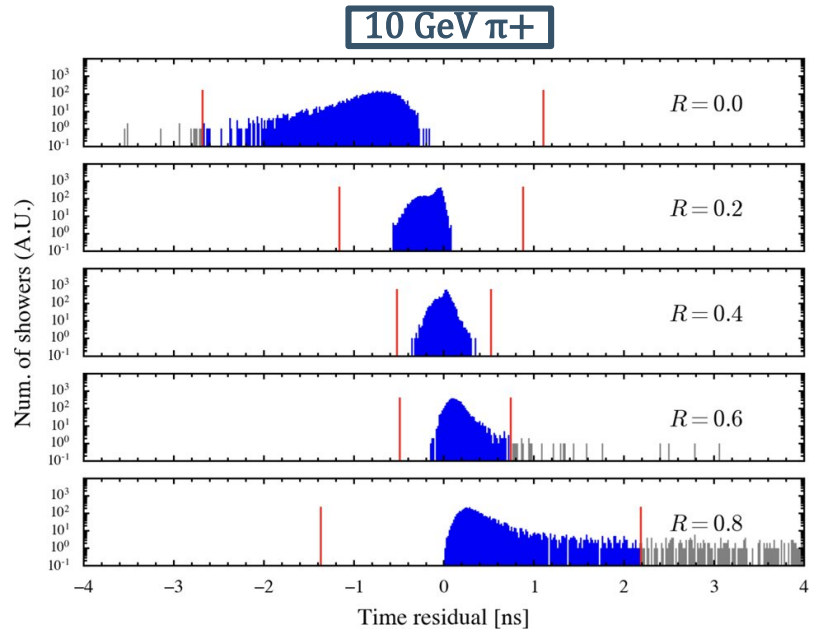
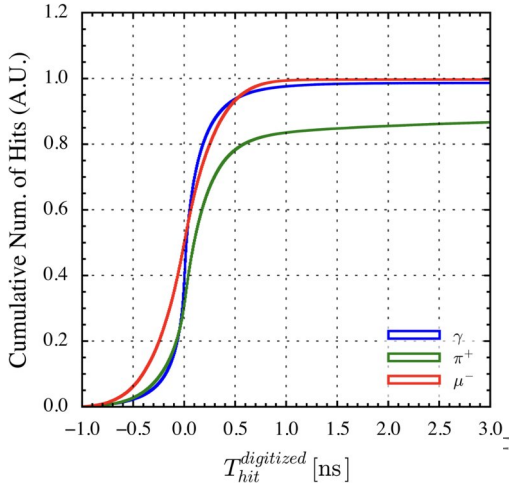


# Cluster time measurement with CEPC calorimeter, Eur. Phys. J. C (2023) 83:93



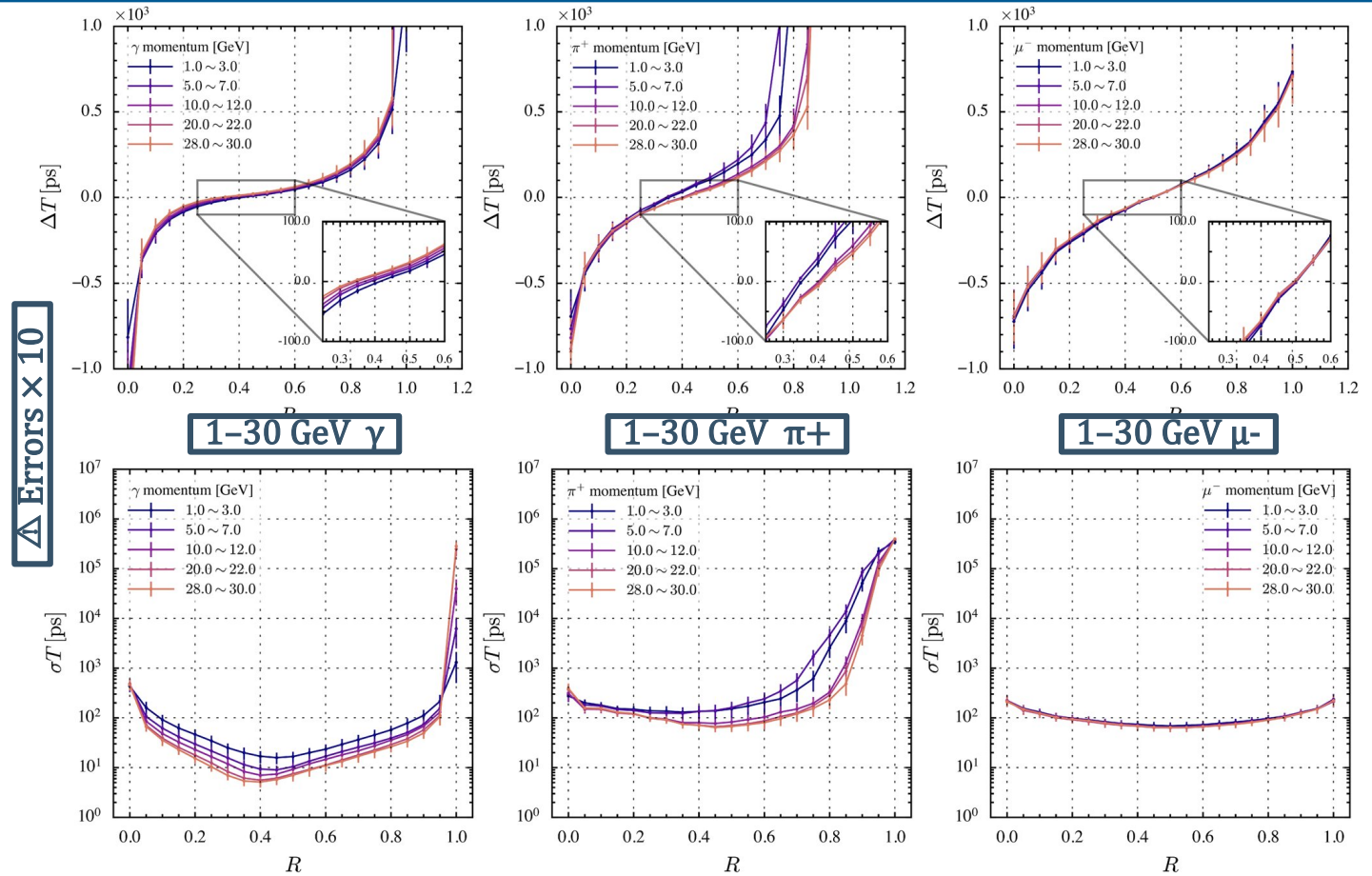
Best  $\langle \text{time} \rangle$  from the core, but where does the core stops ?

- Narrow peak around the middle hit-time distribution
- use the time of the  $(R \cdot N_{hit})$ -th time-ordered hit
- Scan for best  $R$  vs  $t(\text{PID} \ \& \ E)$



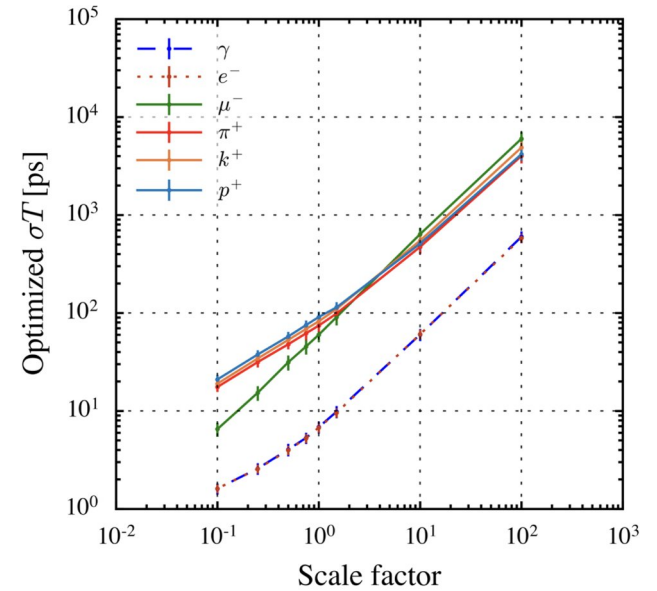
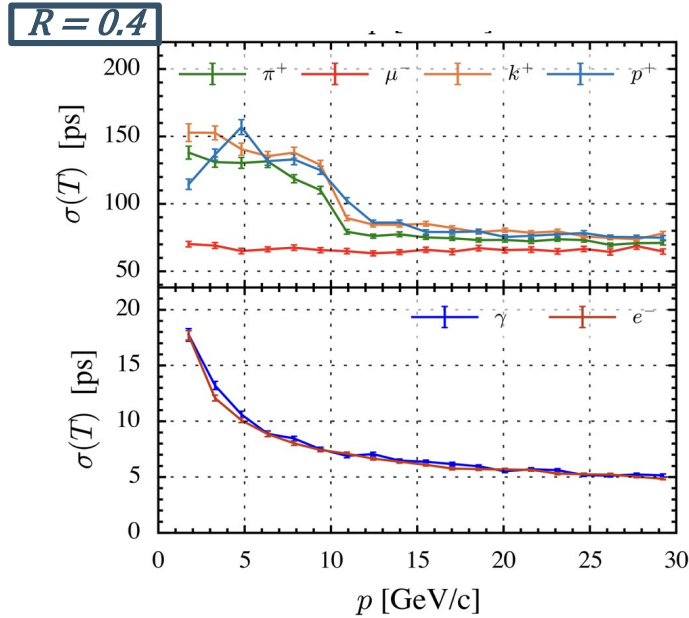
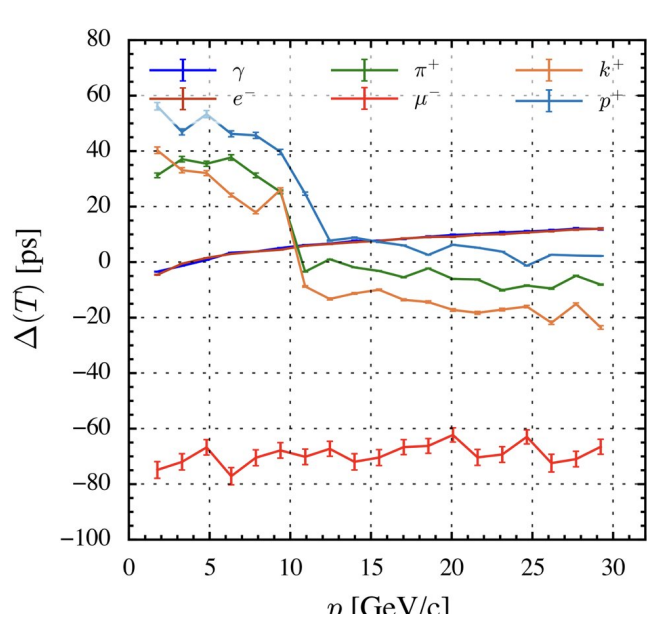
Red lines = outliers quantile-based cut-off

# Biases & resolutions





# Biases and Resolutions

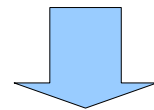


## Hadronics showers (in ECAL only!)

- $\sigma(t) \sim 150\text{--}75$  ps ; Break at 10 due to GEANT4 hadronic models...

## EM showers:

- $\sigma(t) \leq 20$  ps for  $E > 1$  GeV; 5 ps for  $E > 25$  GeV



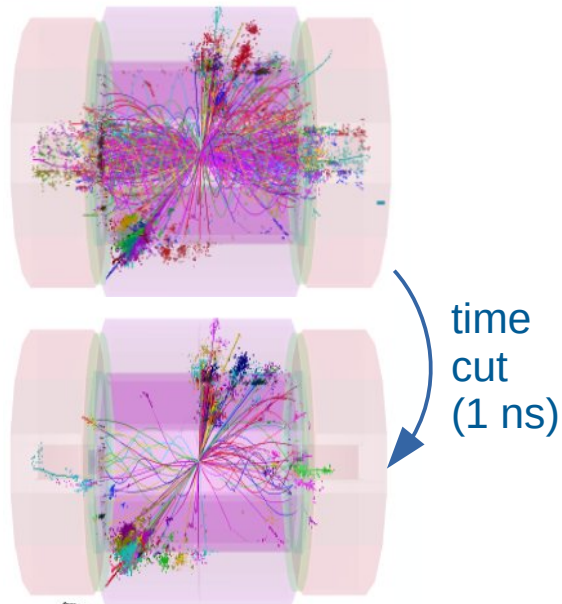
Nlayers, timing layers,  
Si thickness

1 cm/c = 30 ps

# Timing in Calorimeters: 0.02–1 ns range

Recruiting in KIT, JGU, IJClab, LLR and IP2I (Lyon)

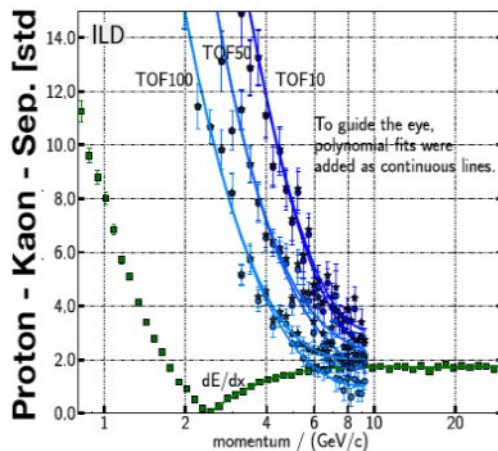
## Cleaning of Events



[CLIC CDR: 1202.5940]  
adapted from L. Emberger

## Particle ID by Time-of-Flight

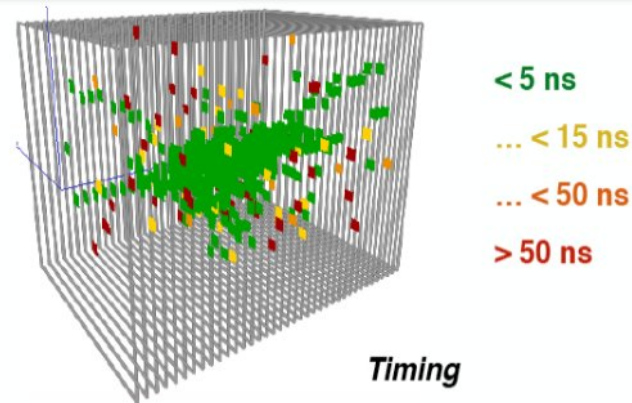
- Complementary to  $dE/dx$ 
  - Here with 100 ps on 10 ECAL hits



S. Dharani, U. Einhaus, J. List

## Ease Particle Flow with ps ?

- Cleaning of late neutrons & back scattering (ns)
- Identify primers in showers
- Help against confusion  
*better separation of showers*
  - Requires '4D clustering'



# Conclusions

## SiW-ECAL technological prototypes

- **2022:** Heterogeneous 15 layers
  - 1<sup>st</sup> full calorimeter working [DESY22, CERN22]
    - Shower seen, Detailed simulation ready
    - Analysis on-going → resolutions, ...
  - Numerous emerging issues
    - gluing, HV filtering at high energy
- **2024:** Uniform ~~15~~ layers
  - → New VFE boards
    - Cleaner PS & Clock distributions; more uniform
  - Gluing being revisited
  - Material available.
  - To be tested in 2024
    - Provide reference sample for GEANT4
    - With funding → “full” LUXE



## SiW-ECAL design for HET factories

- **2023–26:** Power budget & performances to be re visited
  - Occupancy, power, data fluxes (on-going)
    - Granularity; Passive or Active cooling
    - new ASIC attributes
  - 2024–26: **PFA & Physics performances**
  - **Including timing & AI enhanced PFA**



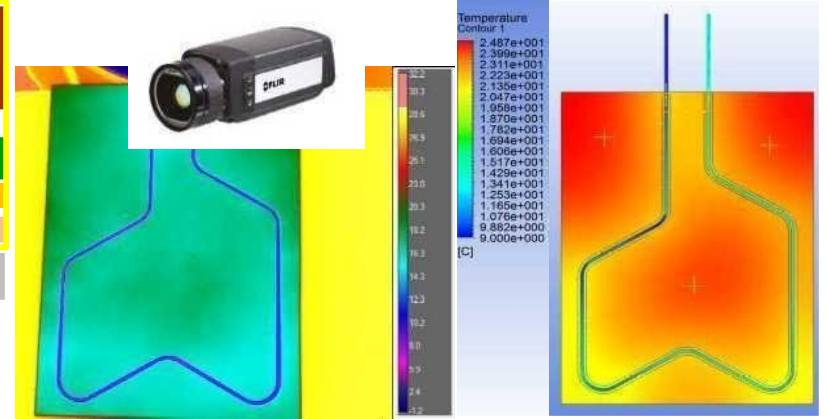
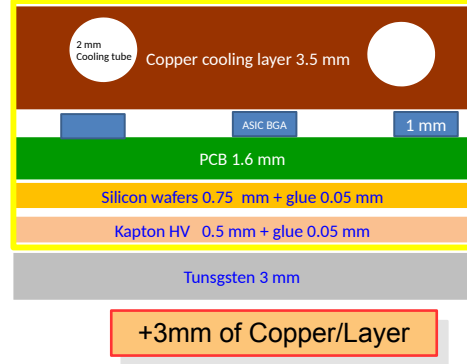
**2025–26:** Blue-print for a SiW-ECAL detector for the next ee collider

- planning for a pilote module @ T<sub>0</sub> collider-8y -5y (1 Mch, 1/60<sup>th</sup> of real detector)  
semi-industrial, quality, ASICs, ...

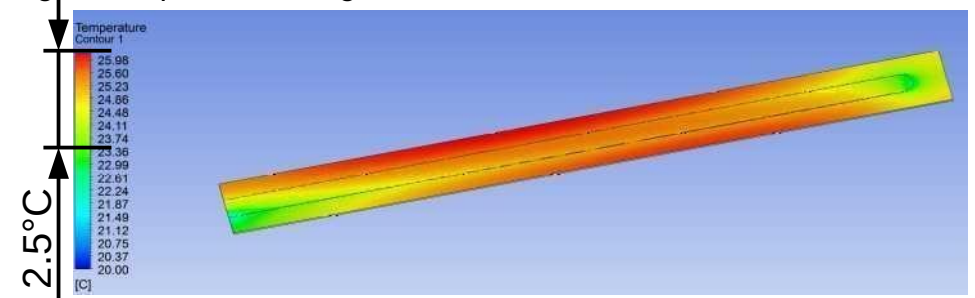
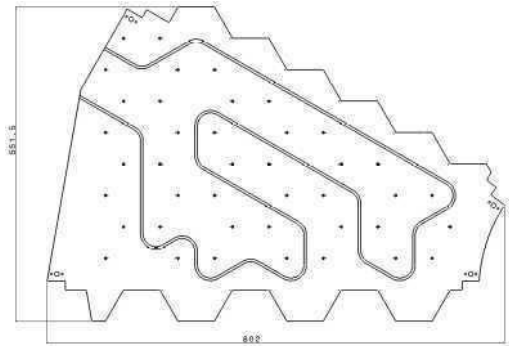
# Back-up

# Services: integration & cooling

T. Pierre Emile



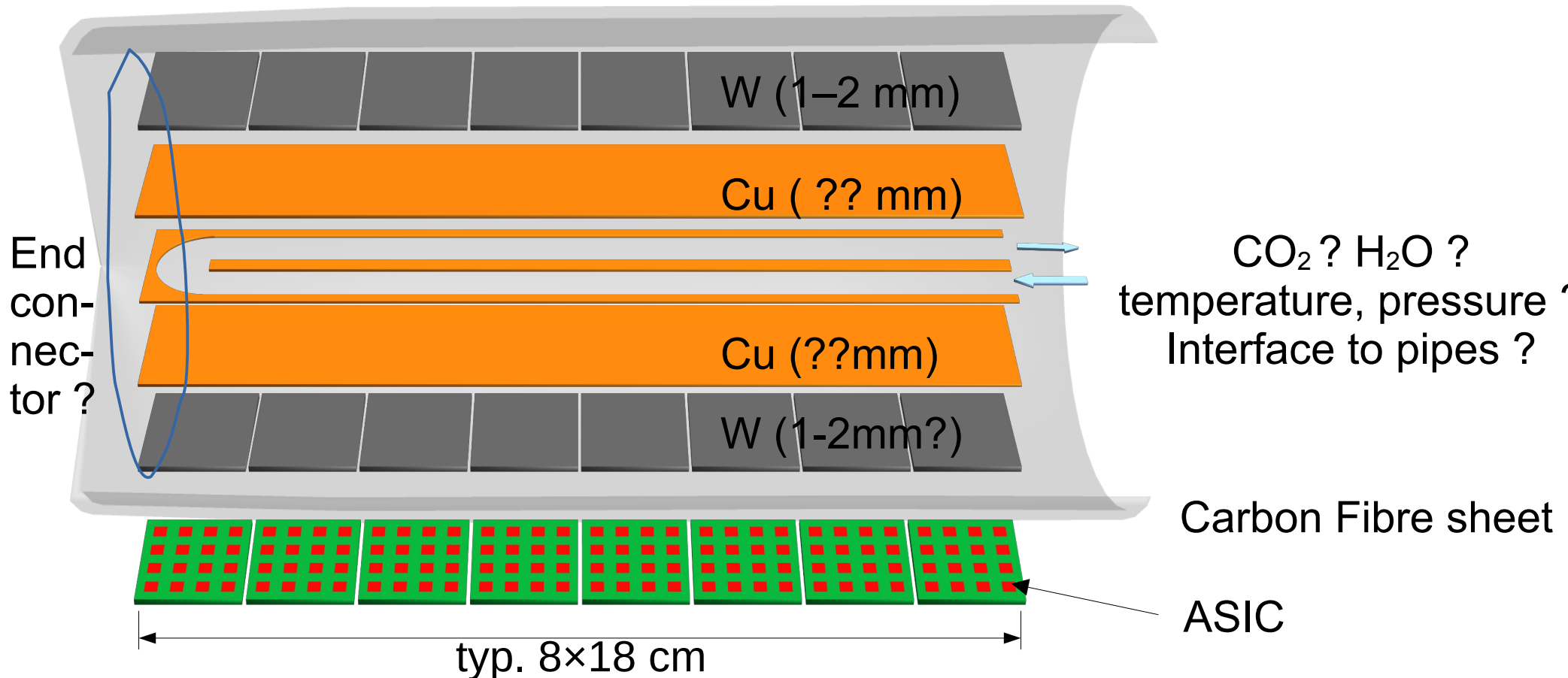
- Pipe insertion process introduces some efficiency loss due to the thermal contact resistance.
- The benefit remains significant with regard to a passive cooling



Thermal static CFD analysis thermal field example using Fluent with 100W extracted and water mass flow rate of 70g/s. operation of Pa BEAB,

with 5 mW/ch × 5×5 mm<sup>2</sup> 37/42

# How to Active cool a slab, without degrading uniformity ?



# Going from 30 to 26 Layers & 500 → 725 μm : performances

## Going from 30 to 26 layers

- Reduction of cost; increase of Energy resolution
  - keep  $24X_0$  (84mm) of Tungsten

## Increasing the Si thickness to 725 μm

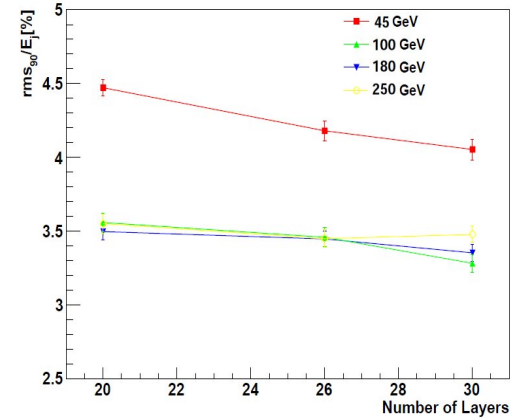
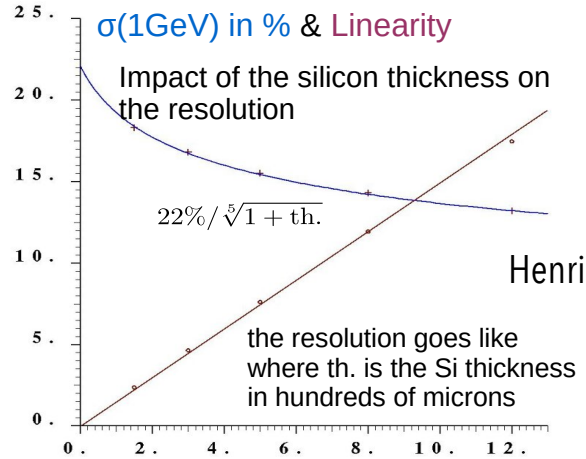
- GR width ↗ ⇒ go to 8" wafers, new design

## Energy resolution $\sigma(E)/E$ :

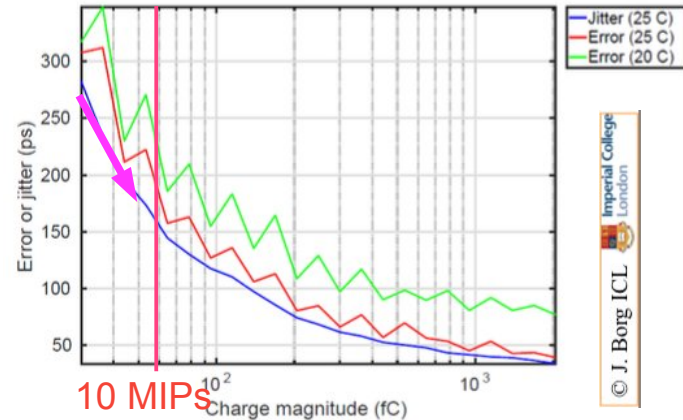
- for 26 layers w.r.t. 30: ↗ +8.5%
  - with 725 μm w.r.t 500 μm : ↘ -6.6% (-8.7% wrt to DBD 300 μm)
- near compensation*

## Time Resolution ?

- Noise  $\sim C_{det} \sim \text{width}^2/th$ ,  
Signal  $\sim th$  ↗ →  $S/N \sim th^2 \sim \times 1.5$   
⇒ Improved timing perf (esp. for mips)

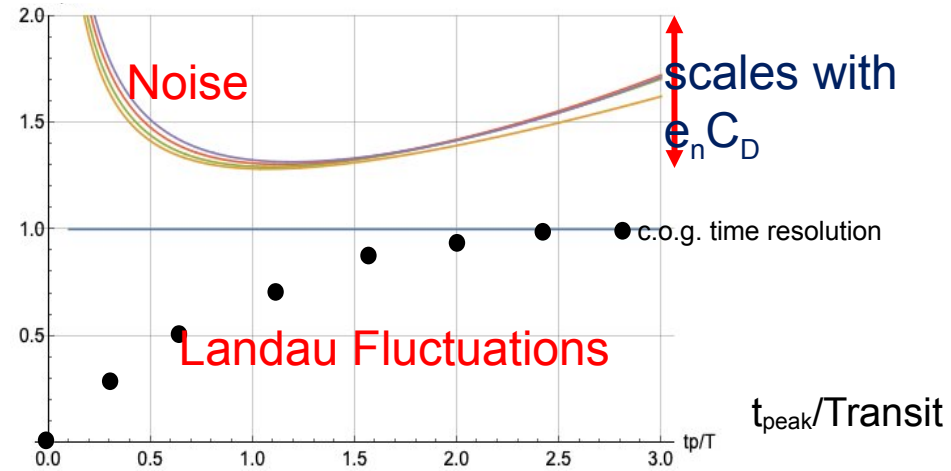
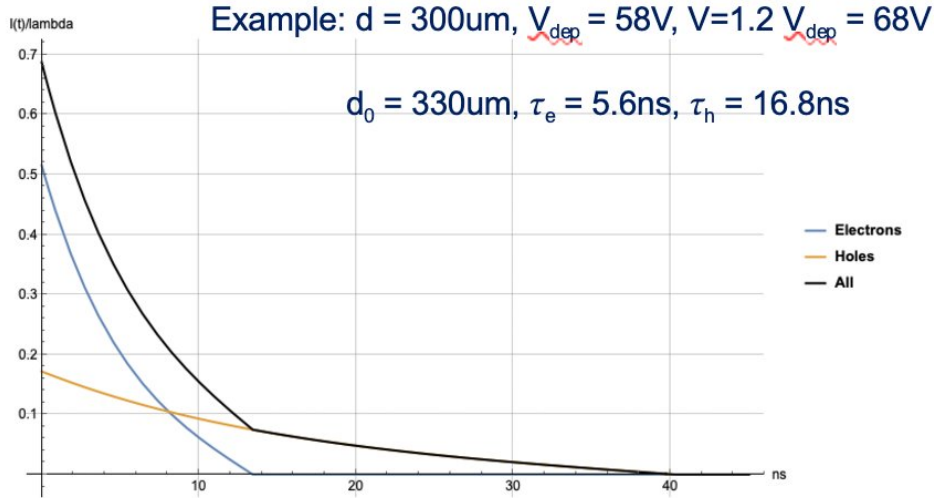


## Si thickness / 100 μm

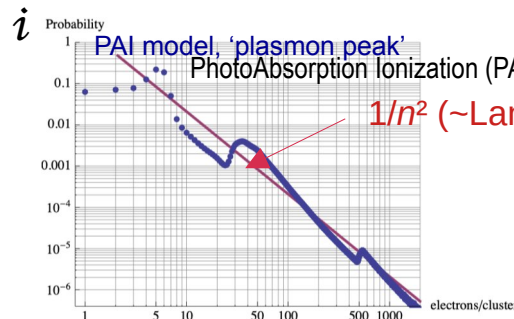
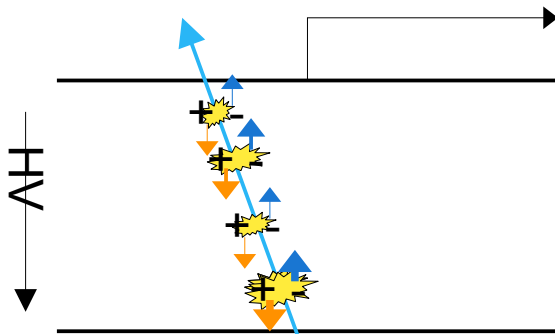


Imperial College London  
© J. Borg ICL

# Time measurement *in Silico*



Landau Fluctuations  $\oplus$  Noise

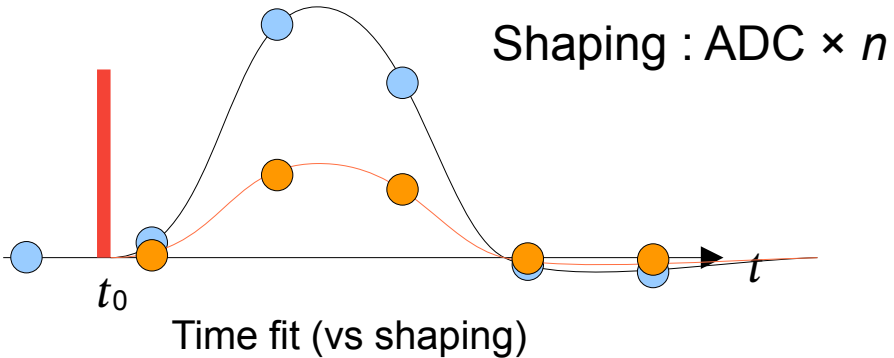
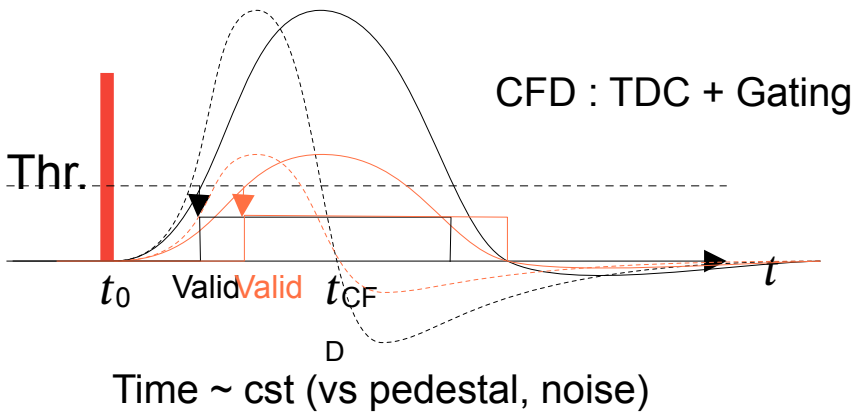
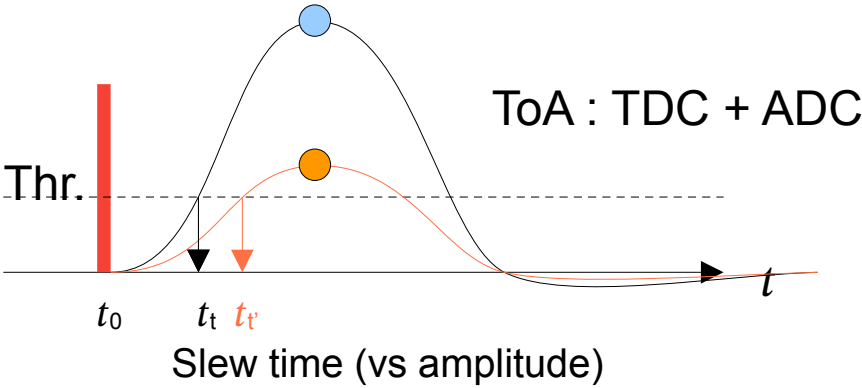


→ optimal thickness of sensors ( $T \leftrightarrow$  thickness) to be determined

S. Riegler



# Time measurement methods



# Time resolution: Quantiles vs Average

