

Pedestal Analysis

Comparison Cob a and Cob c

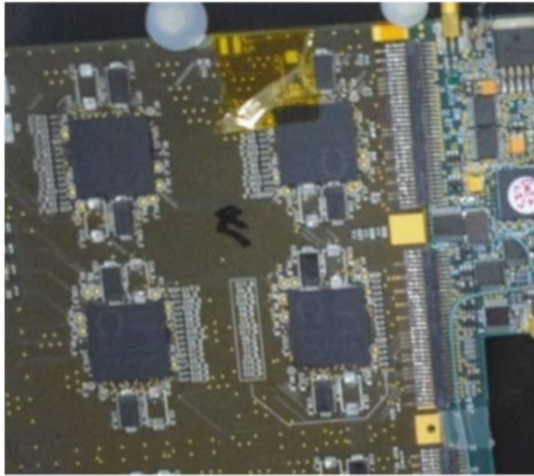
Slb 0 cob c → without extra capacitance

Slb 2 cob a → with extra capacitance (the decoupling capacitances are placed between avdd and gnd, 4 cms cap of ~150uF)

Expecting less noise for the cob a than cob c

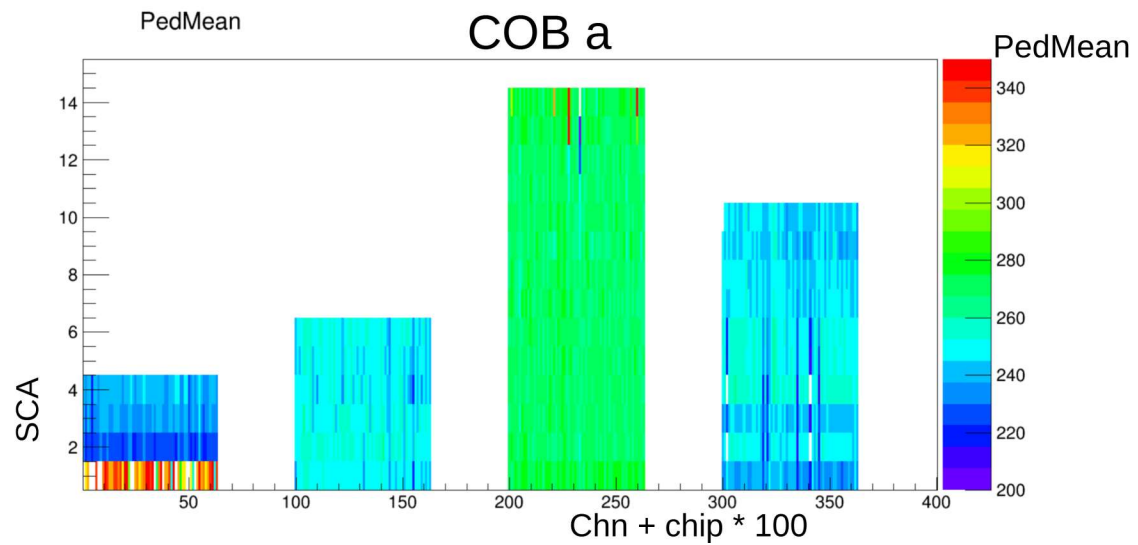
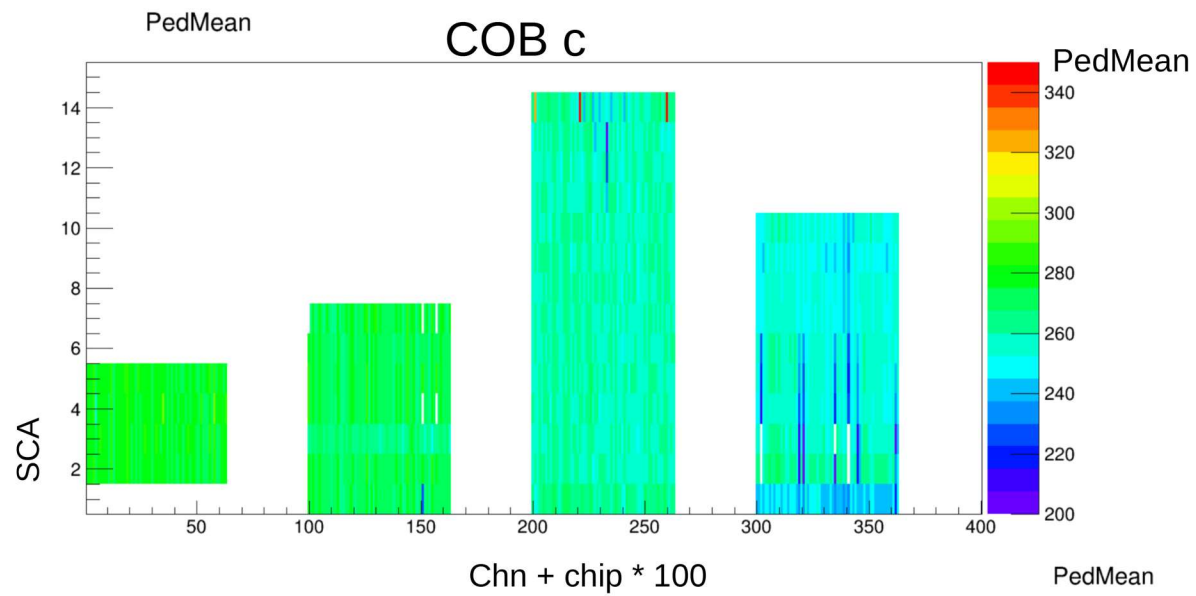
SLB based slabs

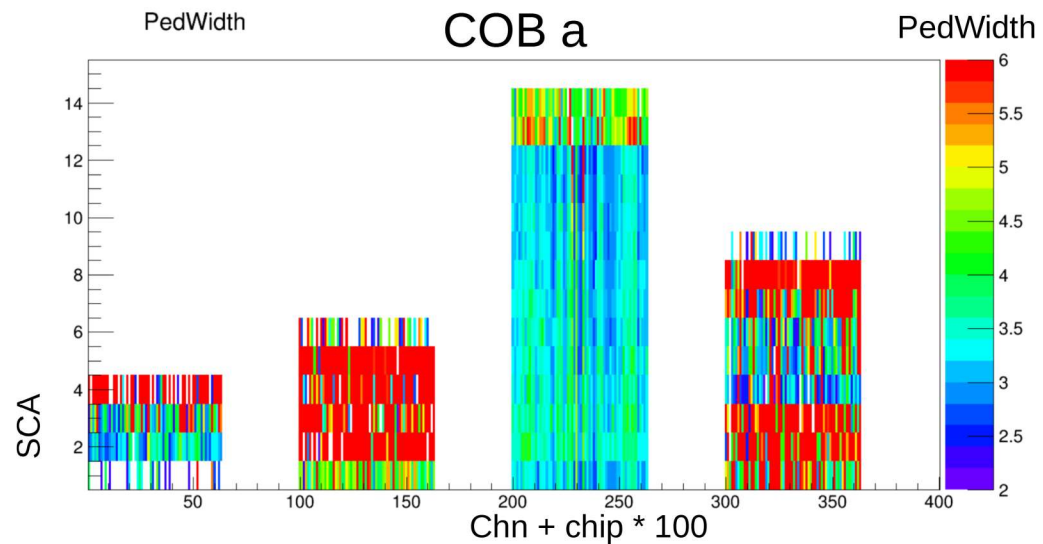
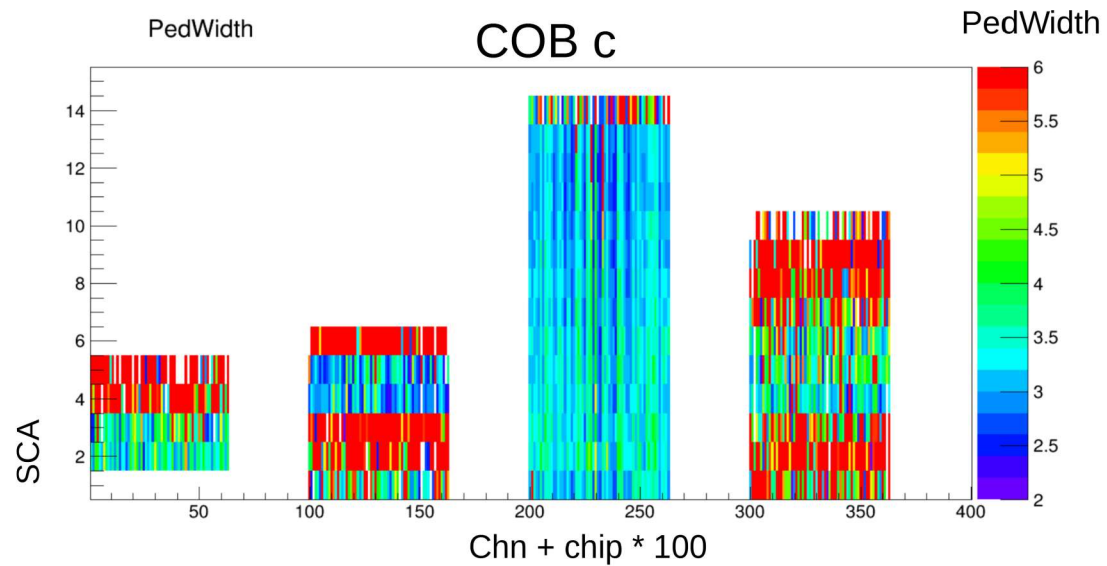
- Two COBs with different amount of extra components (i.e. AVDD, DVDD external decoupling capacitances)
 - COB_a started “naked” and ended up with 4 CMS 140uF capacitances
 - COB_c with zero extra components (but an aluminum plate used as a chip protection used during the gluing).
- 2 FEV12 fully equipped with all components

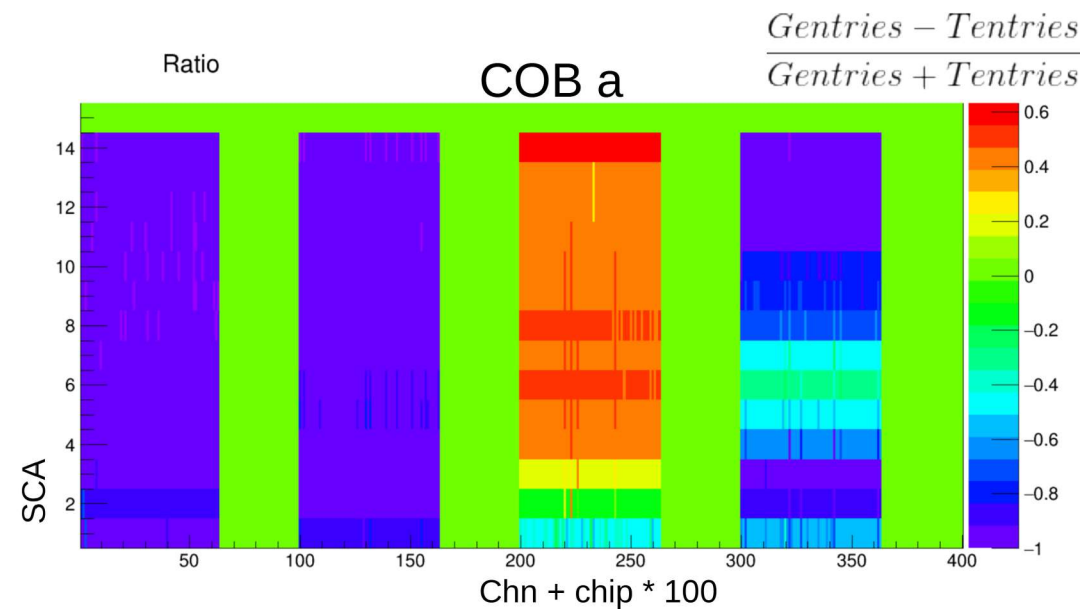
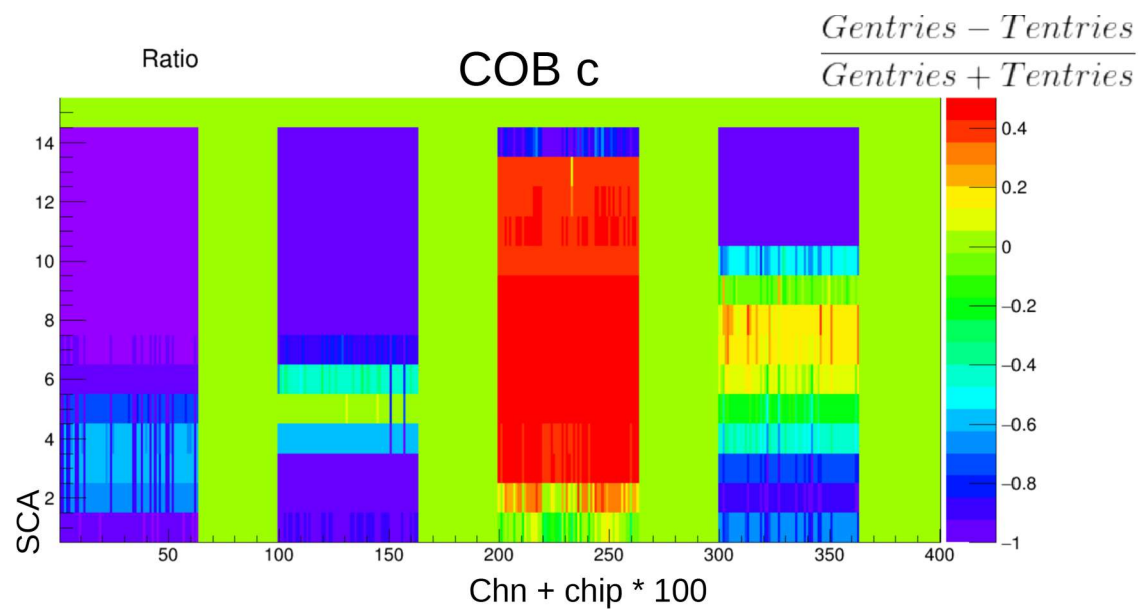


MIP run 32014 (chip 2)

- Pedestal Widths
- Ratio of good entries over tagged entries
- Pedestal Mean







The two COB got quite the same pedestals characteristics.

Are the extra coupling capacitances useful ?

Next steps are :

- Comparing run before and after adding capacitances for COB a (with gain of 3,6pF)
- Study the presence of double pedestal
- Study the retrigger and plane events