1st Analysis Meeting, TB2019

A. Irles, LAL-CNRS/IN2P3 31/07/2019































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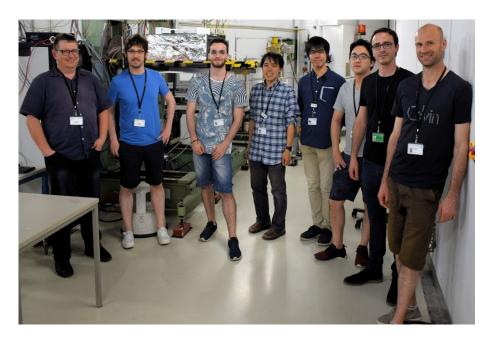
LABORATOIRE DE L'ACCÉLÉRATEUR

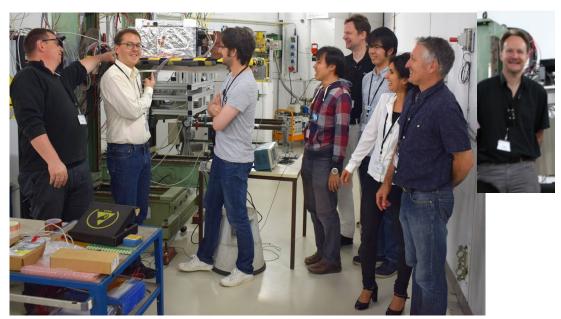
> Presence from











> Plus support & hardware from





Run descripion



End of the beam test, at 14.30. Start dismounting.

Run numbering:

run_CSXXX

- C= configuration
 - o 1: only 5 FEV13 in the box
 - 2: 5 FEV13 + 2 SLB (0 and 3) in the box
 - 3: 5 FEV13 + 4 SLB in the box
 - 4: 5 FEV13 + 4 SLB in the box + tungsten plates
- S= system in the DAQ
 - o 0= only FEV13
 - o 1= only SLBs
 - \circ 2= all

Data location. In the EOS (CERN)

Data Location + Software Location



- ➤ All data is in the CERN EOS: /eos/project/s/siw-ecal/TB2019-06/
 - SLB_data → all converted data + raw (ASCII)

Some runs are still missing... i.e. 32016,17, 18 (not copied from the DAQ pc?)

pyrame/run_XX

All data files are converted? High_gain is high gain? Or is TDC?

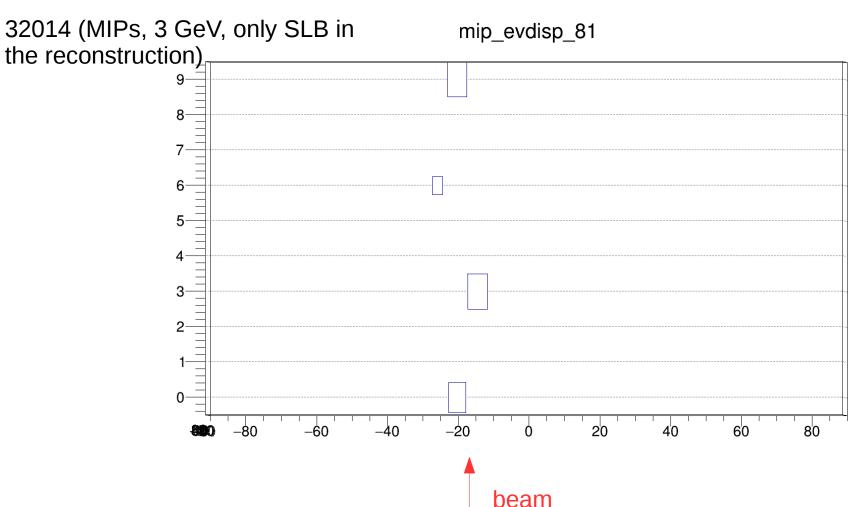
- > Run LISTs:
 - FEV13 → https://drive.google.com/file/d/1uQojlu9KIS9badhVrBf1LRFNt-kz62vV/view?usp=sharing
 - SLB systems... to be done.
 - I would add a new column to the table telling about the status of the conversion to root file.

Data Location + Software Location



- > Software:
 - Preliminary version can be found in /eos/project/s/siw-ecal/TB2019-06/SiWECAL-TB-analysis
- ➤ Soon to be uploaded to the github https://github.com/SiWECAL-TestBeam/SiWECAL-TB-analysis to a new branch
 - To be done today.
- Critical point: event building
 - We need a pedestal file for each slab. Mandatory for the event building.
 - We need mip calibration files... My proposal: not doing a mip calibration but a simple 500um/600um factor between FEV13 and the others.
 - What is the offset between FEV13 and SLB systems? Is it constant?

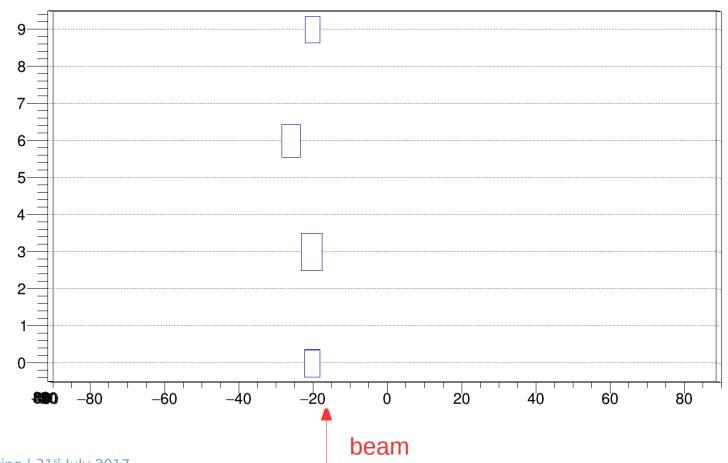






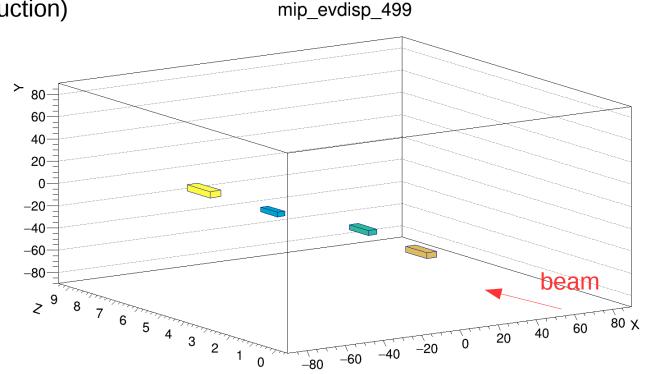
32014 (MIP the reconsti

mip_evdisp_90



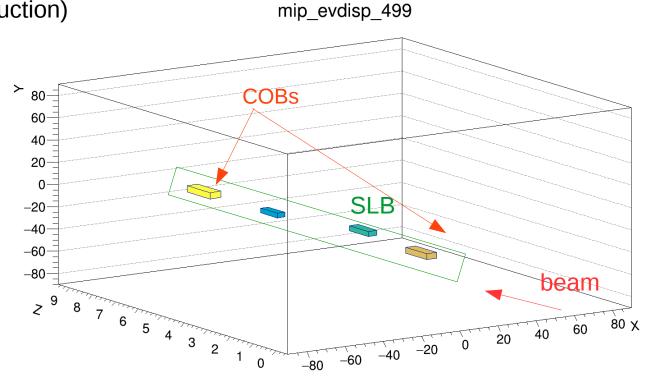


32014 (MIPs, 3 GeV, only SLB in the reconstruction)

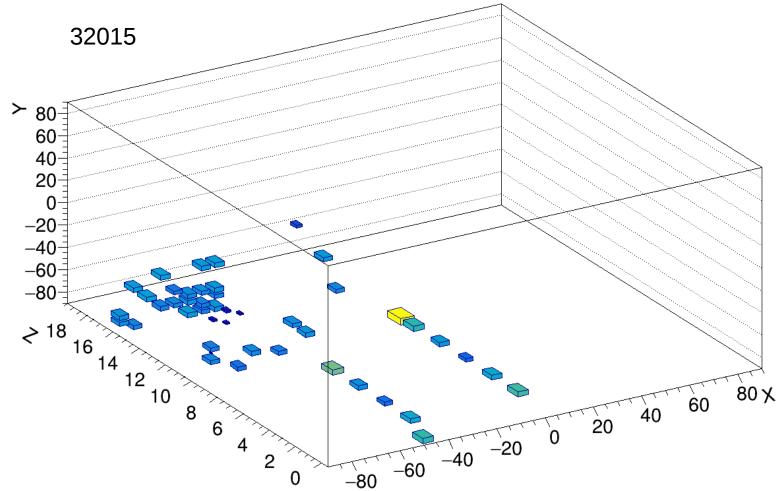




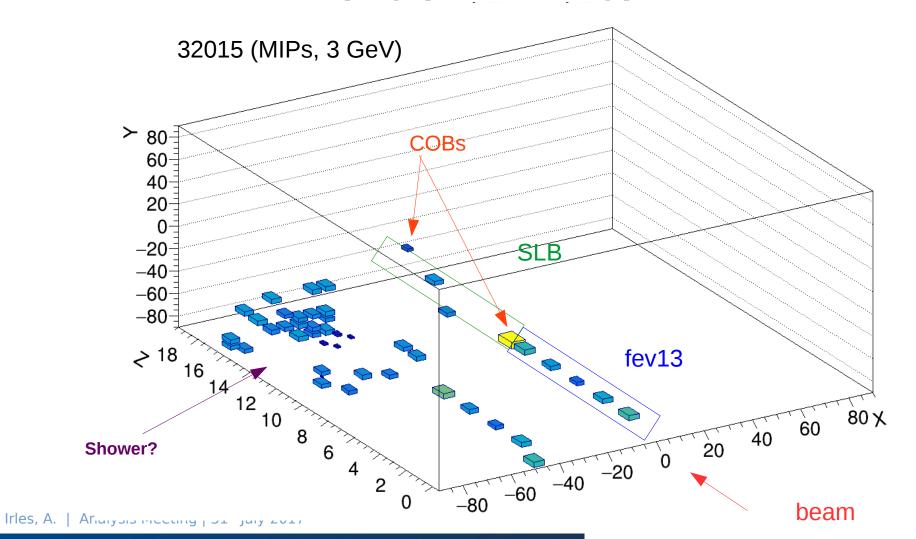
32014 (MIPs, 3 GeV, only SLB in the reconstruction)











Back-up slides



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charge__SLB_0_run_32004_chip3

