The ATLAS Phase II Upgrade

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Contents

- 1. HL-LHC conditions
- 2. ATLAS upgrade projects
 - ITk
 - HGTD
 - TDAQ
 - Muon Spectrometer
 - Calorimeters
- 3. Summary

The High Lumi LHC



We are here

The High Lumi LHC



- Installation 2024-2026
- Begin operation in 2026

The High Lumi LHC

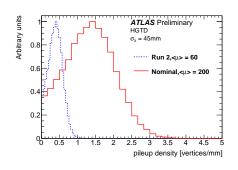


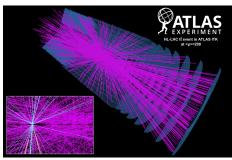
- Installation 2024-2026
- Begin operation in 2026
- Instant luminosity 7.5×10³⁴ cm⁻²s⁻¹
- ▶ Integrated luminosity 3000 fb⁻¹

HL-LHC Conditions

Increased luminosity:

- ▶ Expected $\langle \mu \rangle$ = 200
- Average interaction density:
 ~ 1.8 vtx/mm
- \rightarrow need to keep vertexing and tracking performance



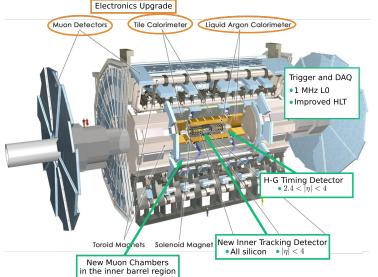


Upgrade motivations:

- High detector occupancy: need to improve reconstruction performance
- ► High irradiation levels: $\sim 10^{16}$ neq/cm² at large $|\eta|$
- Increased trigger rate
- ► Technology update (~20 years old!)

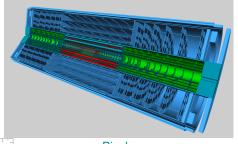
The ATLAS Phase II Upgrade

Update and improve the ATLAS detector to cope with challenging conditions



Inner Tracker: ITk

- ightharpoonup Extended acceptance (2.5 \rightarrow 4)
- Layout optimization:
 - → maximize average number of hits
 - → minimize material and silicon area
 - \rightarrow on-going
- \blacktriangleright Average \sim 13 hits in barrel, and at least 9 pixel hits in the forward region





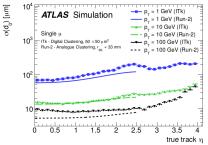
- $50 imes 50~\mu$ m (25 imes 100 μ m)
- Innermost layer: 3D sensor
- Other layers: planar sensors
- single, dual and quad modules

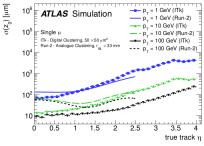
Strip

- Average 75 μ m width
- ▶ Length between ~ 15 and 60 mm depending on layer/ring

ITk Performance

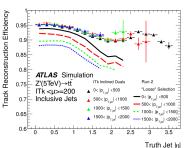
Track impact parameter: transverse (d_0) and longitudinal (z_0)





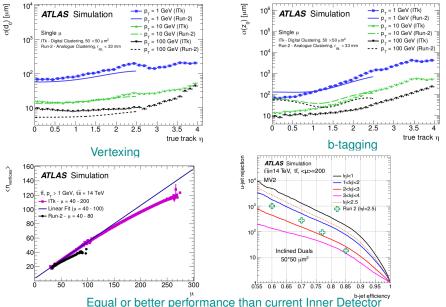
Track reconstruction efficiency in $<\mu> = 200$

- High p_T improvement thanks to higher granularity
- Track reconstruction up to higher η

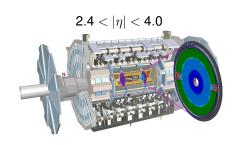


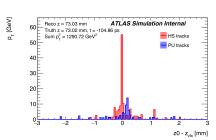
ITk Performance

Track impact parameter: transverse (d_0) and longitudinal (z_0)

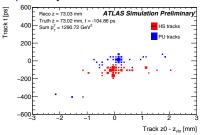


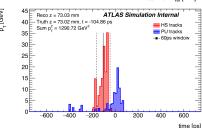
High Granularity Timing Detector: HGTD



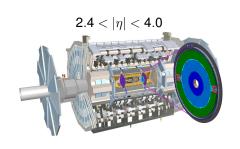


→ Use timing information as additional handle to discriminate hard-scatter and pile-up activity

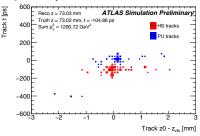




High Granularity Timing Detector: HGTD



→ Use timing information as additional handle to discriminate hard-scatter and pile-up activity



 Low-Gain Avalanche Detector (LGAD): novel silicon sensors with excellent timing performance

Goal:

- ▶ Provide a time measurement for tracks in the forward region with a resolution of 30-50 ps.
- ► Bunch-by-bunch luminosity measurement.

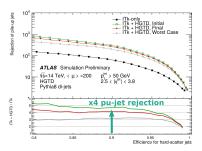
HGTD Performance

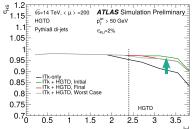
Pile-up jet rejection

- ► Improved id of PV0 tracks
 - → Boost discrimination power of

$$R_{p_{\mathrm{T}}} = \frac{\sum p_{\mathrm{T}}^{\mathrm{trk}} (\mathrm{PV}_{0})}{p_{\mathrm{T}}^{\mathrm{jet}}}$$

 HS-jet efficiency in the forward region similar to central barrel





HGTD Performance

Pile-up jet rejection

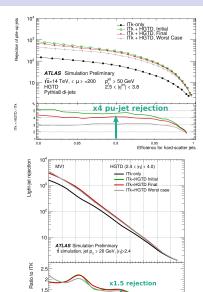
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$$R_{
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m trk}({
m PV}_0}{
ho_{
m T}^{
m jet}}$$

► HS-jet efficiency in the forward region similar to central barrel

Jet b-tagging

- Addition of HGTD helps remove PU tracks
 - → increased light-jet rejection

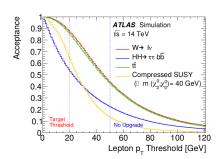


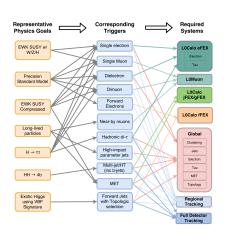
0.5 0.55 0.6 0.65 0.7 0.75 0.8 0.85 0.9 0.95

b-iet efficiency

Trigger and DAQ: physics motivation

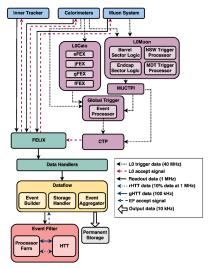
- Development/performance improvement of dedicated trigger for different analyses
- Searches for new physics require to keep the p_T of the different trigger objects as low as possible
- i.e : lowest p_T of the lepton trigger would be 50 GeV without upgrade

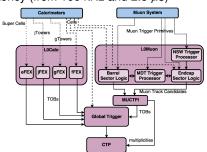




TDAQ upgrade summary

Major upgrade: L0 rate of 1 MHz and 10 μs latency (from 100 kHz and 2.5 μs)

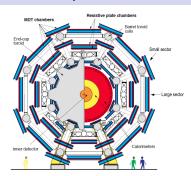




Detailed view of L0 Trigger:
Use of full calorimeter granularity and input from MS

- $\rightarrow \text{offline-like object reconstruction}$
 - → possible to implement more sophisticated algorithms
- ightarrow major upgrades needed in LAr, Tile and MS

Muon Spectrometer

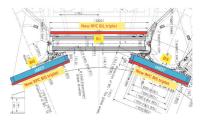


Chamber replacement:

- Phase I: New Small Wheel using sTGC and MicroMegas for triggering and precision tracking $(1.3 < |\eta| < 2.7)$
- Additional RPC BI triplet
 - ightarrow compensate for reduced efficiency
 - → improved trigger capabilities
 - → robustness against failures

Trigger and readout electronics replacement:

- on- and off-detector
- All data streamed @40 MHz
- include MDT position
 → enables more refined and flexible trigger algorithms

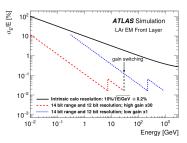


Need to replace BIS MDTs for thinner sMDT

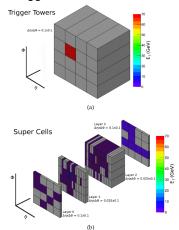
Calorimeters: LAr and TileCal electronics

LAr full electronics upgrade:

- Currently: sampling @40 MHz, send 4 samples on L1 accept (110 kHz)
- New preamp/shaper design with dual gain,
- followed by dual ADC
- Stream all digitized data @40 MHz



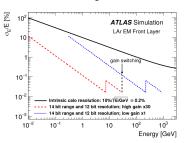
Phase I upgrade: finer granularity at L1 trigger



Calorimeters: LAr and TileCal electronics

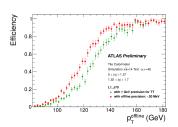
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TileCal mechanics and electronics:

- Currently: analog summing boards combine PMT signals in towers for L1Calo
 - New scheme: shaper, dual ADC, data transfer @40 MHz
- High precision of the digital L0 calo trigger improves jet-trigger performance:



 Mechanical improvements to the drawers for better accessibility

Summary

- ➤ The HL-LHC will provide 3000 fb⁻¹ at 14 TeV, reaching unprecedented levels of instant luminosity and, therefore, at the expense of increasing pile-up
- The ATLAS Phase II upgrade presented in this talk will allow to cope with this harsh conditions
- All upgrade projects are well documented in Technical Design Reports or Proposals (links below):



ITk Strip: ATLAS-TDR-025 ITk Pixel: ATLAS-TDR-30 LAr: ATLAS-TDR-027 TileCal: ATLAS-TDR-028 HGTD: LHCC-P-012 Muon: ATLAS-TDR-026