

## **SMART (Sfp connectivity and Microtca for Advanced Remote Trigger)**

*Thursday, 11 October 2018 12:30 (30 minutes)*

SMART is a new electronic design based on a smooth upgrade of CENTRUM and GTS towards a new time stamping system with trigger option for all GANIL needs and other collaborations interested by this new solution.

Based on  $\mu$ TCA in terms of architecture, on Xilinx ZYNQ and ZYNQ UltraScale+ in terms of FPGA interfaced with SFP/QSFP (copper/fiber) connectivity, this digital system will be able to synchronize all our existing sequencers and digitizers while easily connect to any kind of new boards.

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