

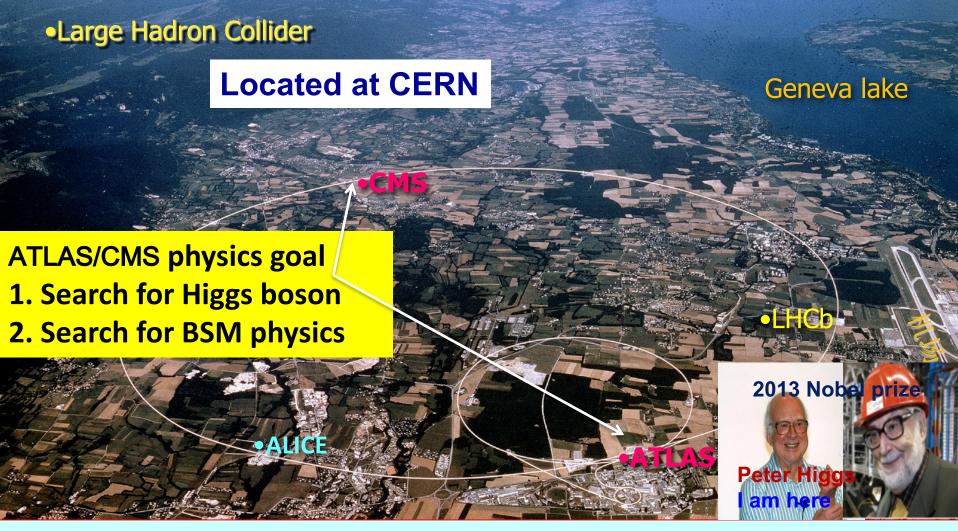


CMS upgrade

Huaqiao Zhang (IHEP)



Large Hadron Collider (LHC)

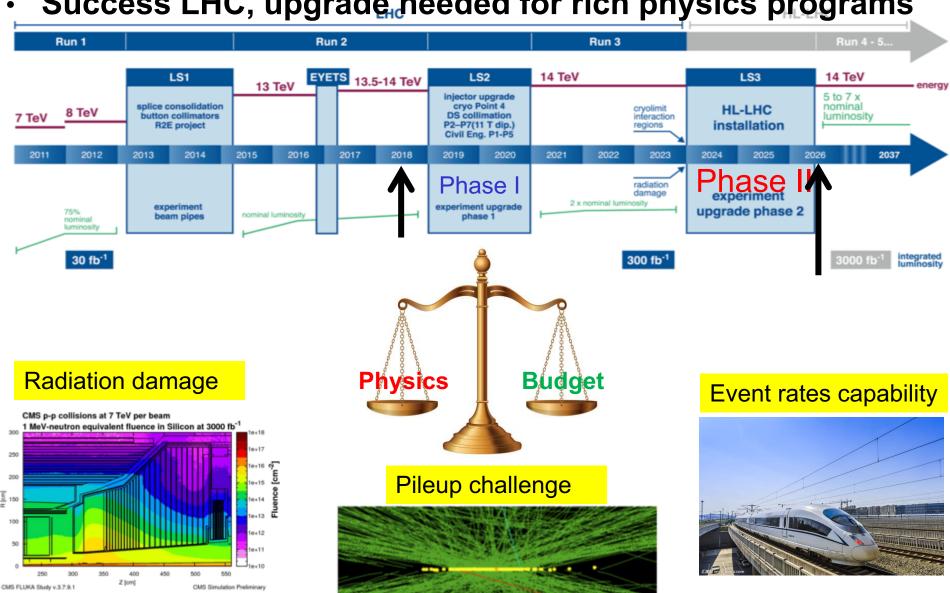


- 27km, 100m underground, ¼ in Switzerland, ¾ in France
- Designed Ecm $14\text{TeV}(14x10^{12}\text{eV})$ for p-p collisions



LHC operation Roadmap

Success LHC, upgrade needed for rich physics programs

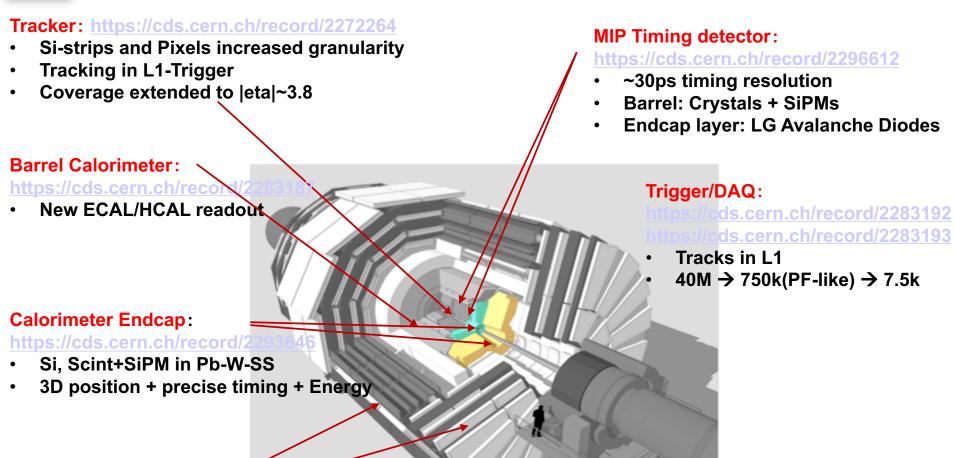


CMS upgrade

2018



Overview of CMS phase II upgrade



Muon system: https://cds.cern.ch/record/2283189

- New FE/BE readout for DT/CSC
- New GEM/RPC 1.4 < |eta| < 2.4
- Coverage extended to |eta|~3

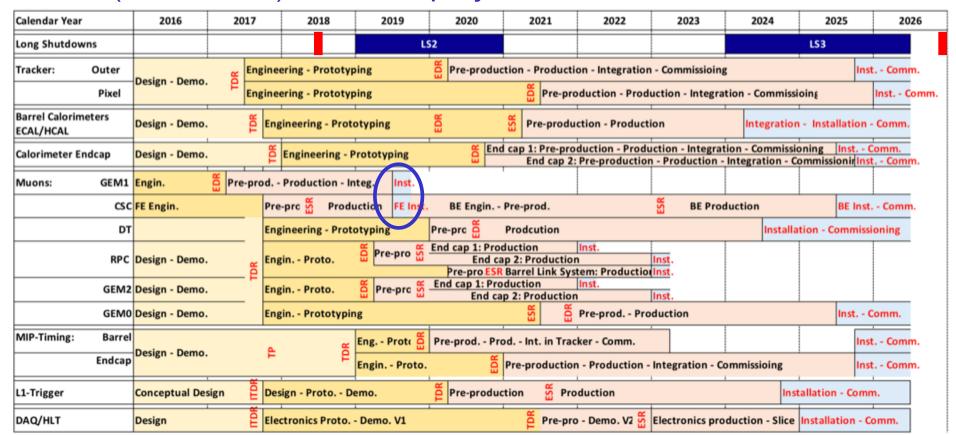
Beam/Luminosity and common Infrastructure

https://cds.cern.ch/record/2020886



Timeline of CMS phase II upgrade

- LS2 (2019-2020):
 - GE1 and CSC FE for inner endcap disk 1-4
 - Beam pipe/magnet infrastructure
- LS3 (2024-2026): All other projects



CERN European Organization for Nuclear Research Organisation européenne pour la recherche nucléaire

CERN-LHCC-2017-009 CMS-TDR-17-001 1 July 2017

CMS



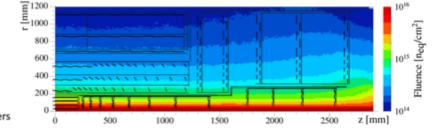
The Phase-2 Upgrade of the CMS Tracker
Technical Design Report



CMS Phase II Tracker design

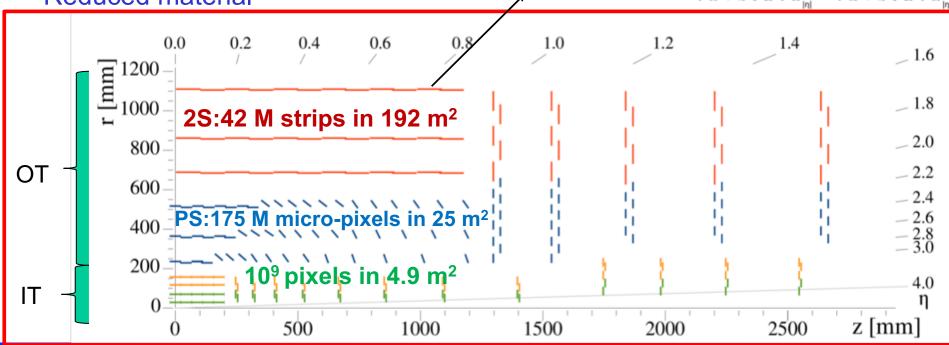
high transverse

- Radiation: ~2.3*10¹⁶ MeV neq/cm²
- 40 MHz input for L1-Trigger
- Increased granularity
 - OT Si-sensors ≈ 200μm thick 90/100μm pitch 2.5/5cm strips 1.5 mm macro-pixels in inner layers
 - IT Si-silicon sensors ≤ 150μm thickness 50x50 to 25x100μm² large pixels in outer layers?





- Extend coverage |eta|<3.8
- Reduced material



momentum

Phase II



Phase II Silicon tracker sensor

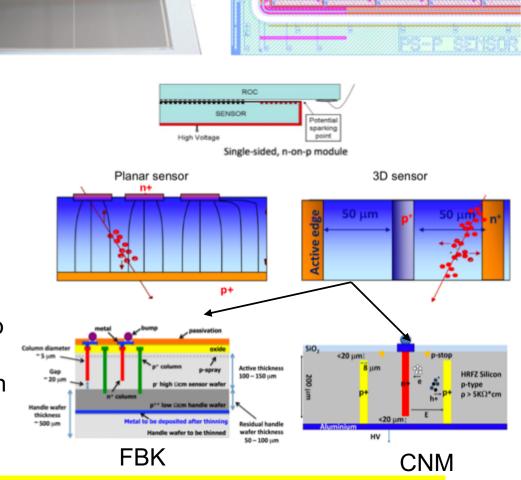
Sensor R&D

2S sensor Outer Tracker: N-in-p planar sensor

- 200-240 micron active layer
- Bias ~800 V
- 3 different sensors
 - 2S: 10*10 cm2, 2032 strip, AC read-out by CBC, noise ~1000e
 - PS-s:5*10 cm2, 1920 strips, AC read-out by SSA, noise ~700 e
 - PS-p: 5*10 cm2, 30208 macro pixels, DC readout by MPA, noise ~175 e

Inner tracker:

- Planar sensors: radiation hardness to be demonstrated, low signal
- 3D sensors: complex fabrication, high capacitance

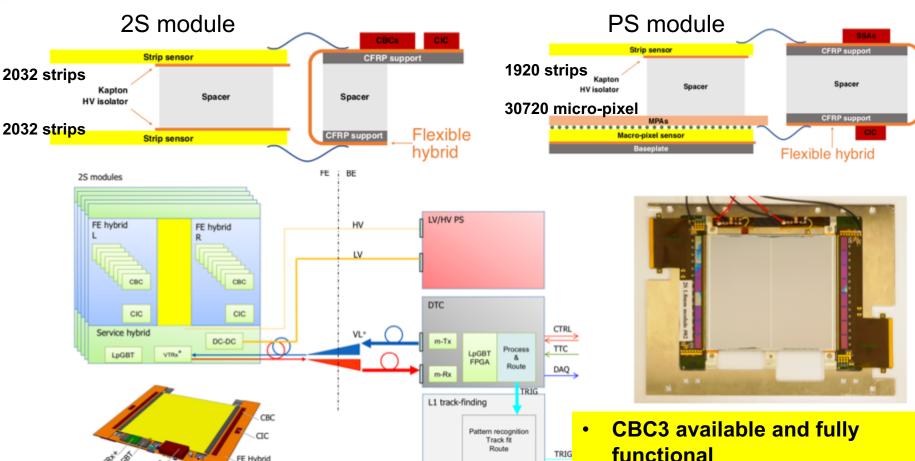


Many R&D efforts still ongoing, Global effort of silicon project at LHC

Macro pixel



Outer tracker readout

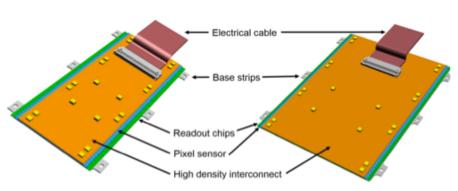


- Trigger data: 80%
- LpGBT: transfer data and distribute clock/control signal
- Filp-chip bump bonding for FE ASICs

- MPA/SSA full size chip received
- **CIC specs finalized**
- **IpGBT expect for users in 2018**
- **DC-DC** powering
- **Hybrids: new orders**

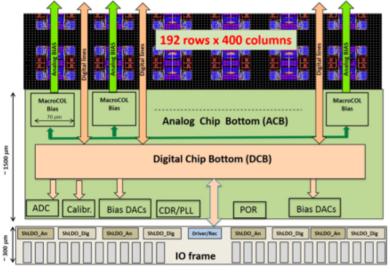


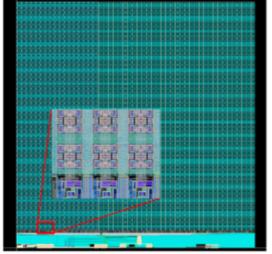
Inner Tracker readout



1*2(L) and 2*2(R) modules

Technology	65 nm CMOS		
Chip size	22 mm × (16.4 mm + 2 mm)		
Pixel size	$50 \times 50 \mu\text{m}^2$, $25 \times 100 \mu\text{m}^2$		
Number of pixels	144 320		
Detector capacitance	< 100 fF (200 fF for edge pixels)		
Detector leakage current	< 10 nA (20 nA for edge pixels)		
Detection threshold	< 600 e-		
In-time threshold	< 1200 e-		
Hit rate	< 3 GHz/cm ²		
Noise hit occupancy	$< 10^{-6}$		
Charge resolution	4 bit ToT (Time over Threshold)		
Pixel region organization	2×2 pixels (alternatively 4×1 or 4×4)		
Hit buffer depth (2 × 2 region)	\geq 8 (for 12.5 μ s latency)		
Hit loss (dead-time + buffer loss)	< 1% at 3 GHz/cm ²		
Trigger rate	≤ 1 MHz		
Readout data rate	1-4 links at 1.28 Gb/s = max. 5.12 Gb/s		
Radiation tolerance	500 Mrad, 1 × 10 ¹⁶ n _{eq} /cm ² at −15 °C		
SEU affecting whole chip	< 0.05/hr/chip at 1.5 GHz/cm ² particle flux		
Power consumption at max. hit/trigger rate	< 1 W/cm ² including SLDO losses		
Temperature range	-40 °C to +40 °C		





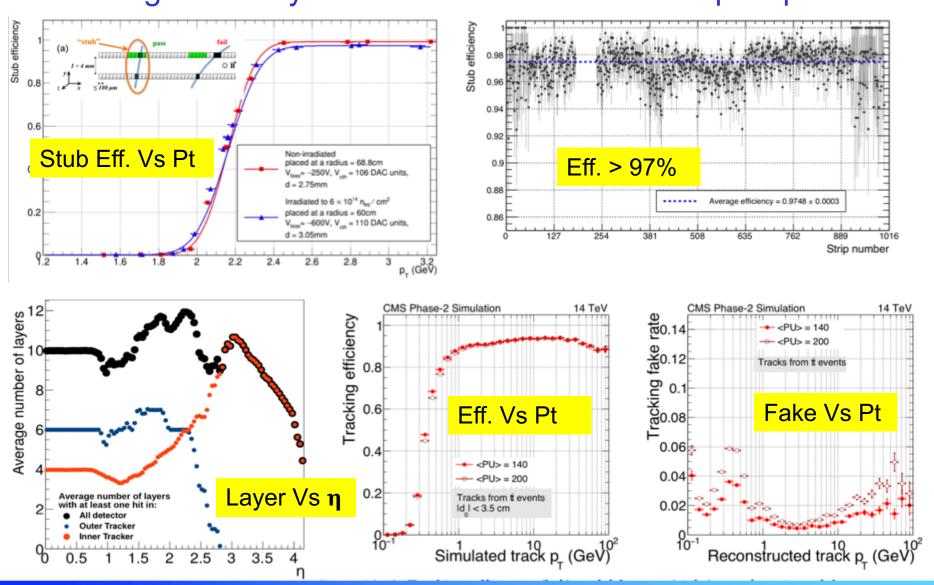
ASIC chip developed by RD53 collaboration for both ATLAS and CMS detectors

Functional overview and detailed layer out of the demonstrator chip

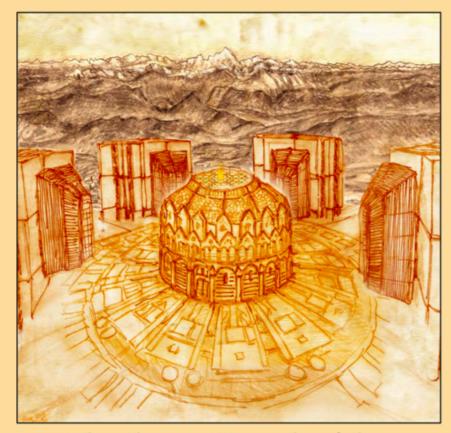


Phase II Tracker performance

Tracking efficiency is robust w.r.t. radiation and pileup



CMS



The Phase-2 Upgrade of the CMS Endcap Calorimeter

Technical Design Report

CMS

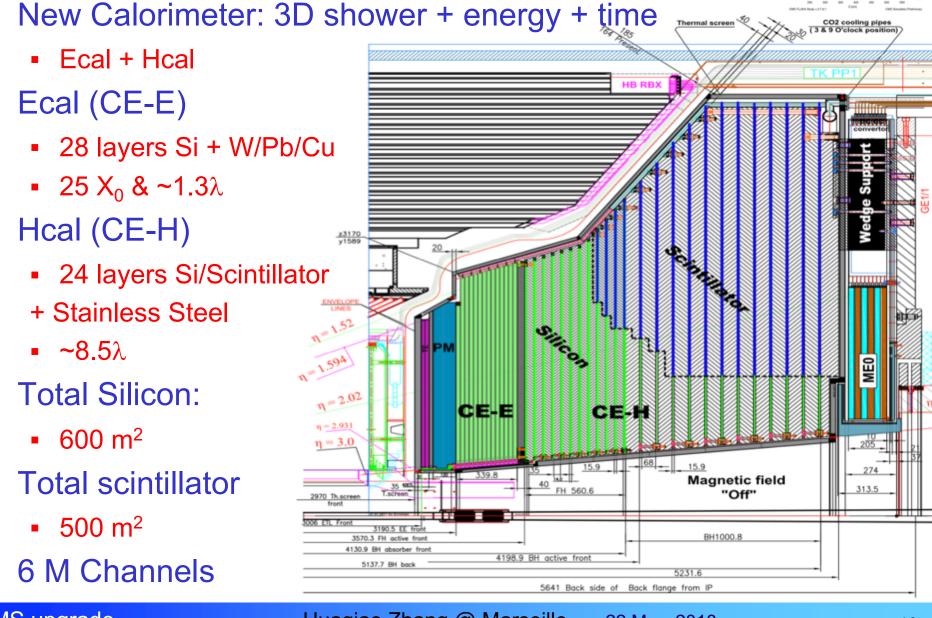


The Phase-2 Upgrade of the CMS Barrel Calorimeters
Technical Design Report



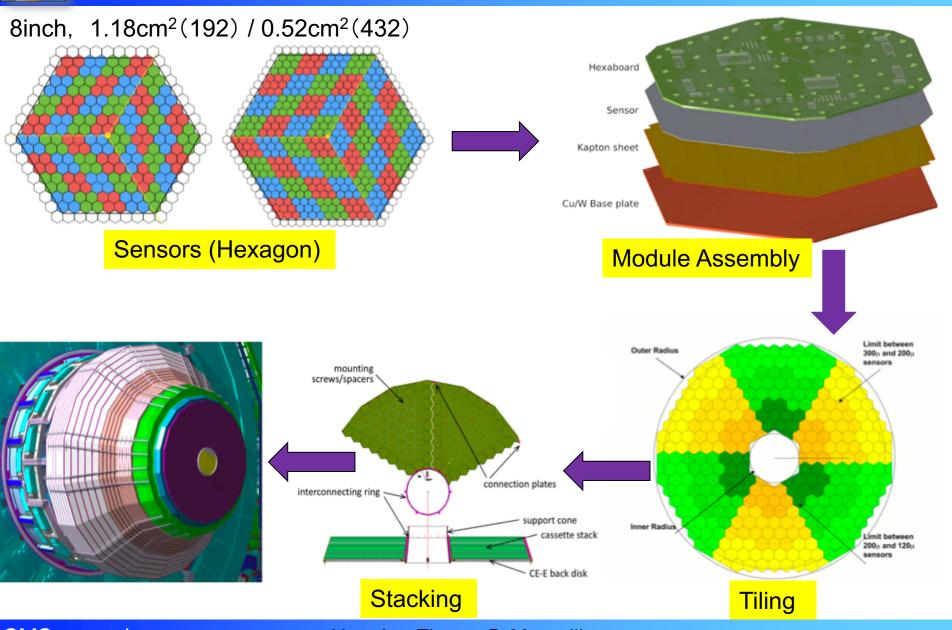
The Phase II EndCap Calorimeter Overview

- Ecal + Hcal
- Ecal (CE-E)
 - 28 layers Si + W/Pb/Cu
 - 25 X₀ & ~1.3λ
- Hcal (CE-H)
 - 24 layers Si/Scintillator
 - + Stainless Steel
 - ~8.5λ
- Total Silicon:
 - 600 m²
- Total scintillator
 - 500 m²
- 6 M Channels





The HGCal design



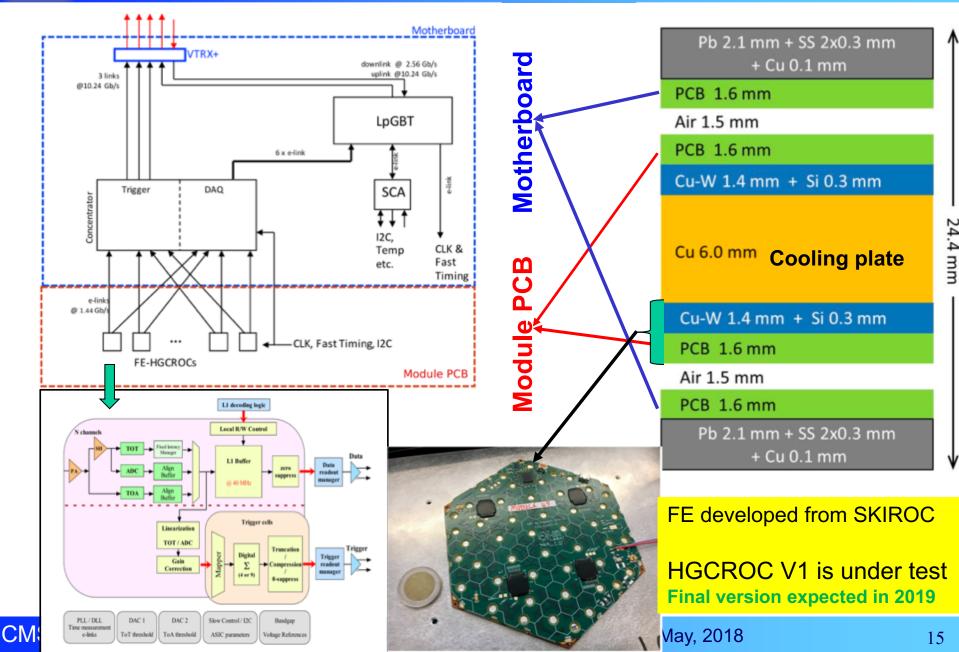
CMS upgrade

Huaqiao Zhang @ Marseille

22 May, 2018



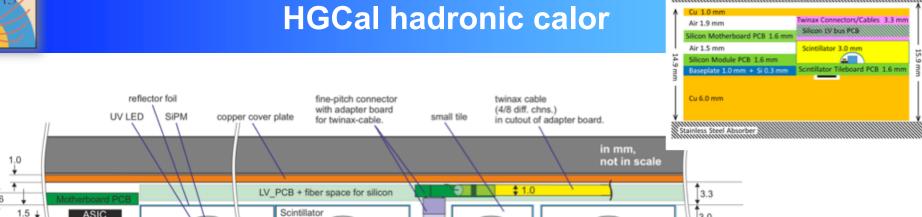
HGCal readout





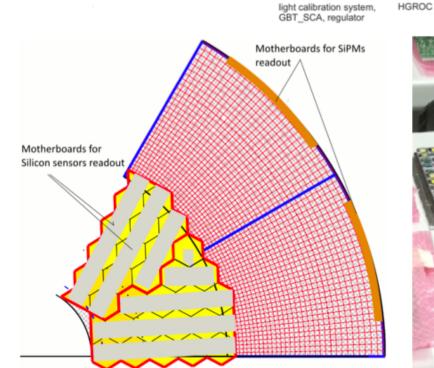
Silicon + Baseplate

Copper Cooling Plate



<-- 10 -->

polyimide isolation (50µm)



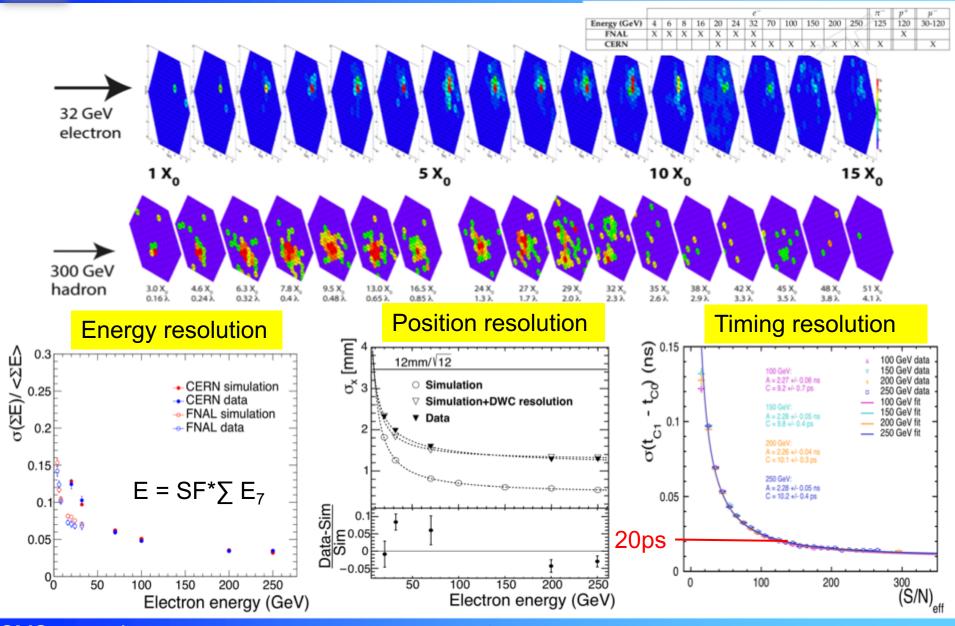
Scintillator Front-End Board



Stainless Steel Absorber

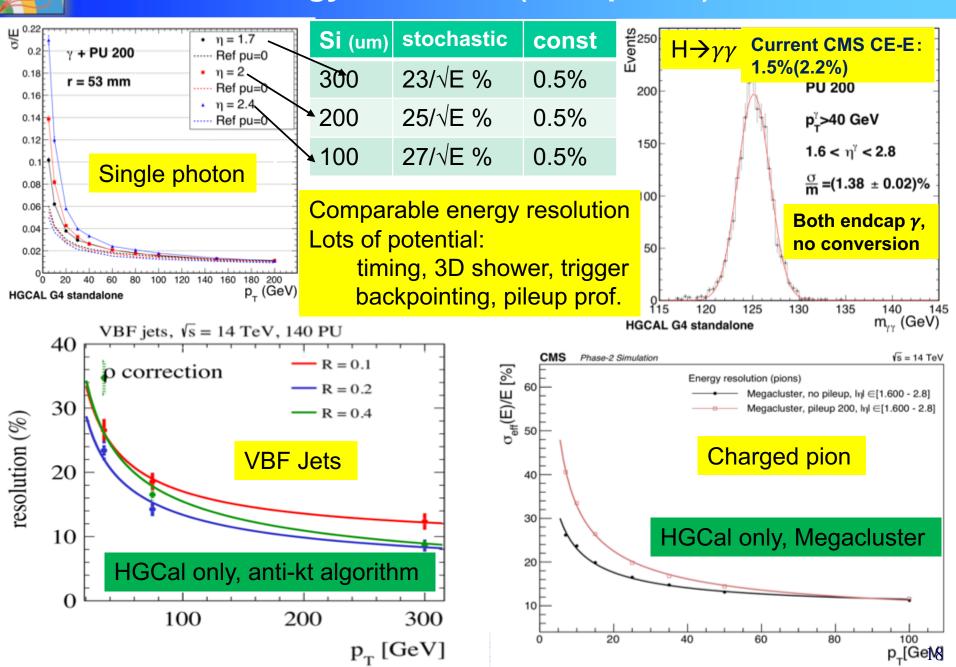


HGCal performance from beam test





Energy resolution (not-optimal)





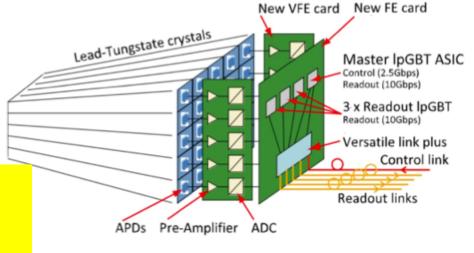
Phase II barrel calorimeter overview

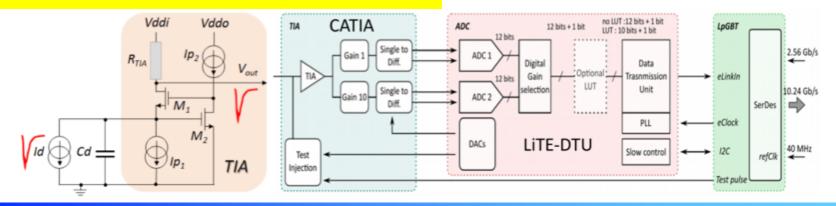
- Upgrade electronics only (very robust detector)
 - VFE 20ns peaking time, 160MHz sampling, 30ps resolution@30GeV e/γ
 - 40 MHz readout capability
 - FE with 10 GB IpGBT ASIC
- Operate at 9°:
 - mitigate APD aging

VFE:

- TIA: V0-ASIC concept demonstrated
- CATIA: full features 2018 (TSMC)
- LiTE-DTU: submission Nov. 2018

FE: demonstrator in 2018



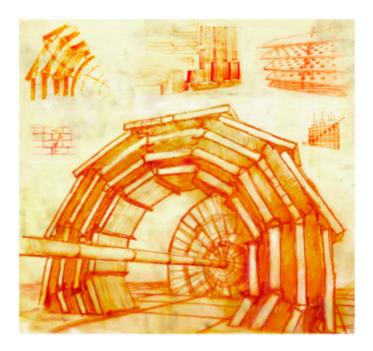


CERN European Organization for Nuclear Research

Organisation européenne pour la recherche nucléaire

CERN-LHCC-2017-012 CMS-TDR-016 12 September 2017

CMS



CERN-LHCC-2017-012 / CMS-TDR-016

The Phase-2 Upgrade of the CMS Muon Detectors
TECHNICAL DESIGN REPORT

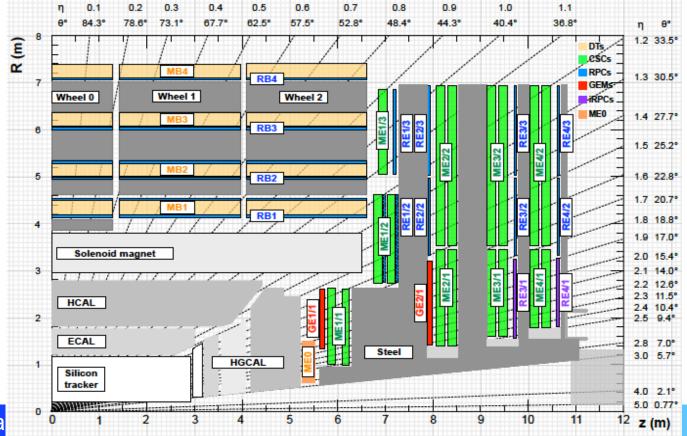
CMS upgrade

20



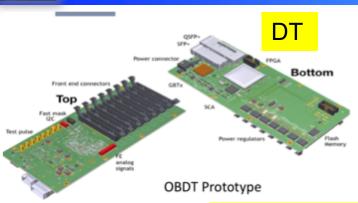
Phase II Muon upgrade overview

- Drift Tube (barrel): 40 MHz readout with improved z/t -precision
- Resistive plate chamber (barrel): readout with improved t-precision
- Cathode Strip chamber (Endcap): ME234/1 readout with higher bandwidth and latency, replace ME1 with higher radiation hard components
- New stations: GEM1/GEM2; iRPC3/iRPC4, 1.6<|η|<2.4; ME0, 1.15 <|η|<3

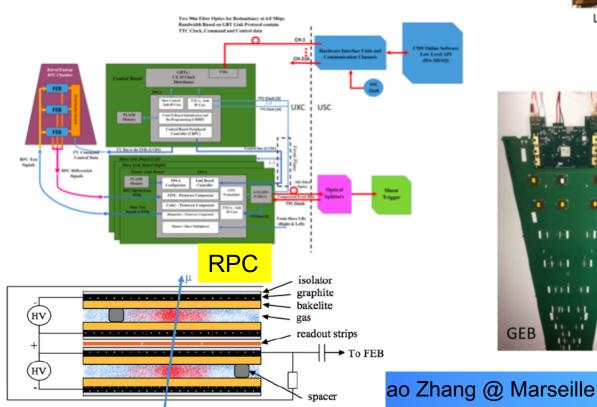


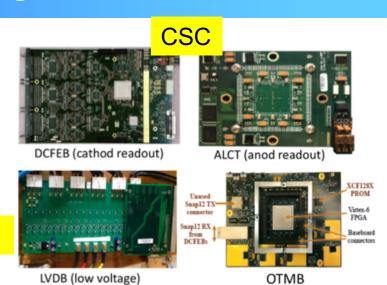


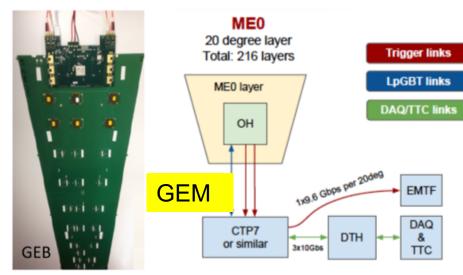
Status of muon upgrades



Prototyping of board/detector

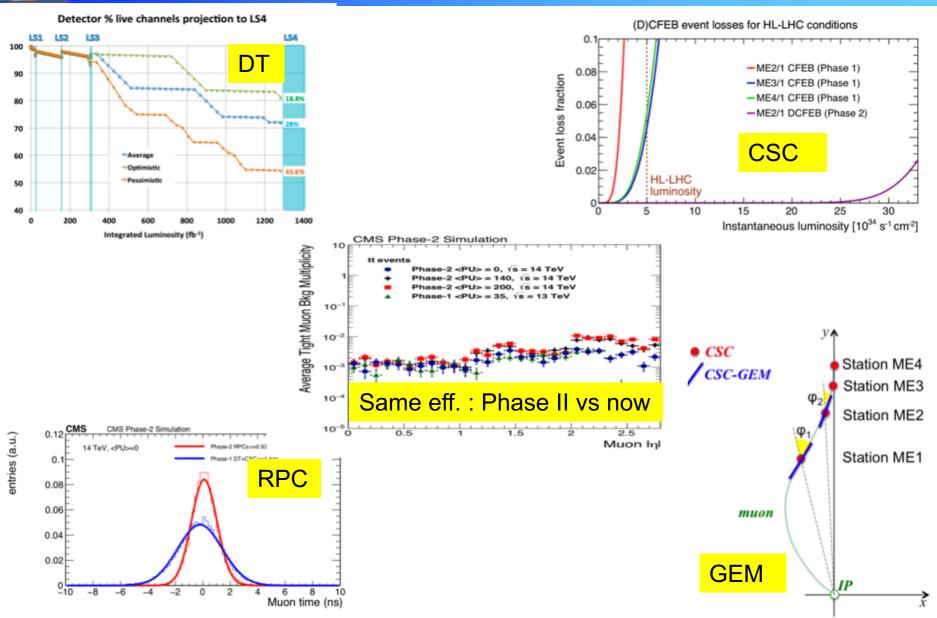








Expect improvement from muon upgrade







TECHNICAL PROPOSAL FOR A MIP TIMING DETECTOR IN THE CMS EXPERIMENT PHASE 2 UPGRADE

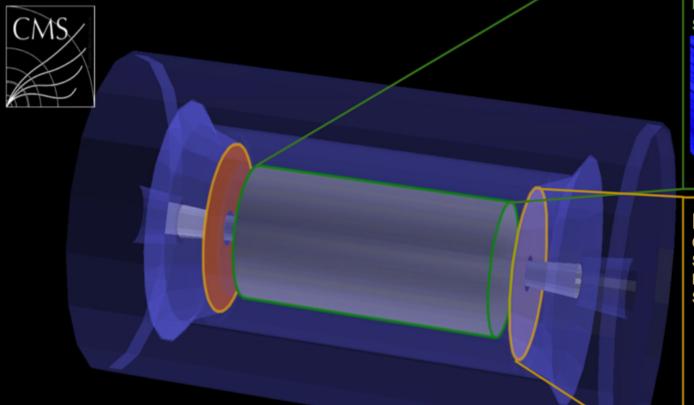
CERN-LHCC-2017-027 / LHCC-P-009 04/05/2018



Mip Timing Detector overview

Calorimeter upgrades:

- Precision timing of showers
- Provide precision timing on high energy photons in ECAL Barrel
- All photons and high energy hadrons in HGCal Endcap



BARREL

TK/ECAL interface ~ 25 mm thick

Surface ~ 40 m²

Radiation level ~ 2x10¹⁴ n_{eq}/cm² Sensors: LYSO crystals + SiPMs

11*11 mm²/cell



ENDCAPS

On the CE nose ~ 42 mm thick

Surface ~ 12 m²

Radiation level ~ 2x10¹⁵ n_{eq}/cm²

Sensors: Si with internal gain (LGAD)
1*3 mm²/cell

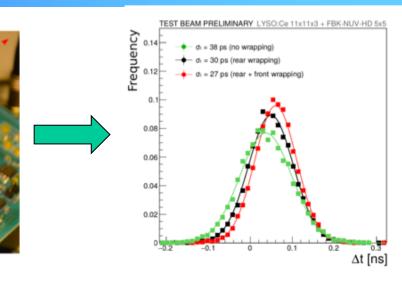


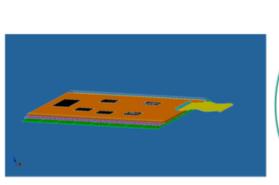
- Thin layer between tracker and calorimeters
- MIP sensitivity with time resolution of ~30 ps (40 ps end of life)
- Hermetic coverage for |η|<3

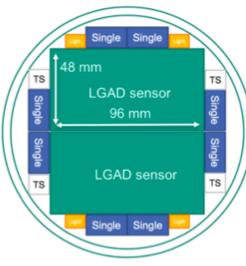


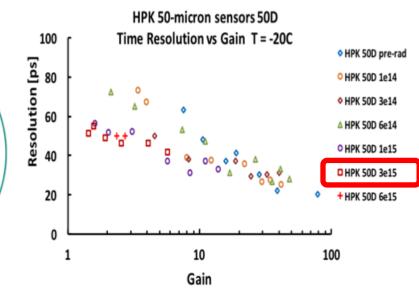
R&D of MTD





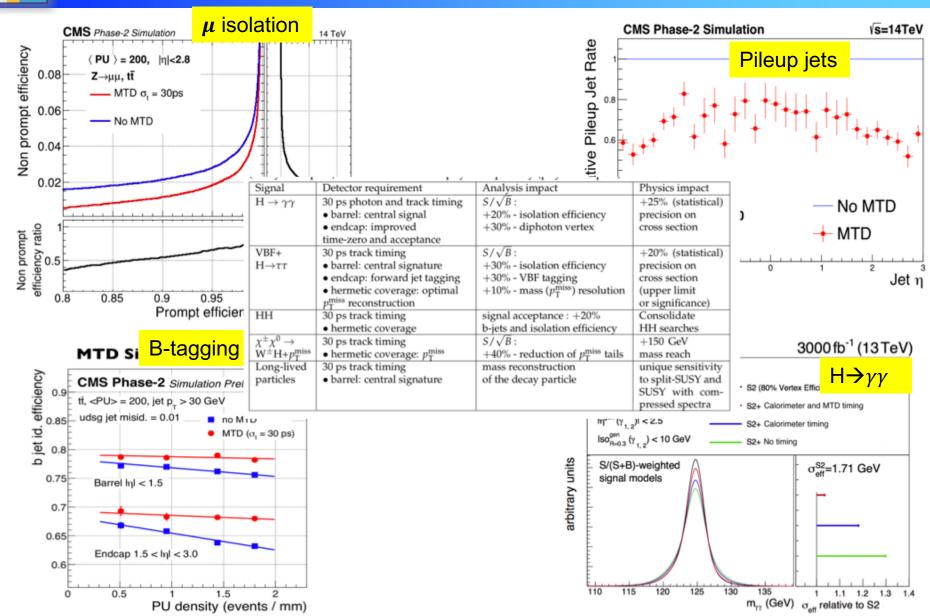








Expected improvement with MIP timing detector







CERN-LHCC-2017-013 CMS-TDR-017 September 12, 2017

CM , 2017 CERN-LHCC-2017-014 CMS-TDR-018 12 September 2017

The Phase-2 Upgrade of the CMS
Level-1 Trigger

Interim Technical Design Report

CMS Collaboration

CERN-LHCC-2017-014 / CMS-TDR-01 09/02/2018

The Phase-2 Upgrade of the CMS DAQ
Interim Technical Design Report

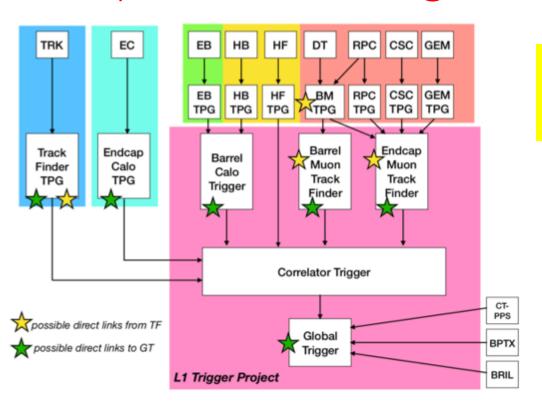
CMS Collaboration

CMS

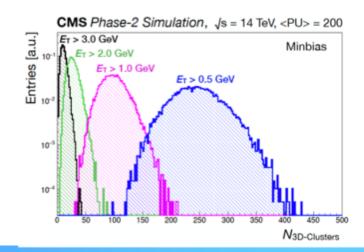
L1-Trigger

- L1-Trigger
 - Tracks in trigger @ 40 MHz
 - >50 Tb/s input
 - 12.5 us latency
 - Accept rate: 500kHz/750 kHz @ 140 PU/200 PU





New firmware and new ideas to handle challenging trigger: PF @ L1, 3D calor clusters

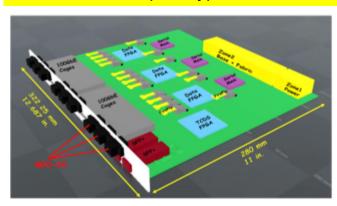


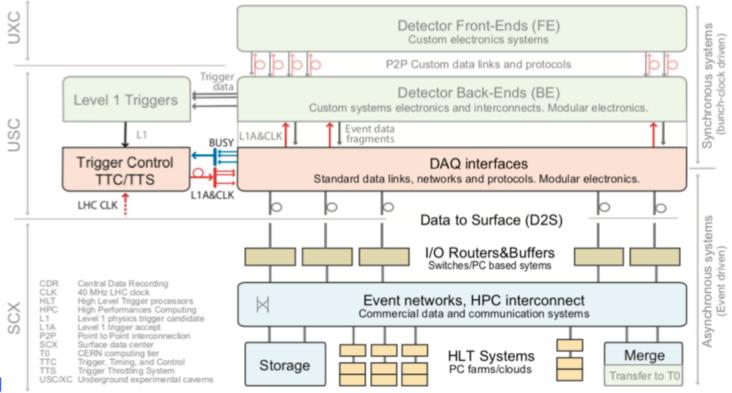


DAQ and HLT

- New TCDS high speed serial distribution of precise clock and multiple triggers data steam
- Systems interface with DTH ATCA boards
- HLT output at 7.5 kHz, 4.5 MHS06 for 500 kHz (140 PU) in LS3 plus 4.7 MHS06for 750 kHz (200 PU) staged to LS4

First ATCA DTH prototype in Q4 2018

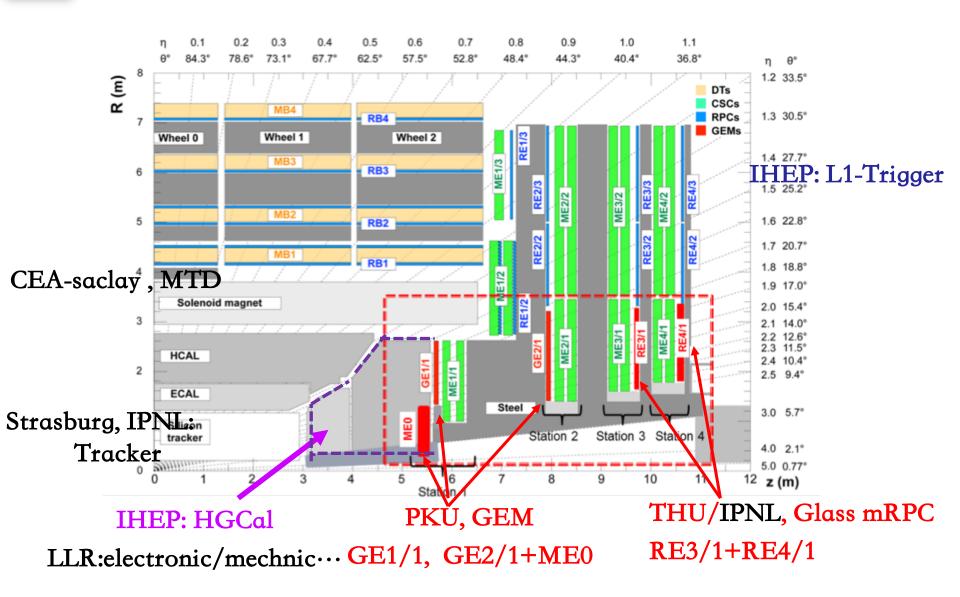




CMS upgra



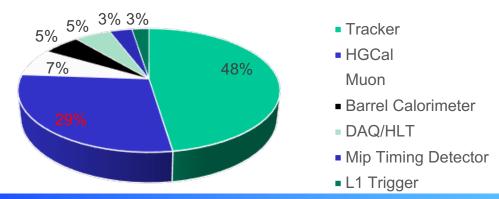
French/Chinese in CMS Phase II upgrade





Summary

- CMS Phase II upgrades have been approved by LHCC/UCG and CERN RB recently
 - L1-Trigger, DAQ/HLT and MIP Timing Detector TDRs is coming
- Production MoU expected to be proceed in coming RRB
- Progress being developed
 - Key components such as silicon sensor, gas detectors
 - Electronics such as various FE ASICS, IpGBTs
 - Improvements to detector performance/physics outcome
- France/Chinese collaborate on CMS upgrades
 Phase II Costs Fraction

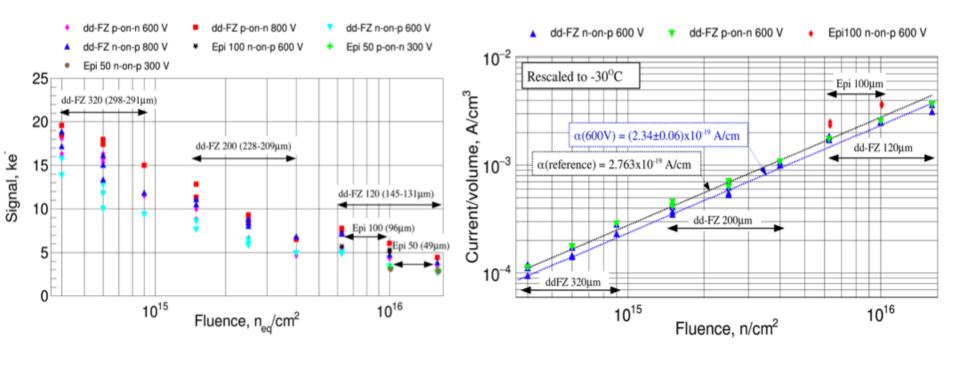




backup

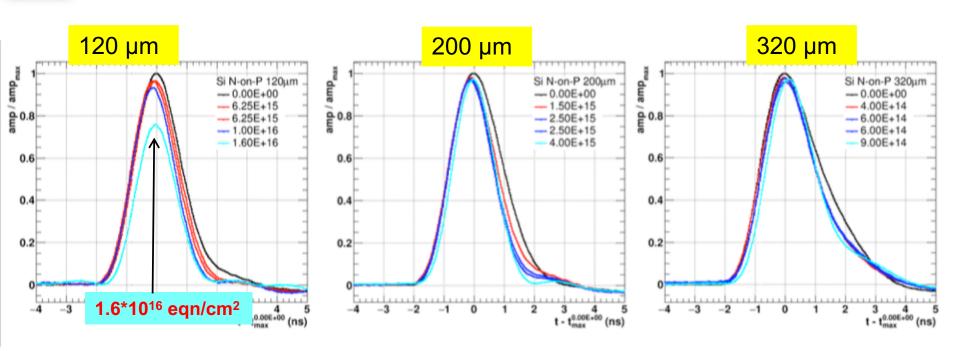


Silicon sensor radiation tests





Silicon sensor irradiation tests

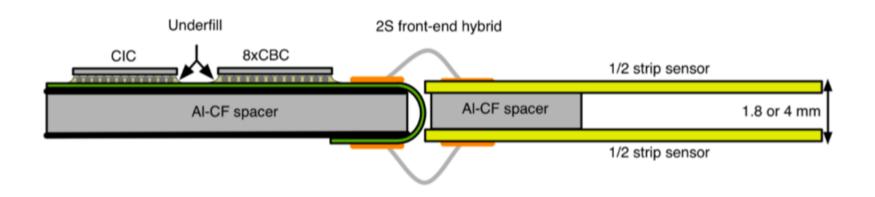


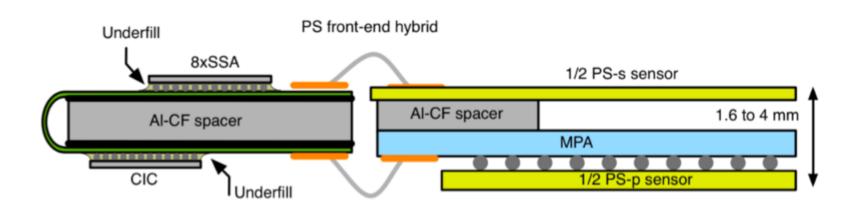
- Si N-on-P sensor produced by Hamamatsu
- 120, 200, 320 um tested
- Neutron radiation up to 1.6*10¹⁶ 1MeV n/cm²(120um)
- Beam and Sr⁹⁰ tests give consistent results

Pulse shape after radiation changed marginally

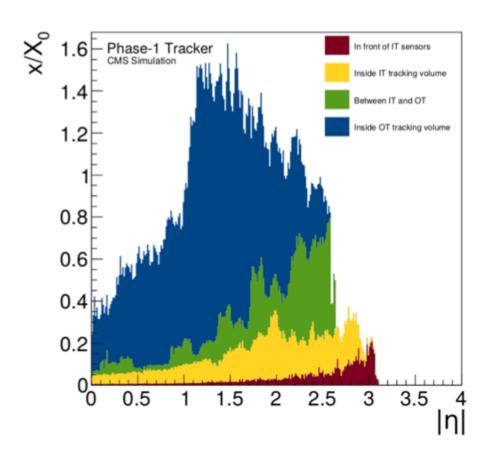


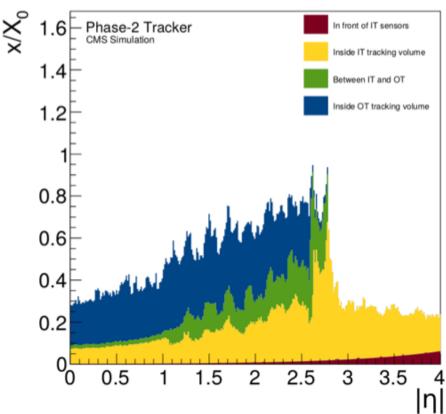
Out tracker module











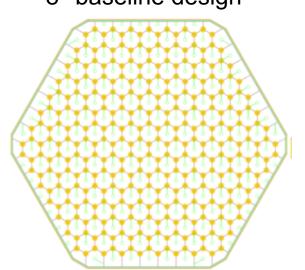


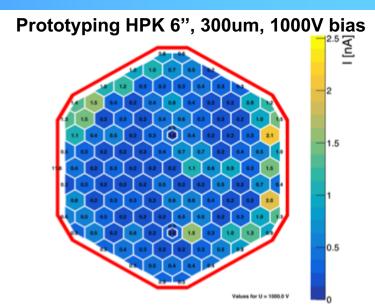
SiO₂

p-bulk

Silicon sensors





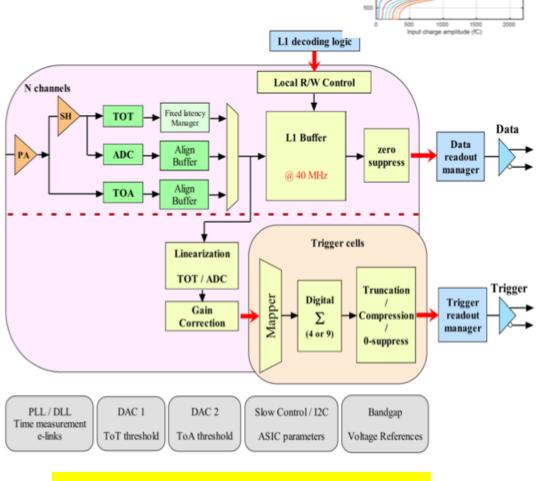


Active thickness (μ m)	300	200	120
Area (m²)	245	181	72
Largest lifetime dose (Mrad)	3	20	100
Largest lifetime fluence (n_{eq}/cm^2)	0.5×10^{15}	2.5×10^{15}	7×10^{15}
Largest outer radius (cm)	≈180	≈100	≈70
Smallest inner radius (cm)	≈100	≈70	≈35
Cell size (cm ²)	1.18	1.18	0.52
Initial S/N for MIP	11	6	4.5
Smallest $S/N(MIP)$ after 3000 fb ⁻¹	4.7	2.3	2.2



Silicon part Front End readout

- Requirements (not a full list)
 - Radiation hard (of course)
 - Low noise: <2500e
 - Including sensor leakage current noise
 - Shape time: <20 ns
 - Sensor pulse Shape is 1-2 ns
 - Dynamic Range 0.2fC -10 pC
 - High gain for < 100 fC
 - ToT for ~100 fC − 10 pC
 - Low power: ~20 mW/channel
 - total ~ 100kW for 6M channels
 - <100 ps time resolution per cell
 - 36 bunch crossings latency



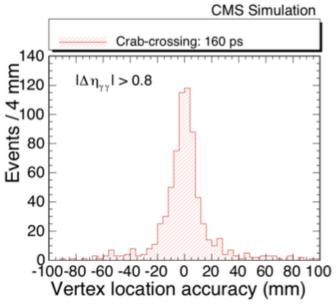
5 2000 1500

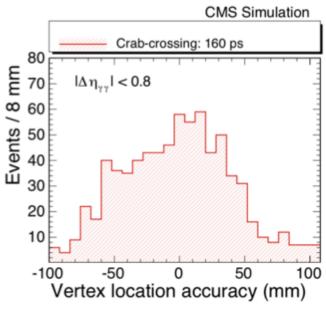
HGCROC1, Submitted July 2017

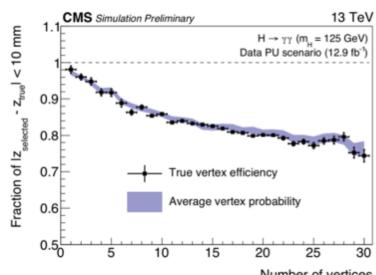
See details from Christophe de La Taille's slides



60 ps time resolution \rightarrow 2 cm position resolution for $|\Delta \eta_{\gamma\gamma}|$ <0.8







CMS upgrade Number of vertices May, 2018 40