

High rate capabilities of tracking arrays

Performances of the AGATA system (tentatively)

Amel Korichi for Emmanuel Clément (GANIL) who prepared these slides

Second AGATA-GRETINA tracking arrays collaboration meeting



**4-6 April 2018
CSNSM-CNRS, Orsay**



AGATA TOPOLOGY

GTS –System :
Clock Distribution
Trigger decision

Request/Rejection/Acceptation

V1 – electronic
(ATCA)

Advanced V1 –
electronic (GGP)

AGATA - Capsules

PAC signal (37/core)

PCIe – I/O

NARVAL/DCOD - online

CrystalProducer

PreProcessing

PSA

Consumer

Raw Traces Written
Histograms

Histograms

Histograms

Histograms
Psa-hits.adf

Ancillaries

Raw Data

Replay – near line

PostPSA

Builder
TimeStamp

Ancillaries

Merger
TimeStamp

Tracking

Where are the bottlenecks ?

GTS -System :
Clock Distribution
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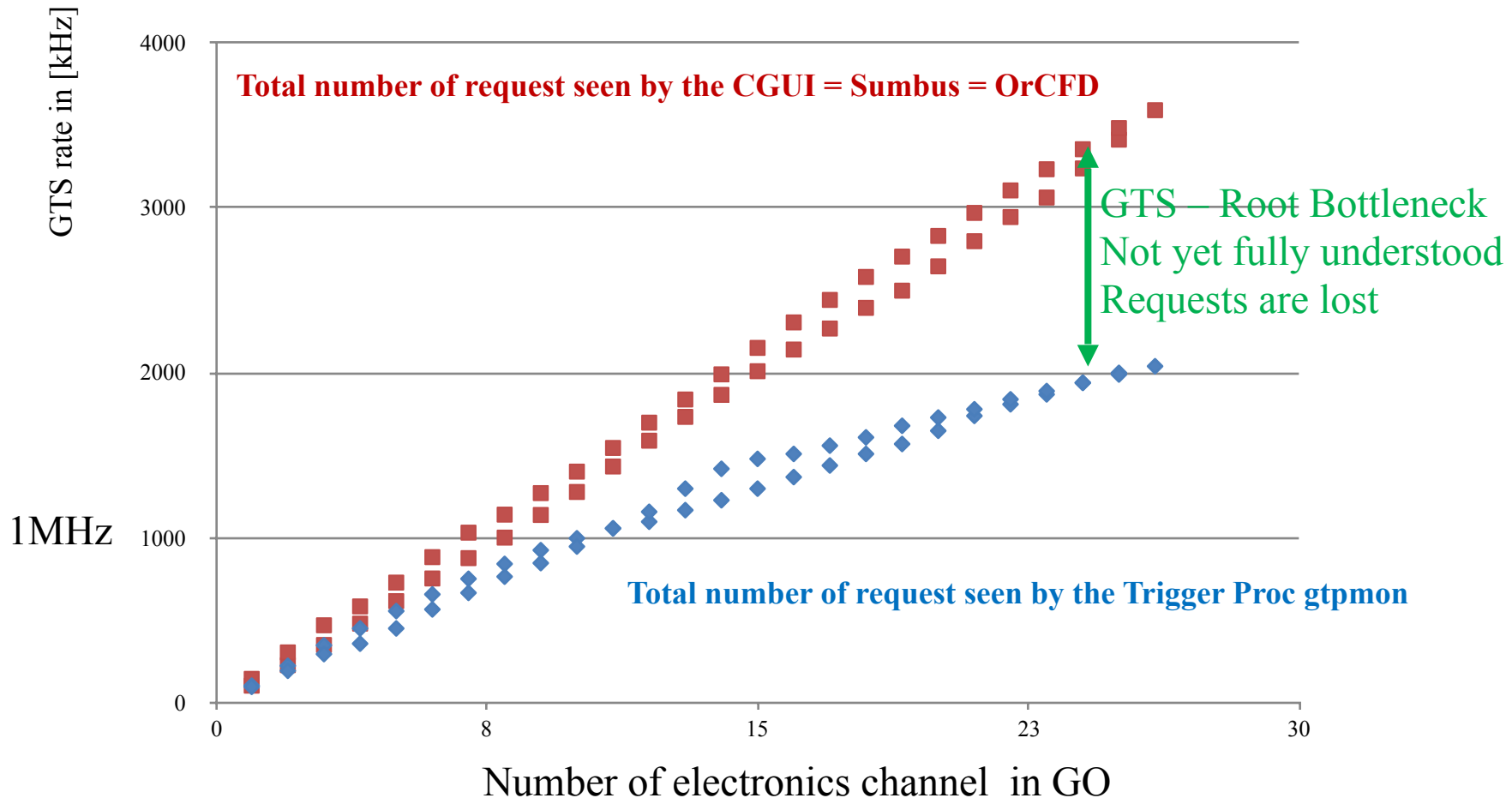
Tracking

1

Before the readout : how much rate the trigger system can manage ?

No Readout ! Just request to the trigger system

Electronics on Drain (no data pre-processing/DAQ)

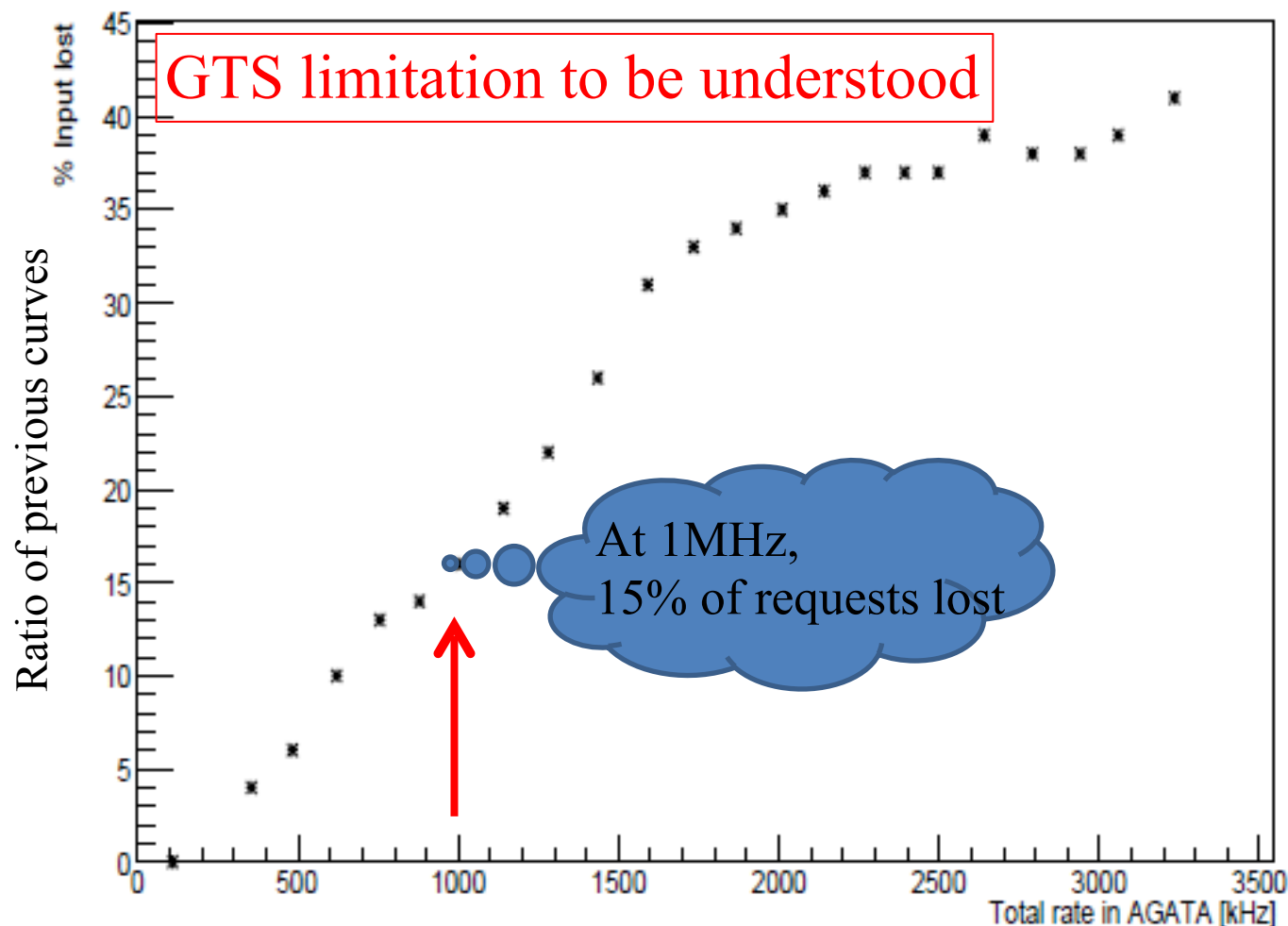


1 Before the readout : how much the trigger system can manage ?

No Readout ! Just request to the trigger system

Electronics on Drain (no data pre-processing/DAQ)

High rate effect 2017 GANIL MBq ^{60}Co source (32 crystals) ATCA + GGP

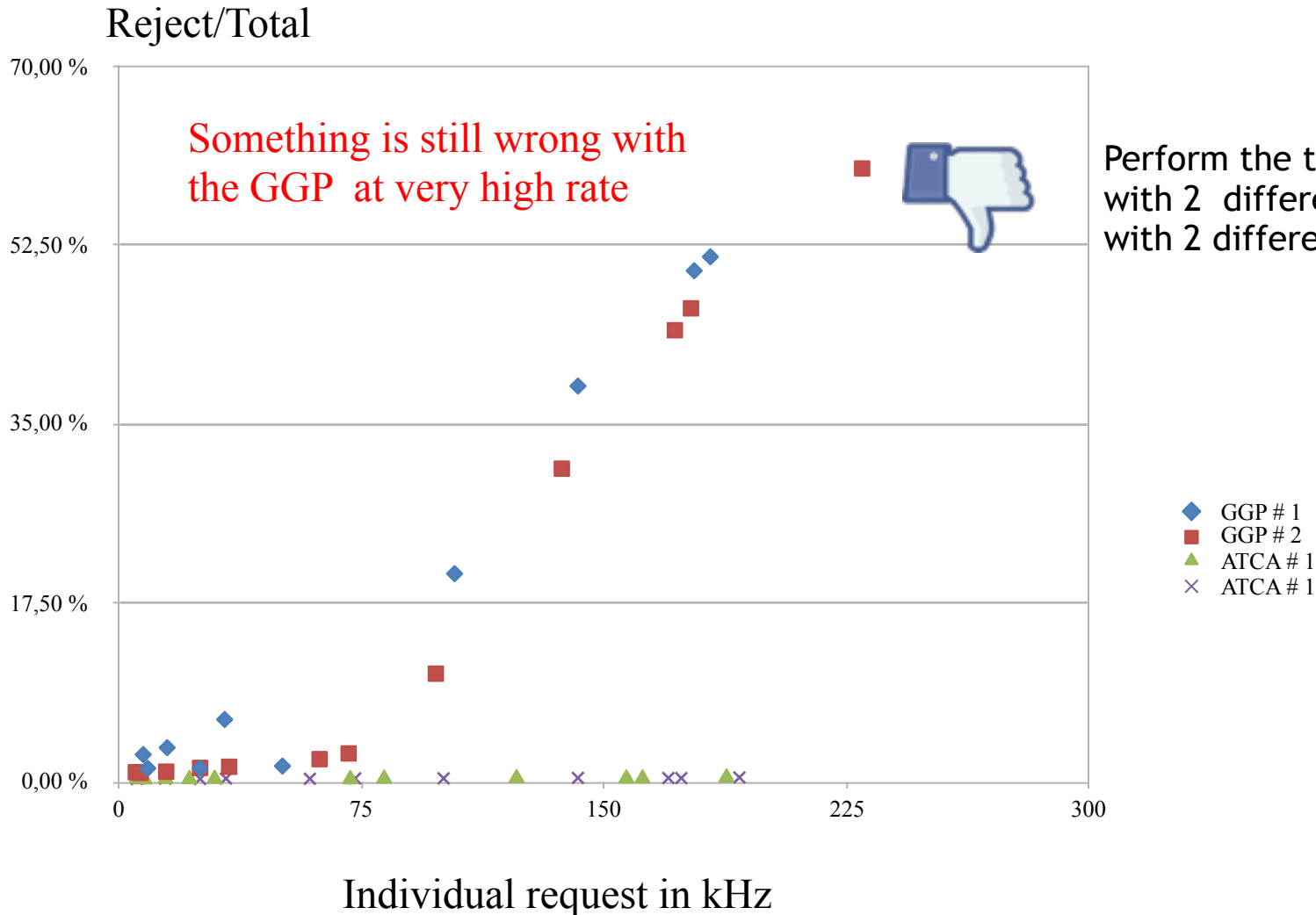


We often run at this rate at GANIL

1 Before the readout : how much the trigger system can manage ?

Only 1 channel is in GO for each point (no pre-processing/DAQ readout)

Just Increase the rate :



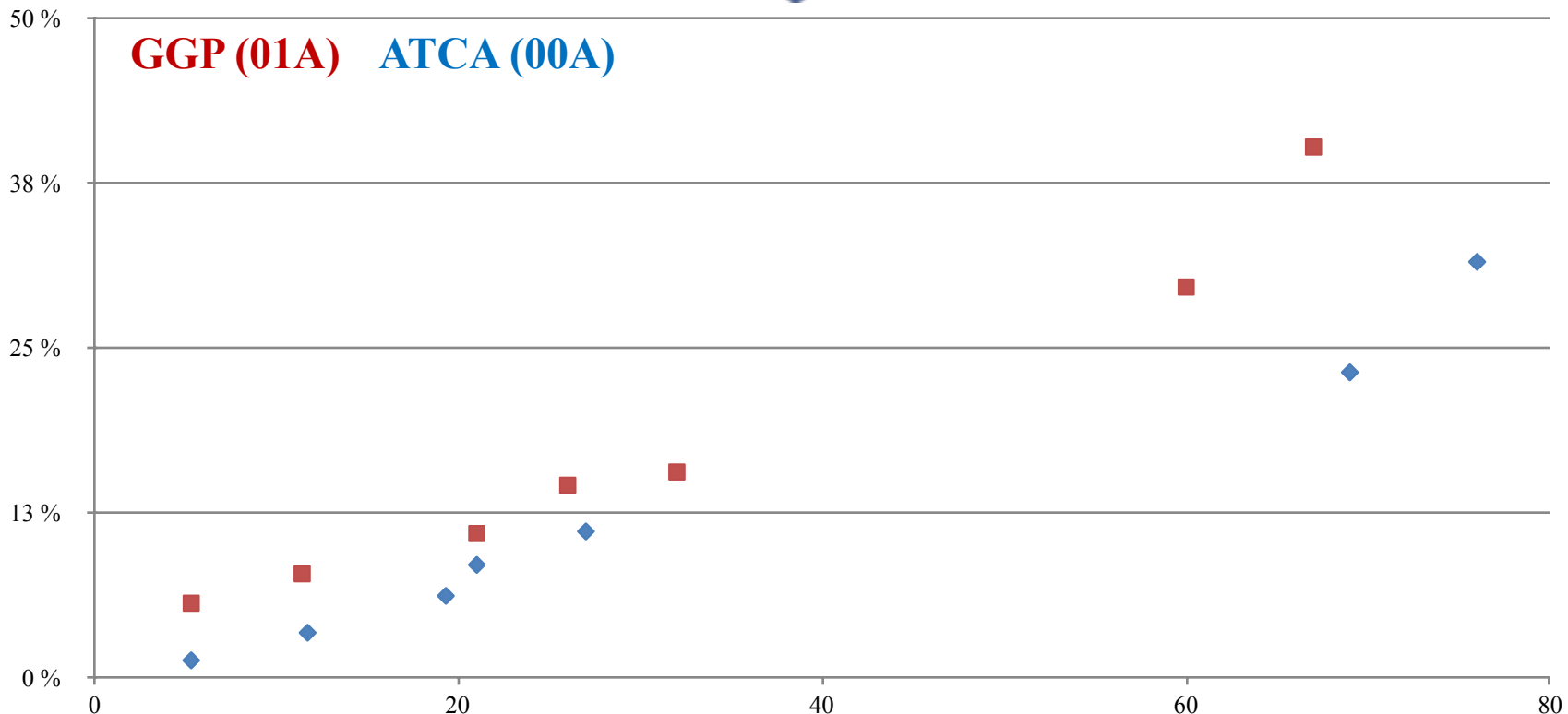
Perform the test with 2 different GGP cards with 2 different ATCA cards

ALL ATCA and GGP electronic channels ON
Varying the source activity



→ GGP influences the ATCA dead Time via the
GTS-Backpressure common flag

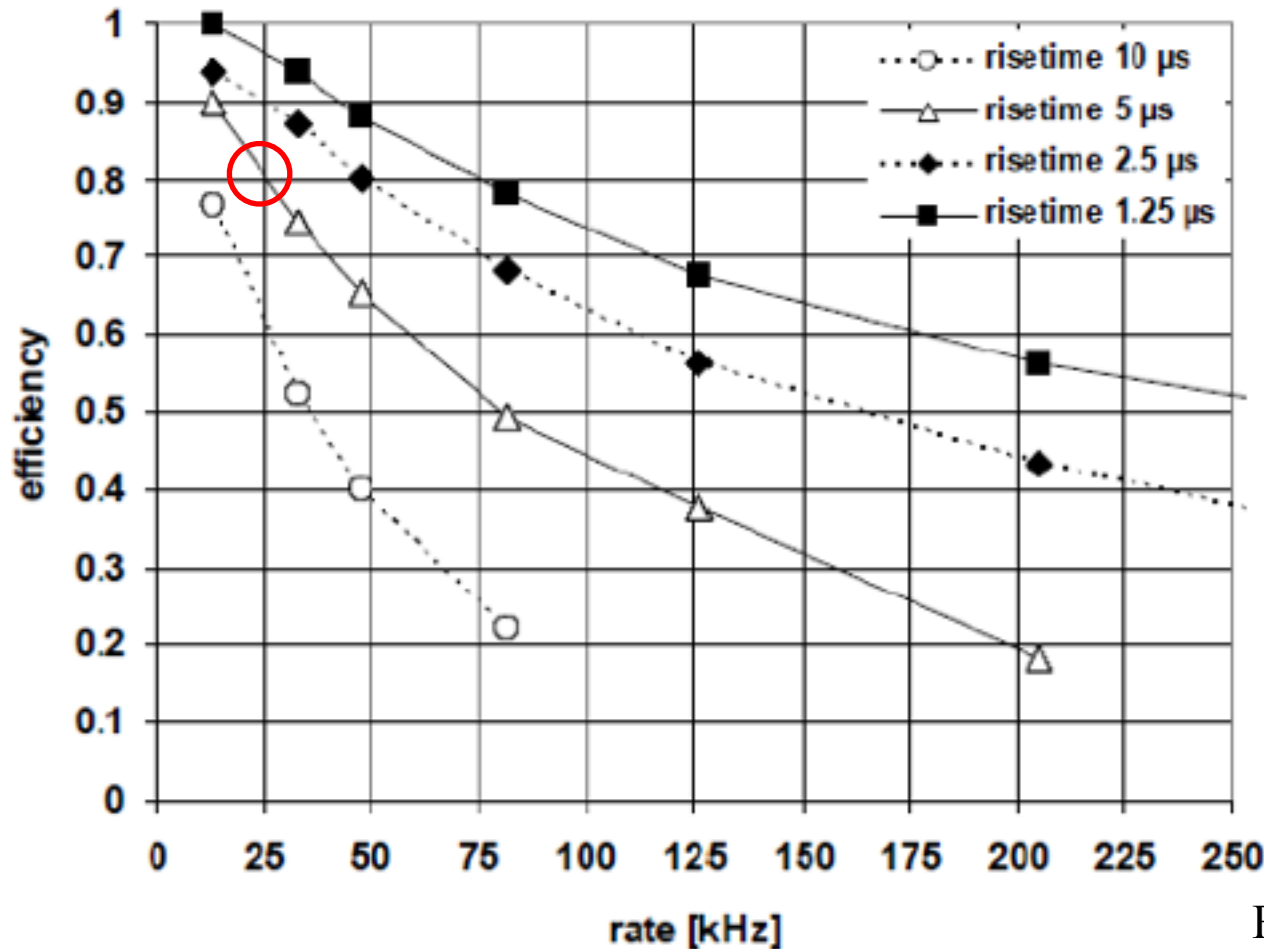
Individual Dead Time



Individual request in kHz

Trapezoidale filter paramaters matter !

At High rate, pile-rejection reduces a lot the efficiency and quality of the data



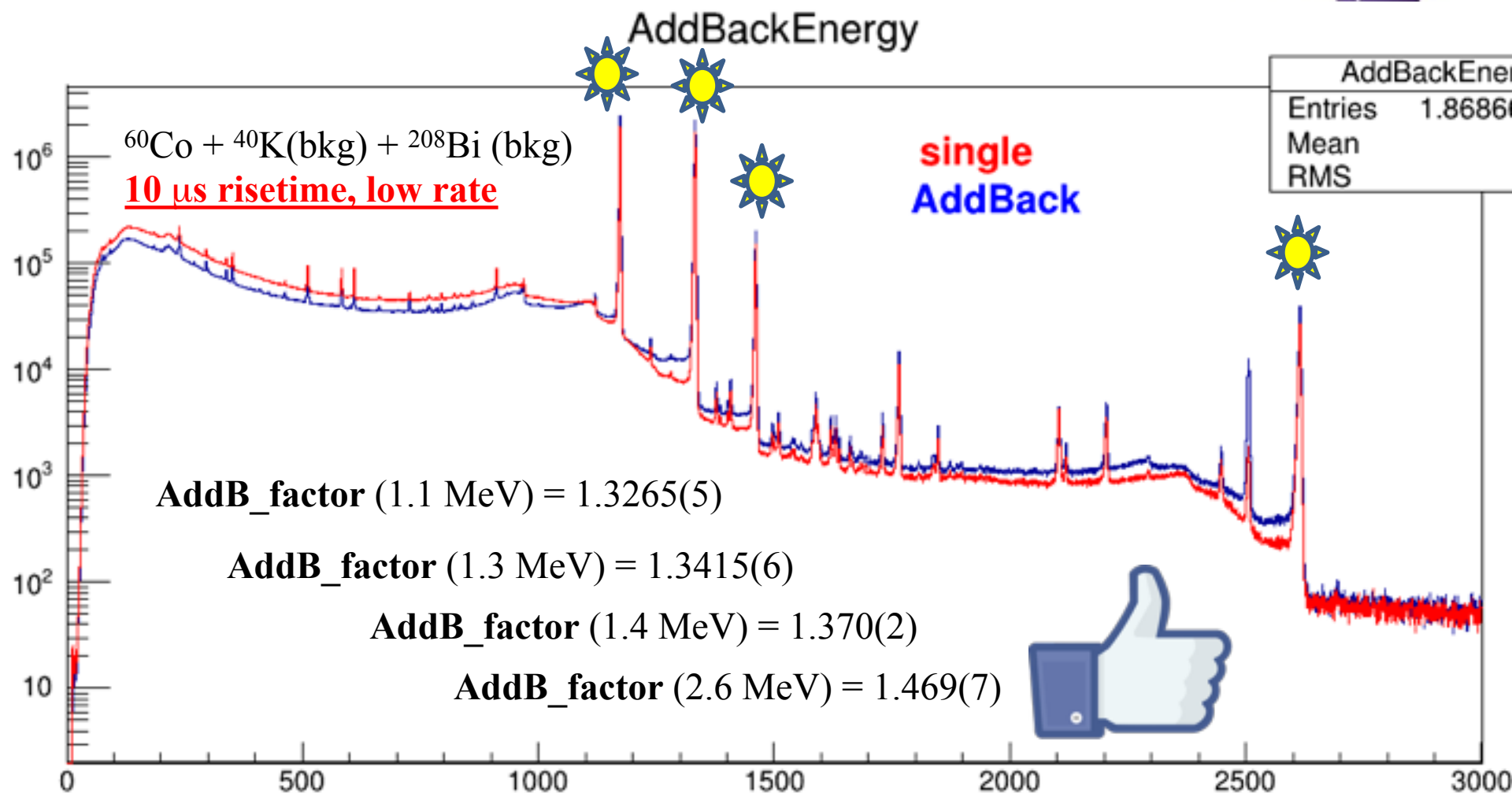
→ at 25kHz/core, a too large risetime (k-parameter) costs 20% of losses (this is known)

F. Recchia et al. LNL report

1

Before the readout : how much the trigger system can manage ?

Inappropriate RiseT vs AddBack factor



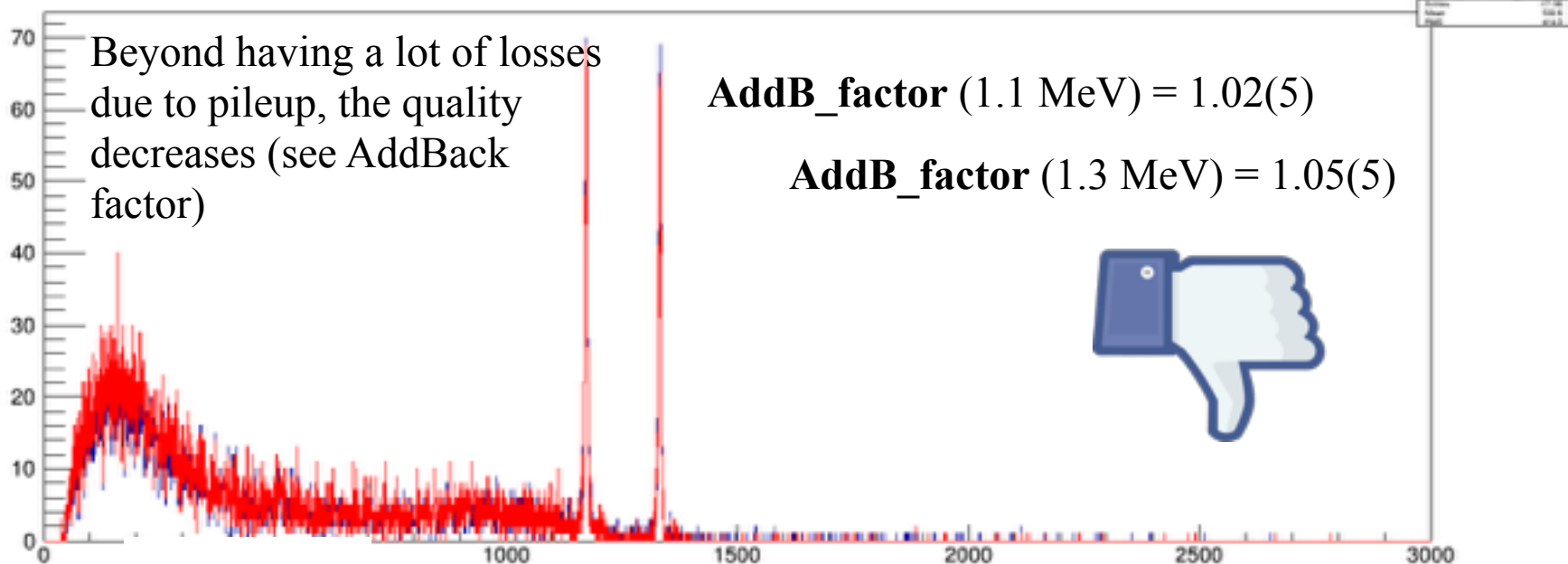
To make it easy : only core (E0) energies are considered. No Tracking but a simple AddBack procedure using neighboring cores $\text{AddB_factor} = \text{Int}(\text{AddB}) / \text{Int}(\text{Single})$

Before the readout : how much the trigger system can manage ?

ACAYA



10 μ s RiseT Pile up will reject 60% of the requests



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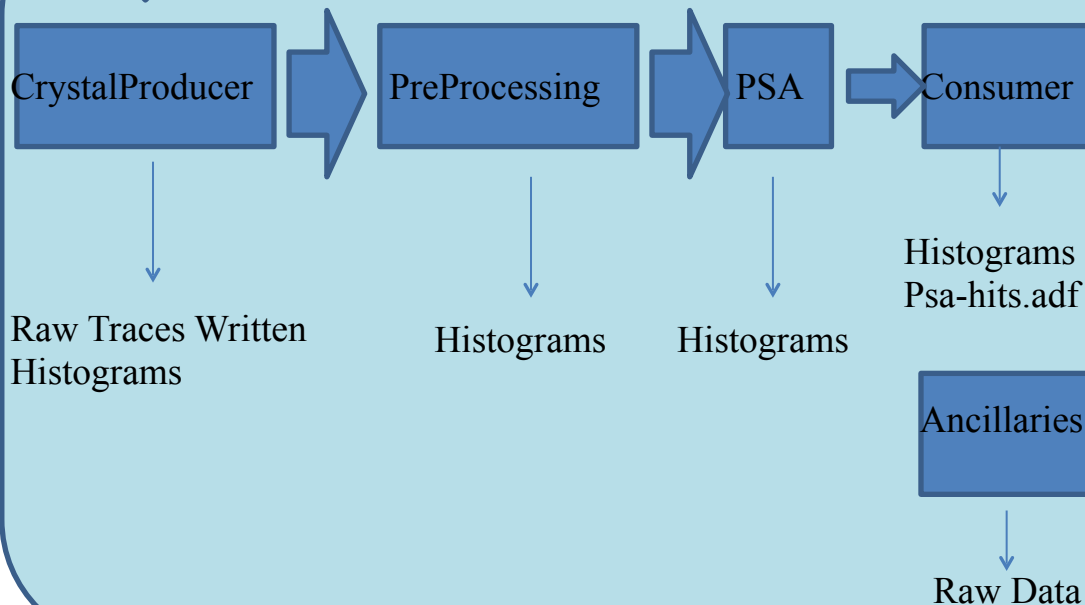
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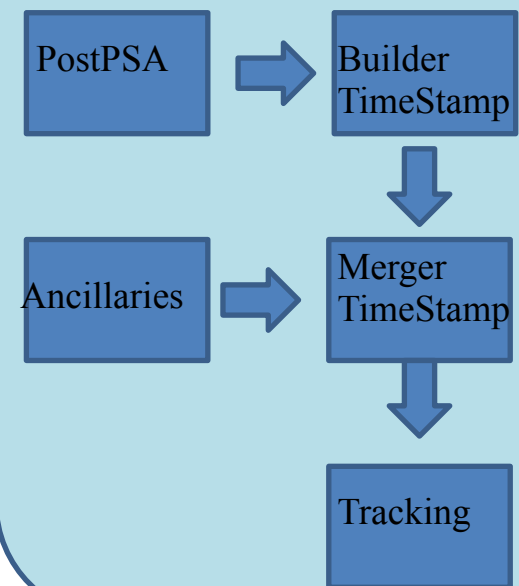
2 Now see What's going on at the
crystal producer level

2 PCIe – I/O

NARVAL/DCOD - online



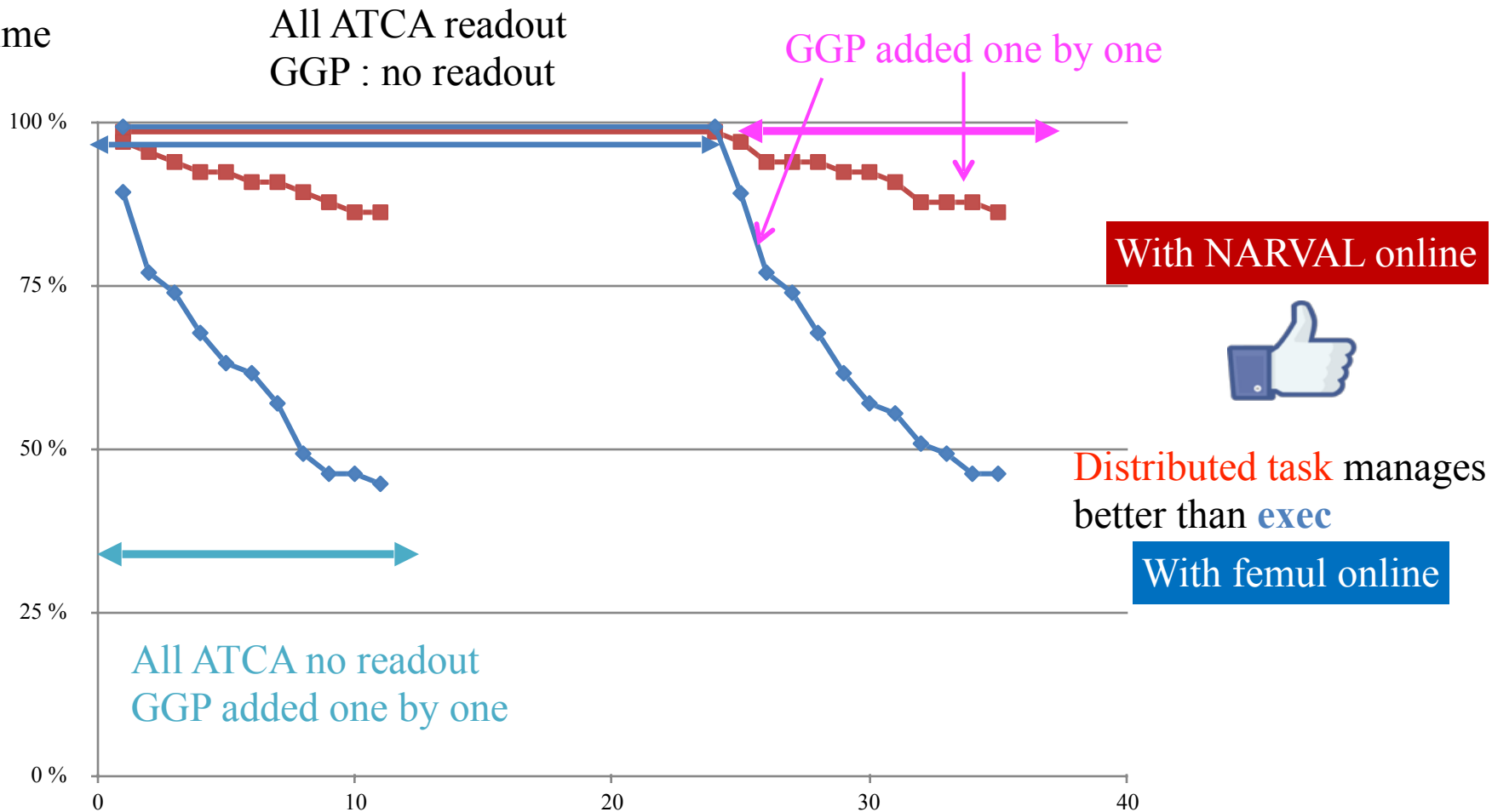
Replay – near line



2 The delicate process of the PCIe readout

Problem with the GGP readout before 2018

Global
Lifetime

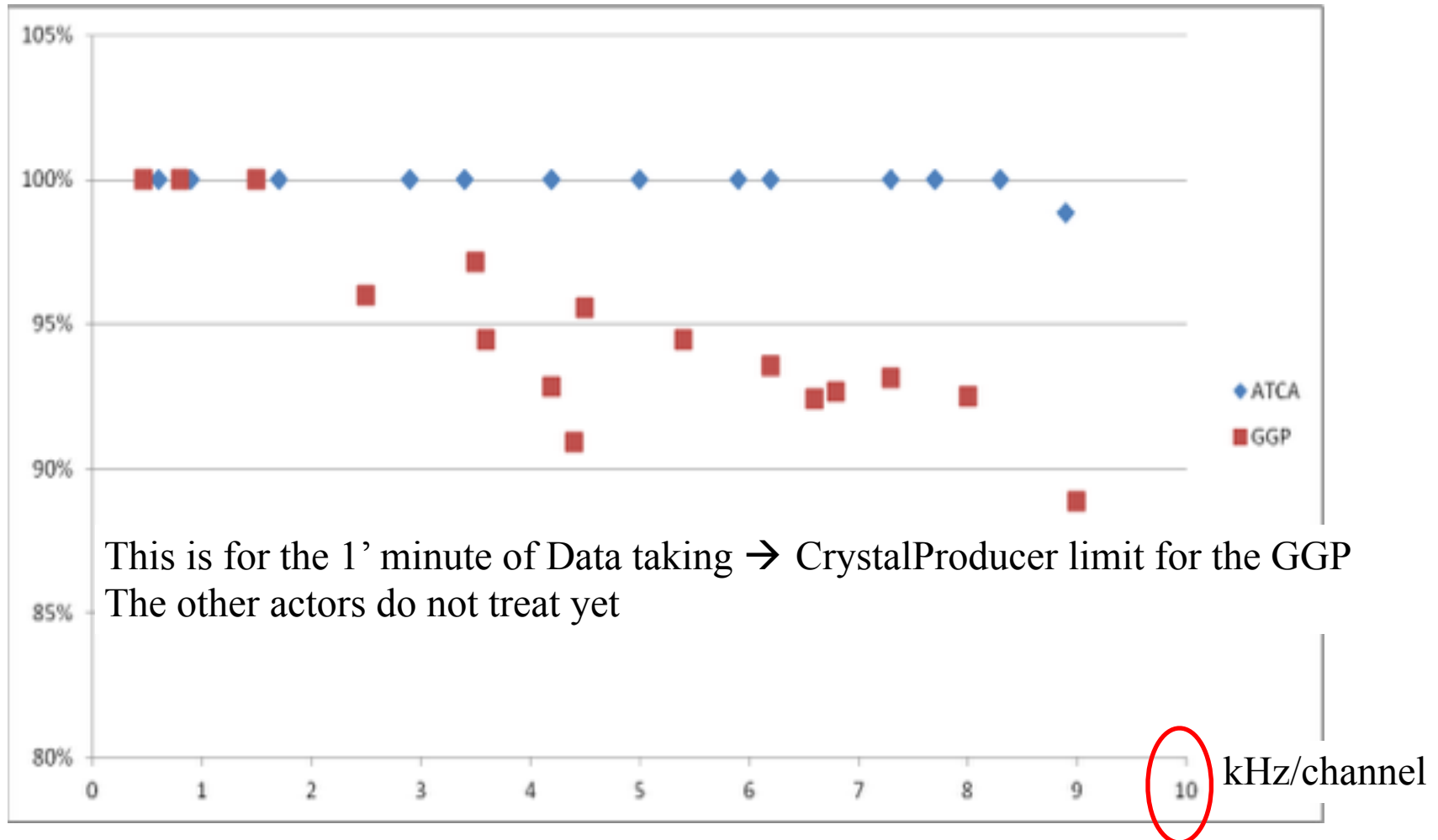


Number of channel writing data (24 ATCA + 11 GGP = 35)

~3kHz/core

Problem with the GGP readout before 2018

Individual Lifetime



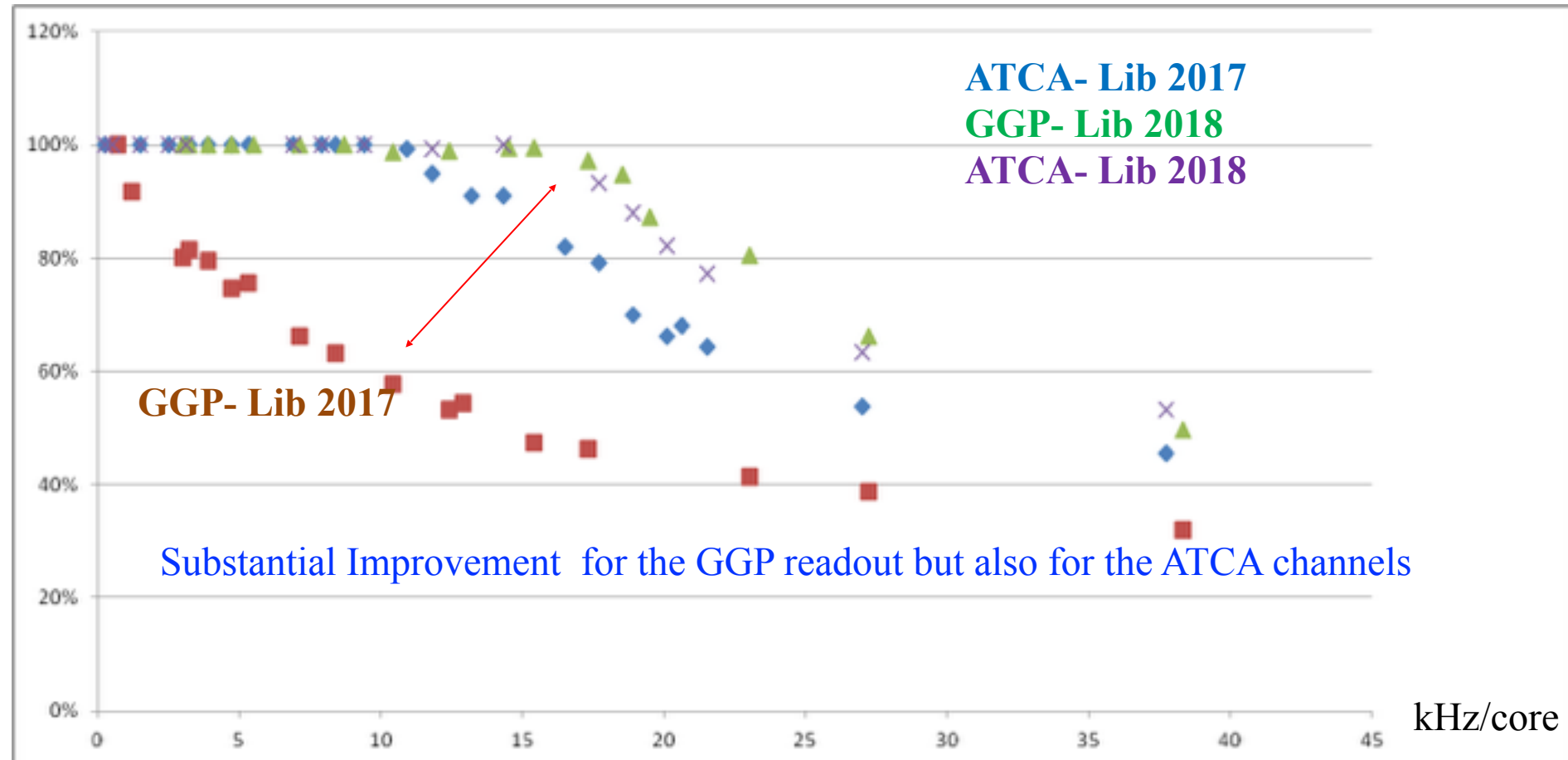
One channel at the time is taking data

No Trace, no Histo, no PSA, only adf

2 The delicate process of the PCIe readout in DCOD

Major release of the CrystalProducer in 2018 (D. Bazzacco)

→ Massive use of Threads in the DMA readout



No Trace, no PSA, no Histo, only adf files



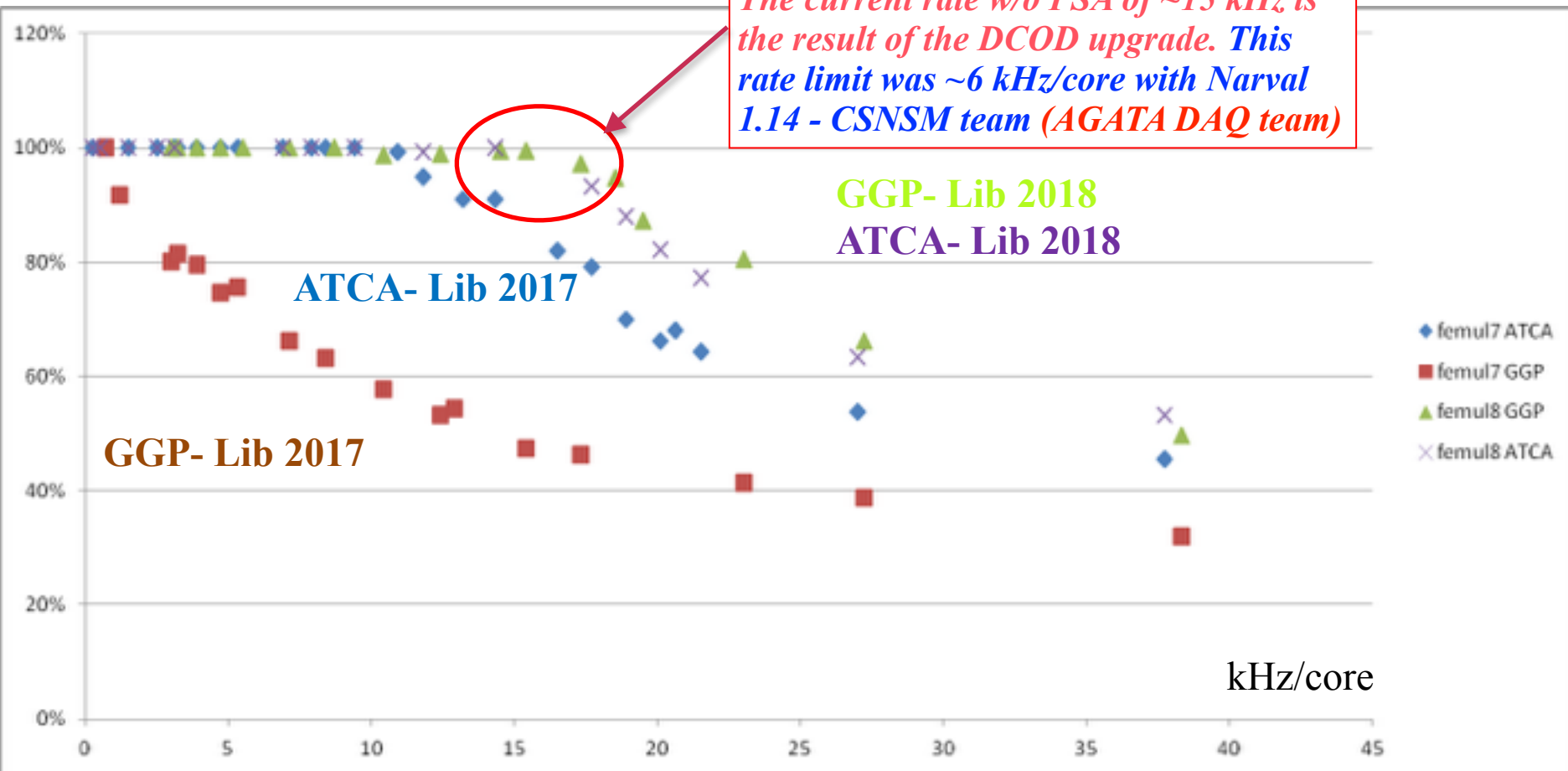
We do not yet look at the PSA limitation

The delicate process of the PCIe readout in DCOD

Major release of the CrystalProducer in 2018

→ Massive use of Threads in the DMA readout

The current rate w/o PSA of ~15 kHz is the result of the DCOD upgrade. This rate limit was ~6 kHz/core with Narval 1.14 - CSNSM team (AGATA DAQ team)



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3 4 More access to disk with traces and histograms

Workstation are busy when doing disks access to write the short traces and the histograms and do not acquire the data meanwhile

The minimum is to keep the “adf” hits position and energy after the psa but one can save the short traces to re-run the psa if desired. This has an impact (and this is rate dependent)

✓ Cdat (traces) induce a $\sim 4\%$ dead time at low rate (200 Hz/core) and $\sim 15\%$ at 2-4 kHz/core

✓ Binary (TkT) spectra induce a $\sim 3\%$ dead time at 2-4 kHz/core

Of course if the validation by crystal is low (high trigger condition by fold or ancillaries coincidence), this is not an issue.

At VAMOS, we were validating ~ 100 Hz/core in coincidence with a particle in the spectrometer, therefore these additional outputs have a marginal effect (but fill the disks)

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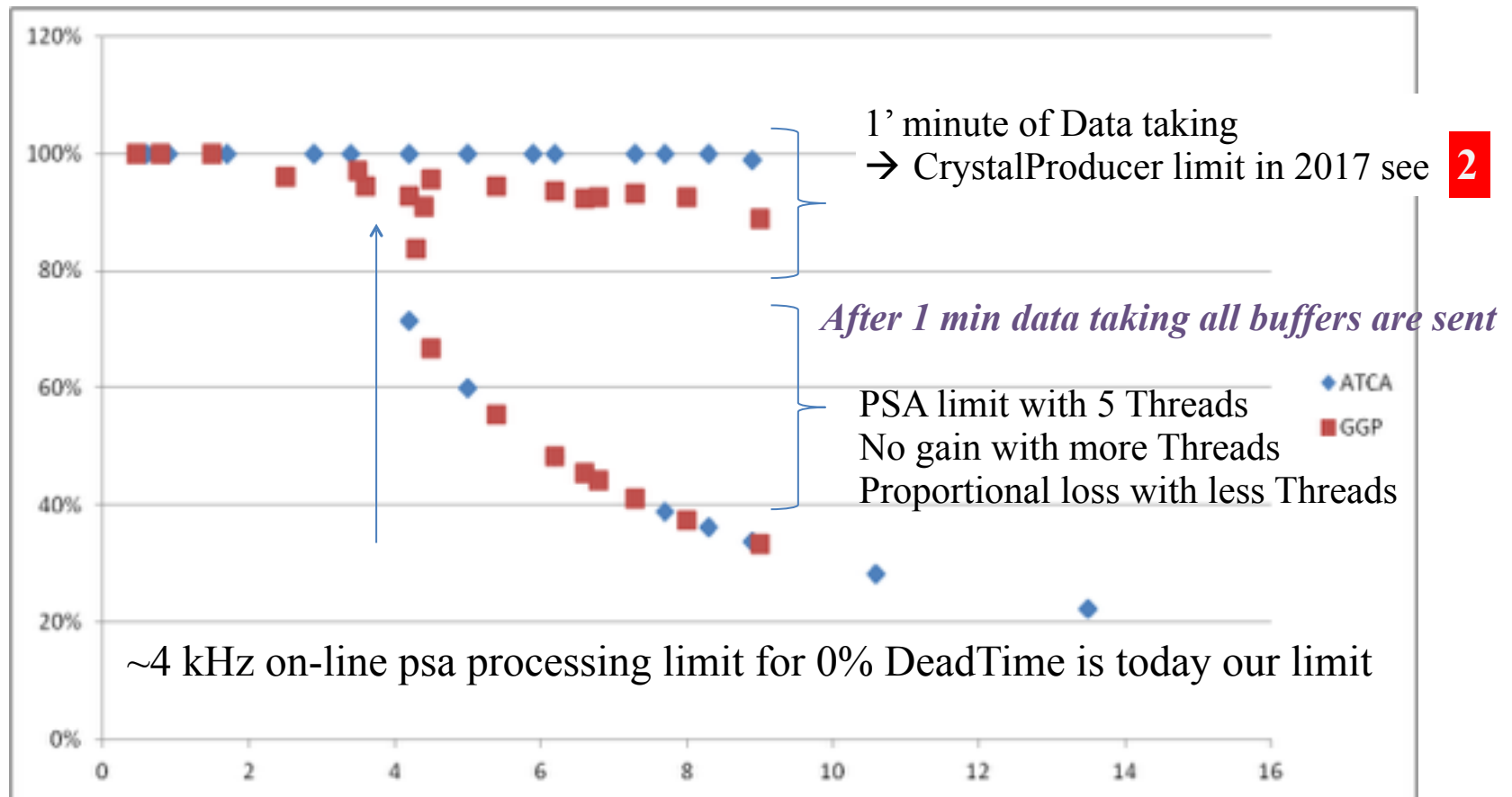
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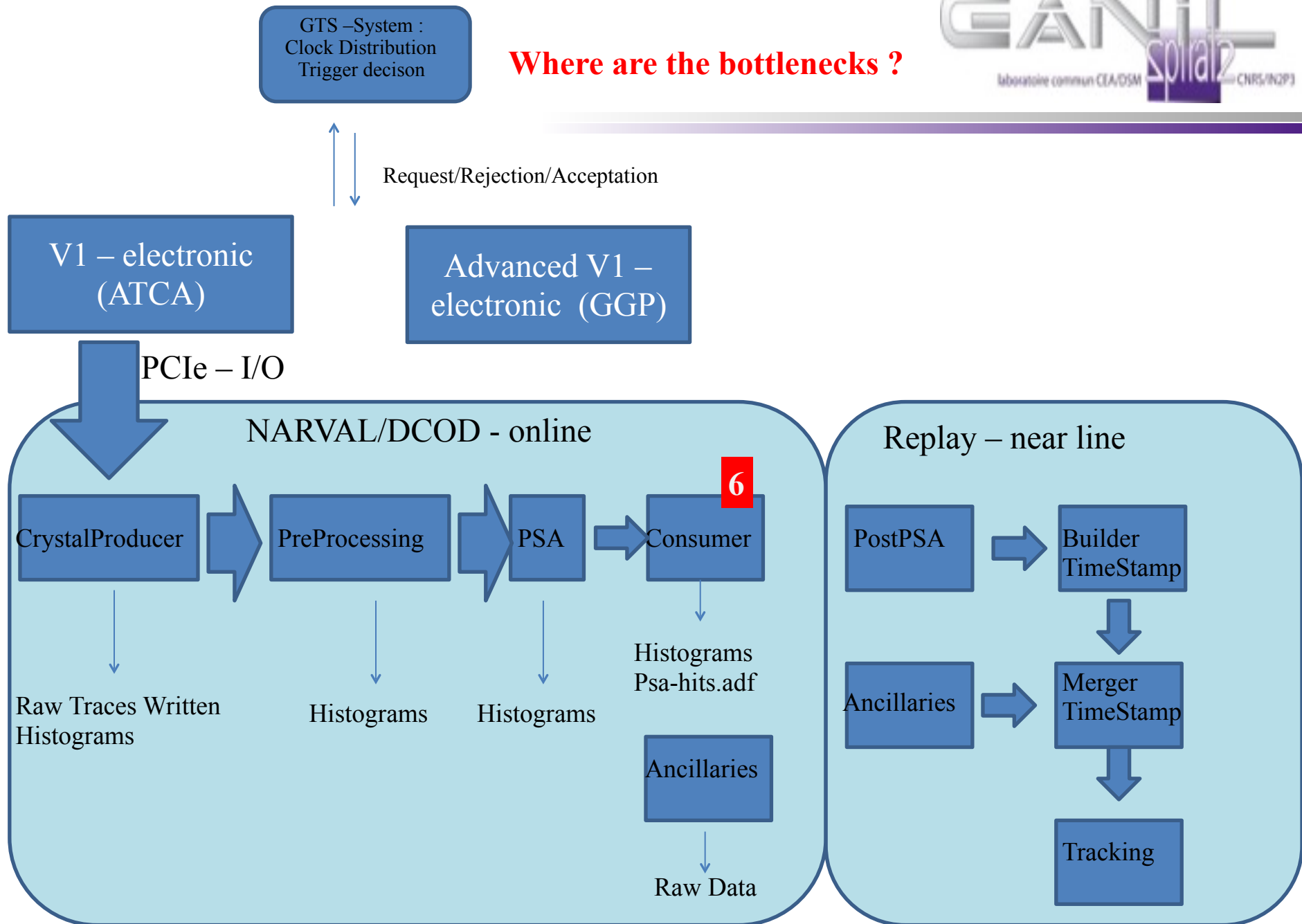
Tracking

The PSA library runs on-line with Threads. We usually run with a configuration per workstation (managing 1 crystal) having 5 threads of 300 events each.

Individual Lifetime



Where are the bottlenecks ?



The current minimum is to keep the output of the local level processing (psa.adf)

Even there, there are limitations (not yet easy to quantify) with online data at high rate

Writing each psa.adf cost backpressure to the system