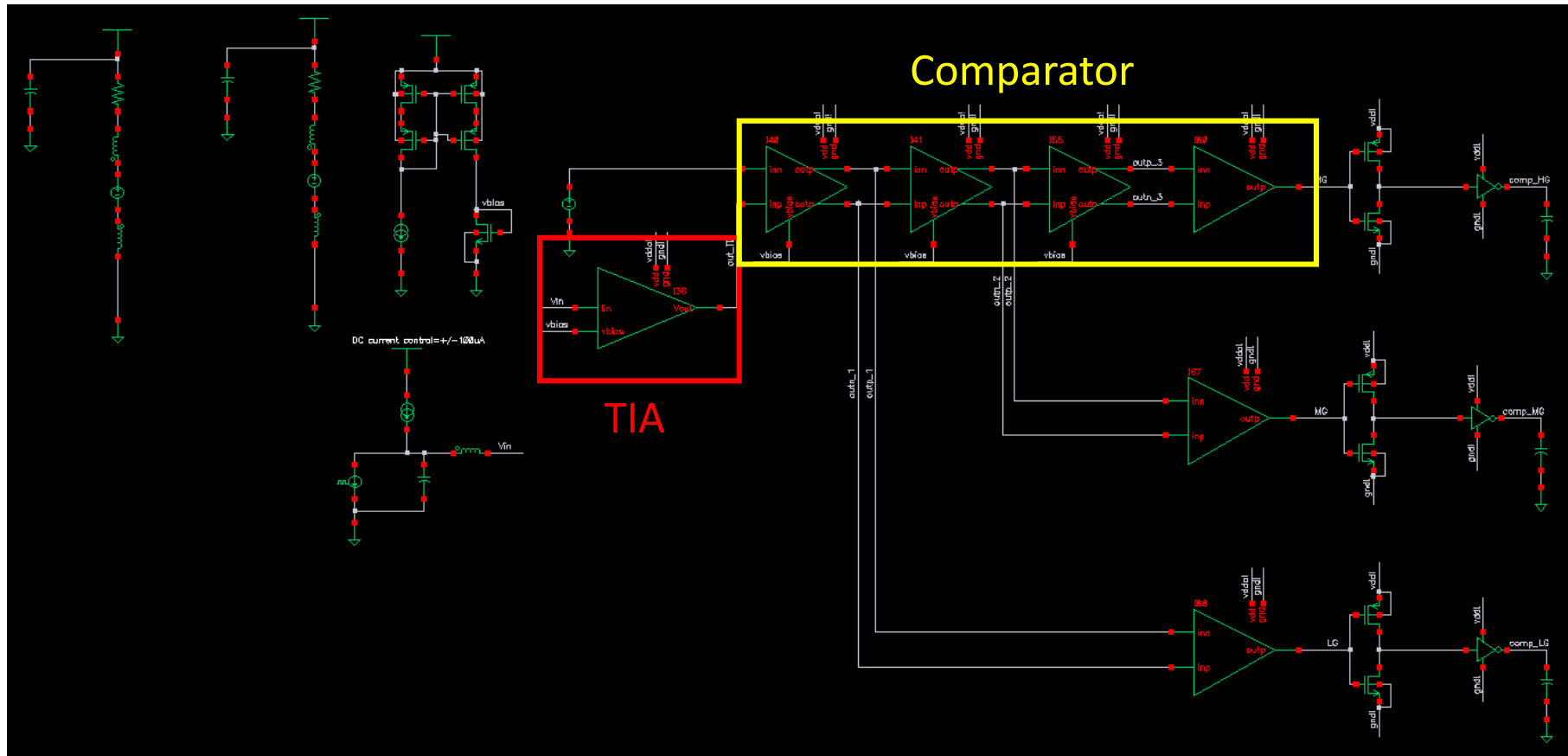


BB130

LPSC Contribution

TIA + CT Comparator

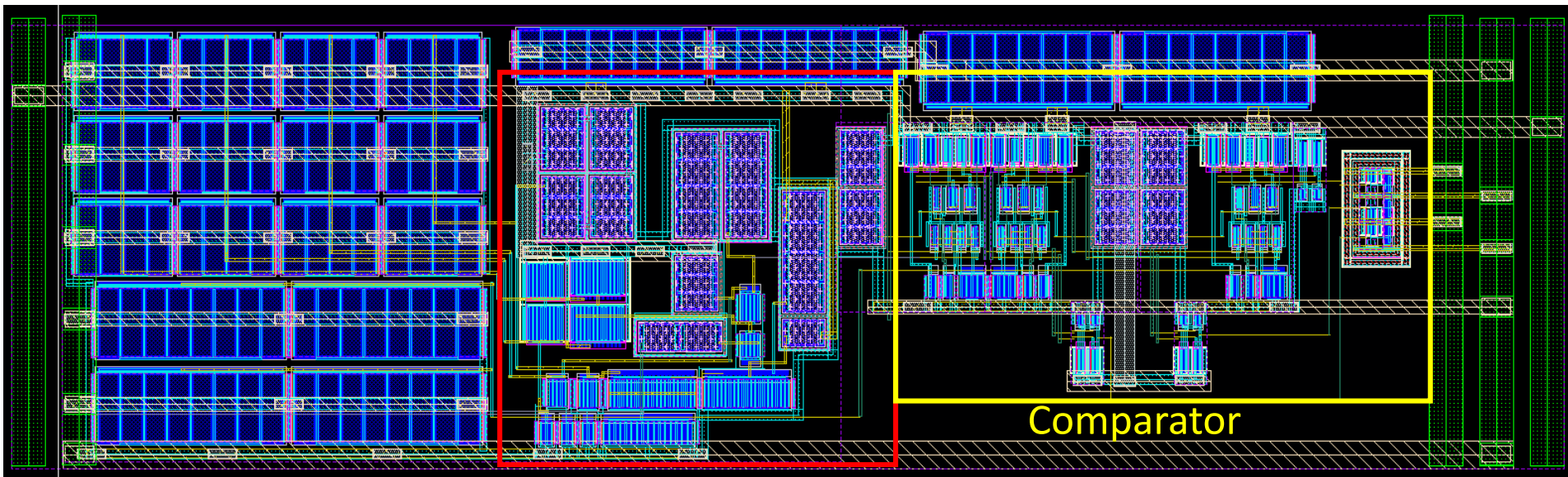


Simulation results: Comparaison

Post-layout –simulation $I_{in}=4\mu A$

[illegible]

1 channel layout (44 x 260 μm^2)



TIA

Noise tran simulation TT extracted

