

CMOS Pixel Sensors for the CBM Micro-Vertex Detector

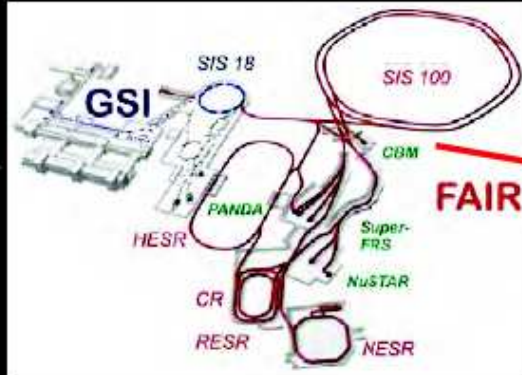
M.Winter / IPHC, 18 May 2017

Contents

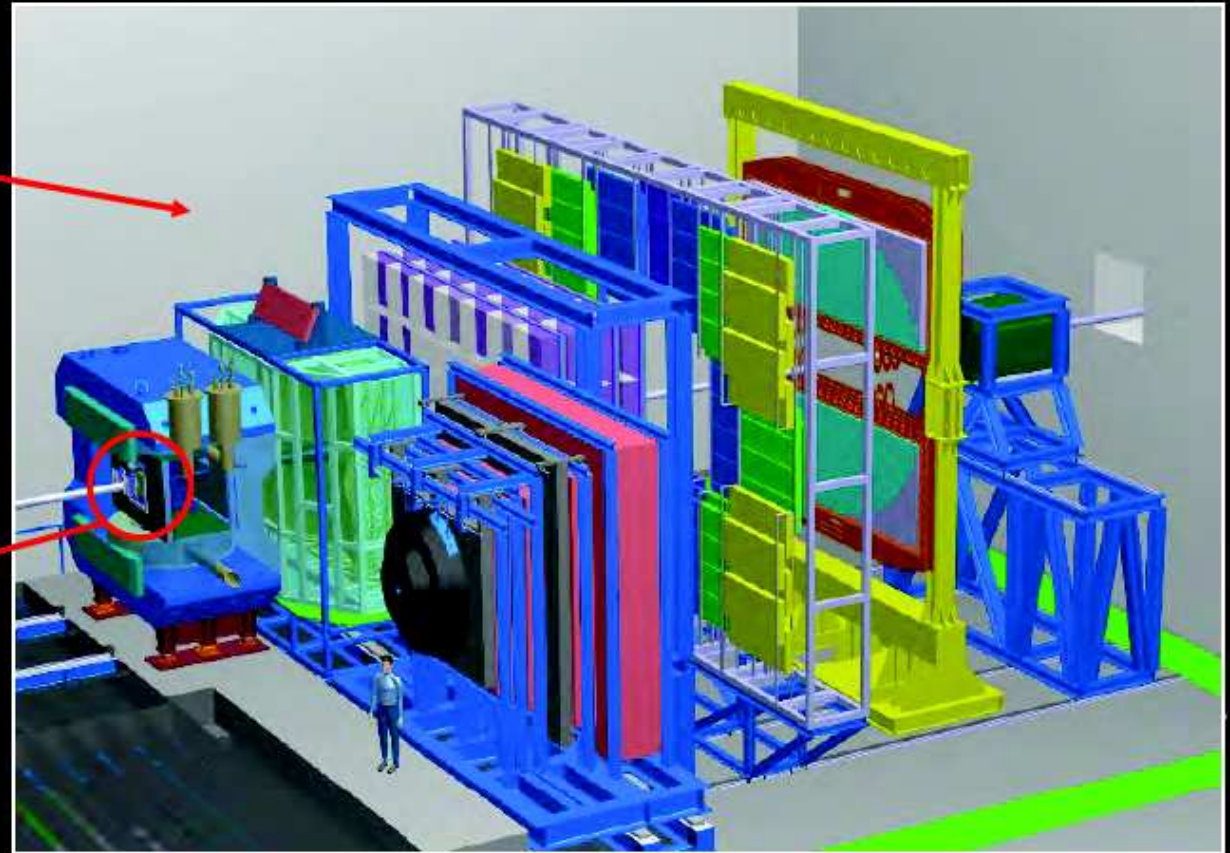
- **Introduction: the CBM-MVD**
- **MIMOSIS: the CMOS Pixel Sensor for the MVD**
- **Development context and plans**
- **Summary**

The CBM Micro-Vertex Detector

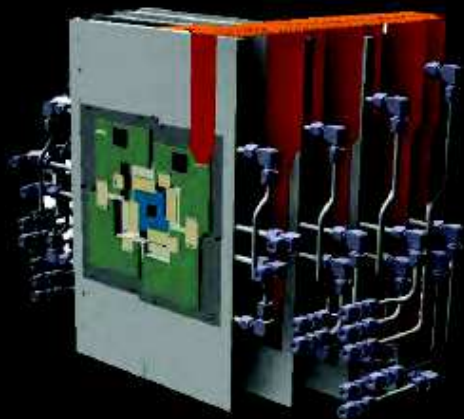
FAIR @ Darmstadt, Germany



The fixed-target CBM experiment

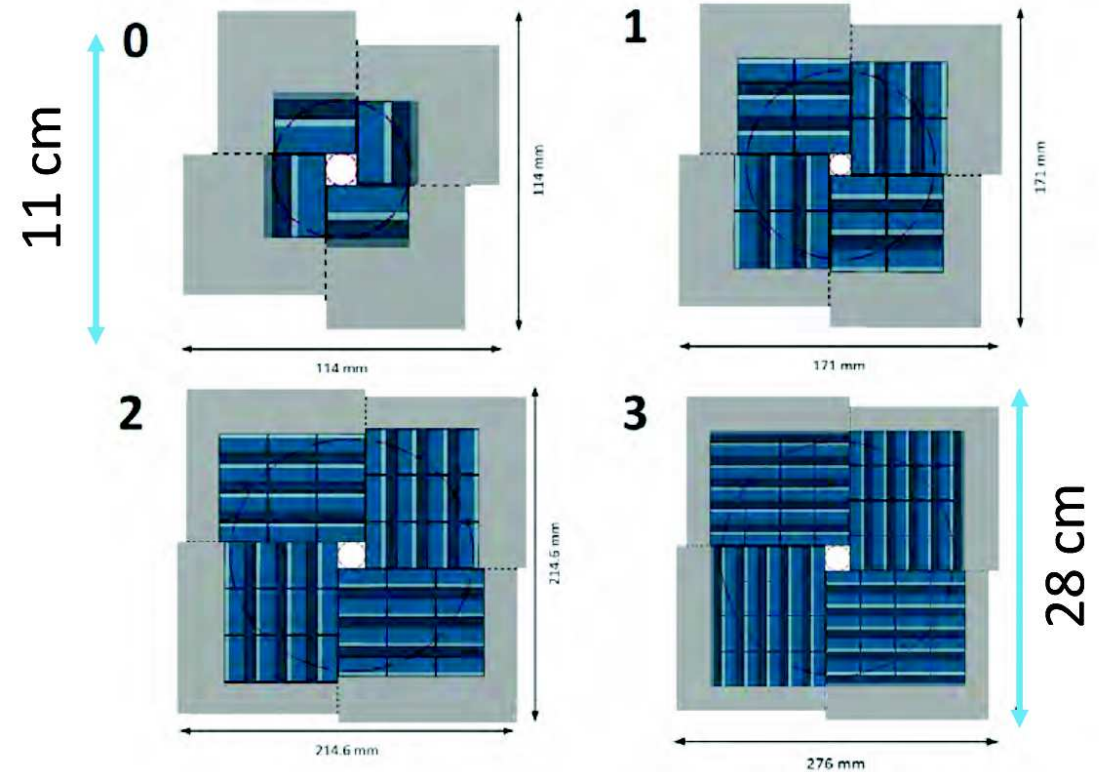
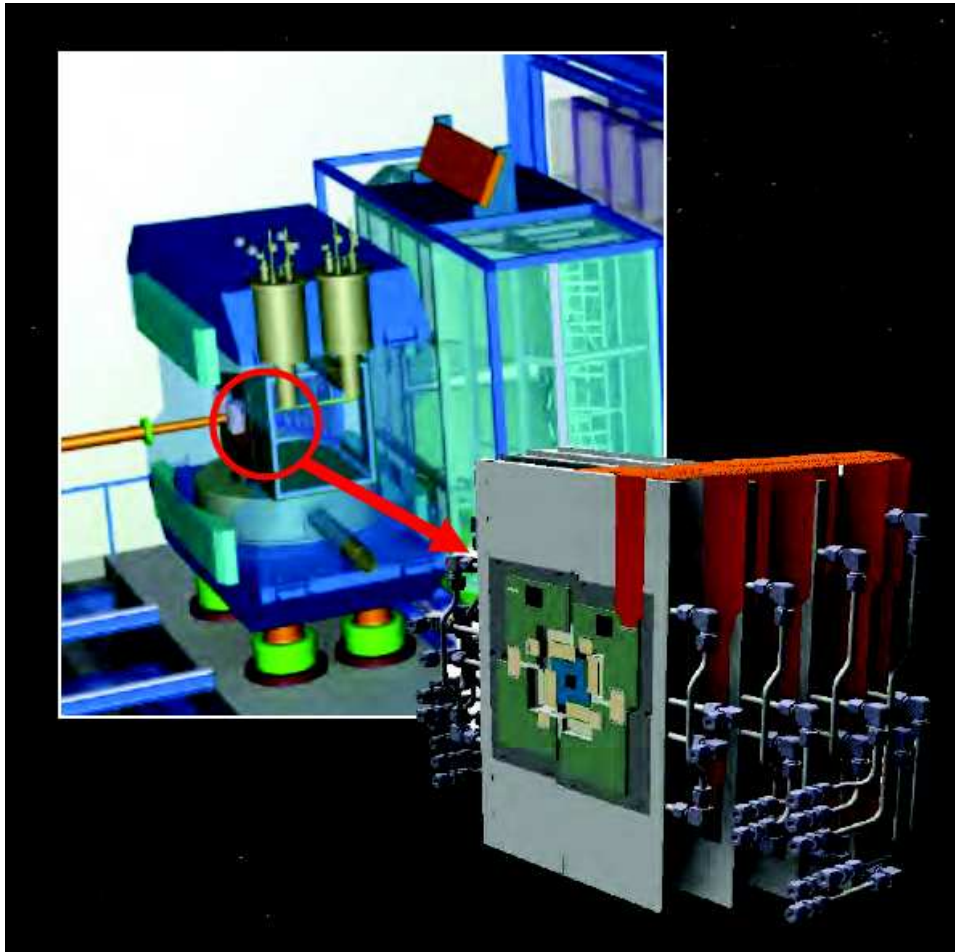


The CBM - MVD



- 4 double-sided stations located at 5, 10, 15, 20 cm behind the target

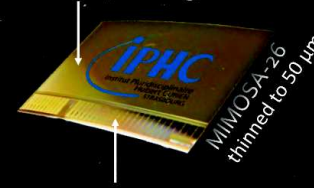
The CBM Micro-Vertex Detector



- MVD based on $50 \mu\text{m}$ thin CMOS sensors
 - single point resolution: $5 \mu\text{m} \times 5 \mu\text{m}$
 - time resolution: $5 \mu\text{s}$
 - power consumption $\lesssim 200\text{--}350 \text{ mW/cm}^2$
 - radiation tol. (-20°C): 3 MRad , $3 \cdot 10^{13} \text{ n}_{eq}/\text{cm}^2$

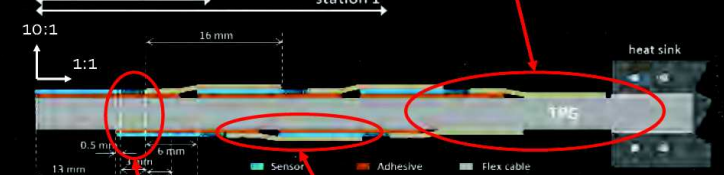
Detector Composition and Integration

- Sensing matrix
- $18.4 \times 18.4 \mu\text{m}^2$ pixel pitch
 - 1152×576 pixel
 - $119.2 \mu\text{s}$ integration time



Detector Composition/ Cross Section:

Geometrical acceptance:
station 0 station 1



Read-out area w/ integrated DAQ

- Threshold Voltages
- Signal discrimination
- Digital data pre-processing
- Slow control

Passive part of sensors
covered by opposite sensor

Very thin cables
power and steer sensors

Holding structure to evacuate
heat (no air cooling in vacuum)

heat sink

CMOS Pixel Sensors (CPS): Main Features

- **CPS fabricated via industrial processes used for ASIC production**

- Thin sensitive volume (e.g. 20 - 40 μm)
- Full signal processing micro-circuitry integrated on chip (low noise !)
 - ⇒ Very modest material budget: $\sim 0.05 \% X_0$

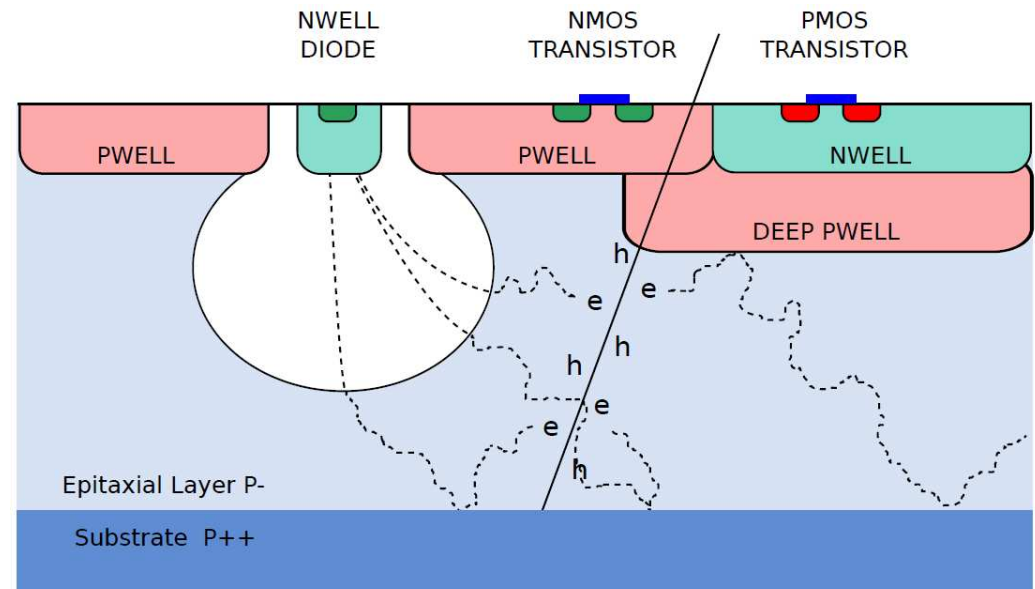
- **CPS provide $O(\mu m)$ spatial resolution**

- Small pixels: typically 20-30 $\mu m \times 20-30 \mu m$
- Charge sharing between neighbouring pixels
 - ⇒ binary charge encoding often sufficient

- **Read-out speed & radiation tolerance**

- Usually the main R&D drivers
- In conflict with small pixels, low power, thin sensitive volume, etc.

- **CPS used successfully in STAR-RHIC/BNL (2014-16), presently produced for ALICE-ITS&MFT (30,000 sensors)**



CPS Design Required for the MVD: MIMOSIS Sensor

- **MIMOSIS derived from ALPIDE pixel array read-out architecture (ITS, MFT)**
- **Required radiation tolerance significantly higher than ALPIDE**
($\gtrsim 10$ times higher locally)
- **Required data throughput ~ 10 times higher than ALPIDE**
(hit density actually up to $\sim 10^2$ times higher locally)

	ITS (IB)	CBM (1 st station)
Radiation Load TID	~ 270 krad	3 Mrad @ -20°C, 1 Mrad @ +30°C
Radiation Load NIEL	$\sim 1,7 \times 10^{12}$ 1MeV n_{eq}/cm^2	3×10^{13} n_{eq}/cm^2 @ -20°C, 1×10^{13} n_{eq}/cm^2 @ +30°C
Peak hit rate	$\sim 1,25 \times 10^6/cm^2/s \rightarrow 1,25 \times 10^4/mm^2/s$	$7 \times 10^5/mm^2/s$ (x 56 times than ITS)
Trigger	yes	no
Heavy Ion Effect		

- **Fully revisited digital circuitry (data sparsification & transfer logic)**

IPHC Team developing the MIMOSIS Sensor

- **Development started in 2003**

↳ R&D went through numerous different intermediate applications

- **IPHC-PICSEL team members involved presently:**

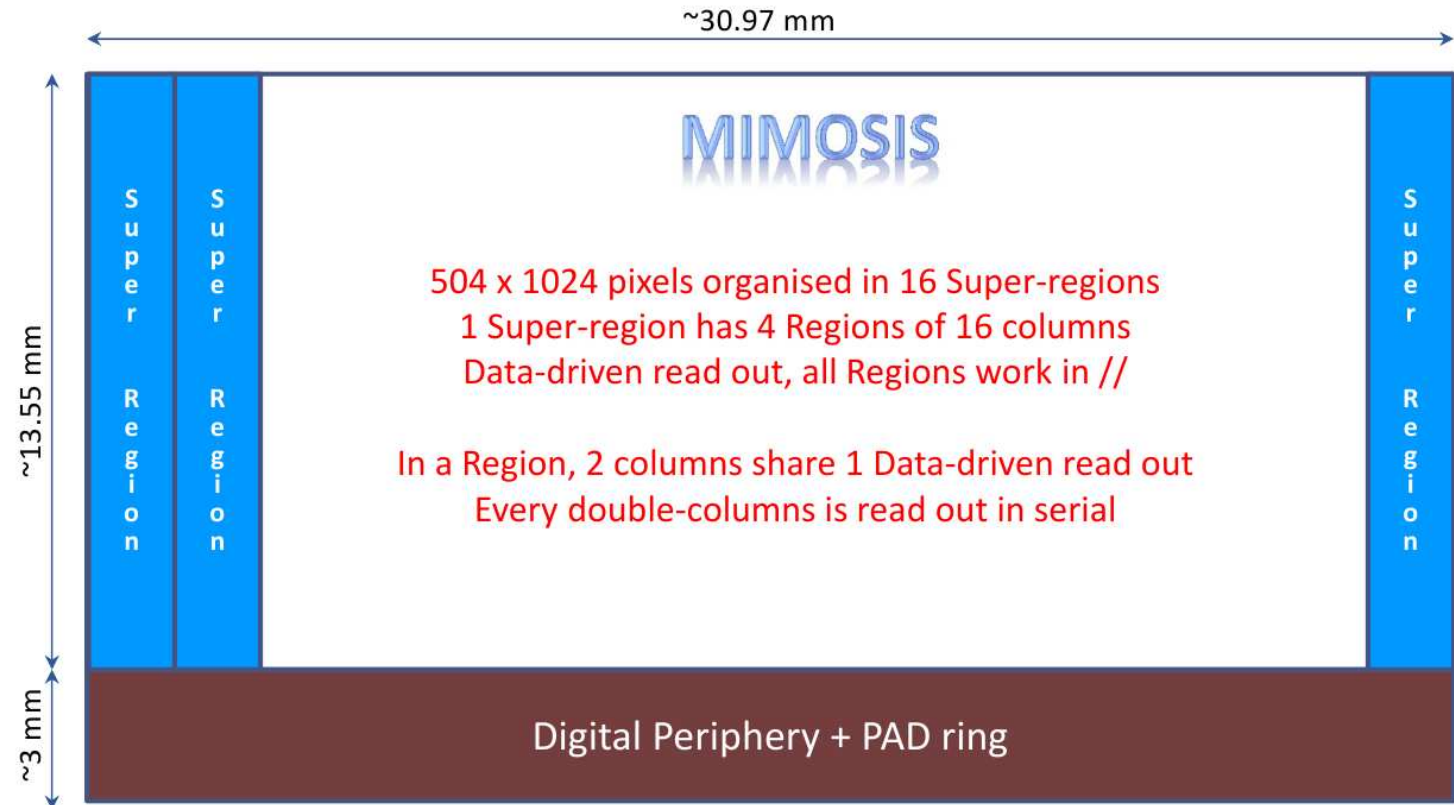
- physicist: M.Winter
- μ -circuit designers: A.Dorokhov, G.Dozière, A.Himmi, Ch.Hu-Guo, F.Morel, H.Pham, I.Valin, Y.Zhao
- electronics engineers: G.Claus, M.Goffe

- **Responsibility: realisation of the MVD sensor MIMOSIS**

- sensor design
- foundry runs of consecutive prototypes
- electronics/functionnal tests of prototypes
- prototypes' detection performance assessment (in collaboration with IKF-Frankfurt)

MIMOSIS Overview

- **Pixel dimensions**
 $26.88\ \mu m \times 30.24\ \mu m$
- **In-pixel discrimination**
- **Binary charge encoding**
- **Data driven read-out**



- **MIMOSIS development plan:**

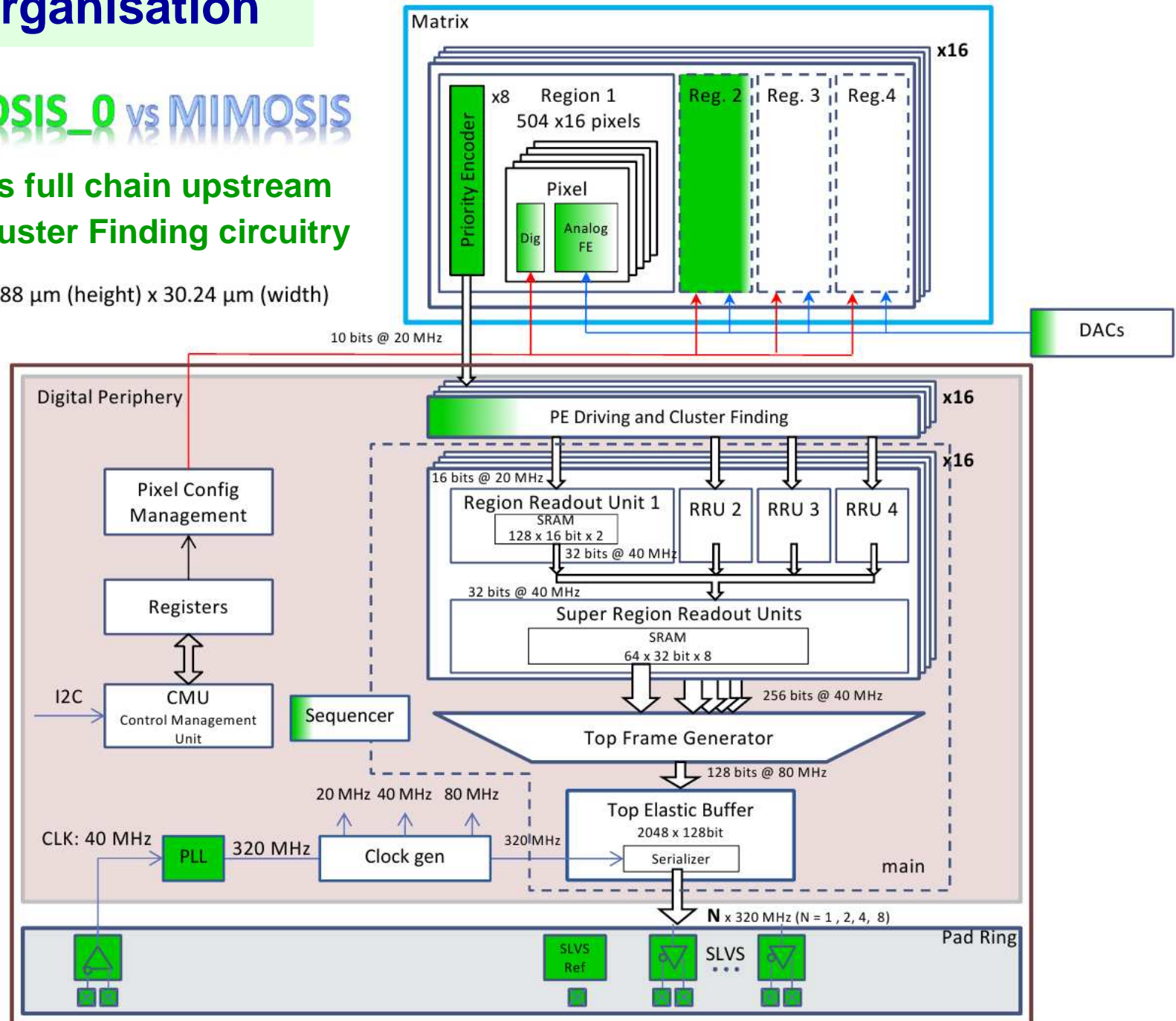
- MIMOSIS-0: portion of pixel array with 2 diff. pixel designs ➞ submitted in May/'17
- MIMOSIS-1: 1st prototype of complete sensor, to be submitted in \gtrsim Q2/'18
- MIMOSIS-2: 2nd prototype of complete sensor, to be submitted in \gtrsim Q2/'19
- MIMOSIS-3: final sensor pre-production, to be submitted early in 2020

Overall Sensor Organisation

1. MIMOSIS_0 vs MIMOSIS

Includes full chain upstream
from Cluster Finding circuitry

Pixel dimension: 26.88 μm (height) x 30.24 μm (width)



SUMMARY

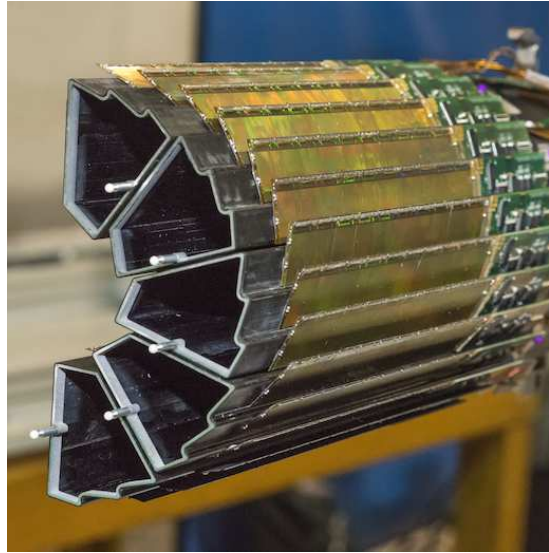
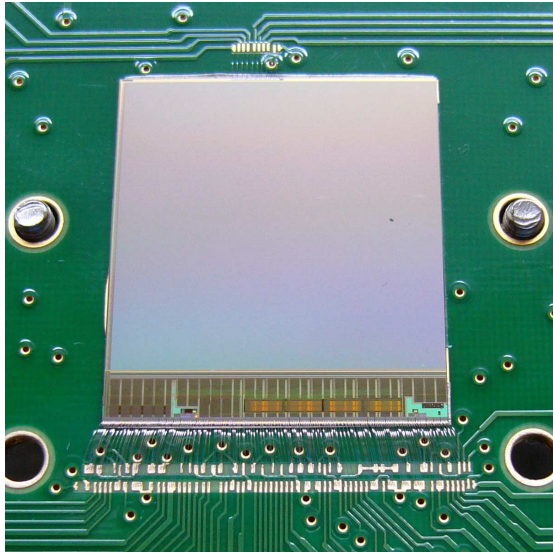
- **CBM-MVD:**
 - requires very high precision in challenging running conditions
 - will be equipped with CPS designed and developed at PHC-Strasbourg
 - based on > 10 years of fruitful partnership between IPHC and IKF(+GSI)
- **Technological aspects:**
 - well defined CPS development plan until 2020
 - challenging development: no table ready solution available
 - will become the state-of-the-art (CPS, whole detector)
- **Partnership extension :**
 - CPS detection performance and radiation tolerance assessment
 - contribution to system integration studies and preparation for assembly
 - N.B.: acquired expertise addresses a very promising instrumentation domain
 - numerous spin-offs

Achievement: MIMOSA-28 & STAR-PXL Detector (+ spin-offs)

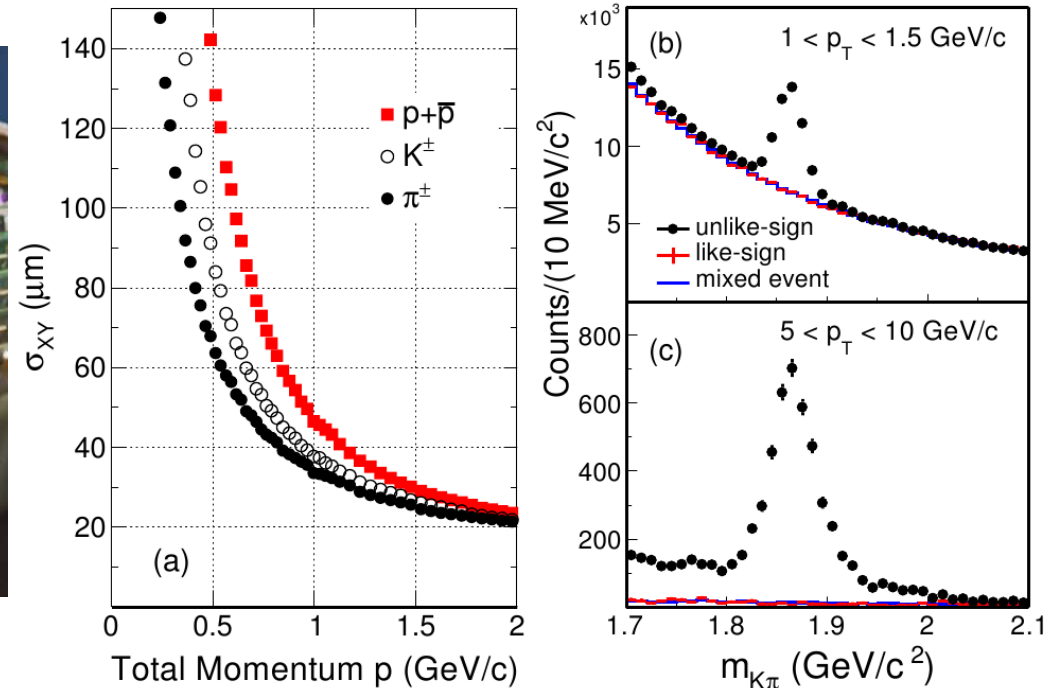
- MIMOSA-28: 1st CPS equipping a subatomic phys. experiment (STAR at RHIC/BNL)

$\sigma_{R\Phi,Z} \simeq 3.7 \mu m$; thickness $\simeq 50 \mu m$; 970,000 pixels over $2 \times 2 \text{ cm}^2$; $> 10^6 \text{ part./cm}^2/\text{s}$

3 date taking campaigns (2014–16) \Rightarrow state-of-the-art of the technology



STAR Au+Au @ 200 GeV, 0-80%

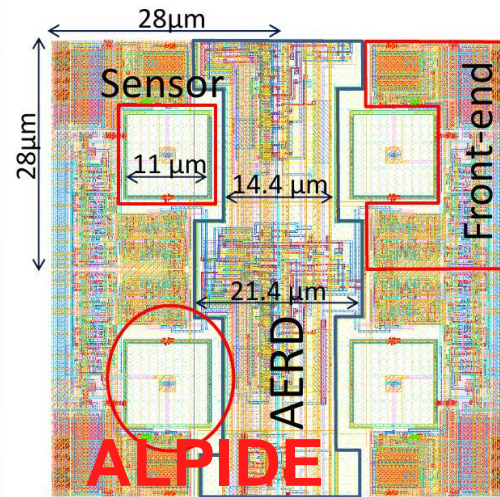
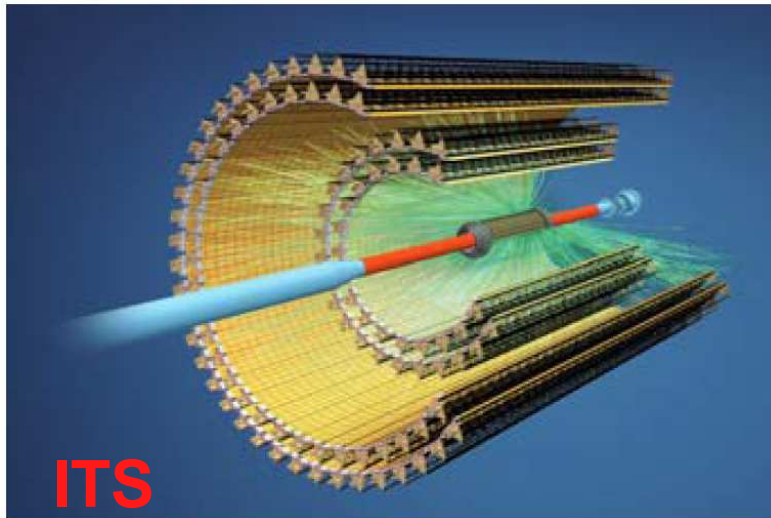


- MIMOSA-28 equips numerous devices, e.g.:

- AIDA BT: 4 millions of pixels per plane ($4 \times 4 \text{ cm}^2$, $< 0.1\% X_0$)
- BT part of LNF permanent infrastructure (450 MeV e^-)
- telescope for hadrontherapy (GSI), etc.
- demonstrator for inner tracker upgrade of BES-3 expt. at BEPC/IHEP

Achievement: 2 sensors for ALICE-ITS (LHC)

- Objective: 1st pixellated inner tracker ($> 10 \text{ m}^2$ sensitive area, 25,000 CPS)
- More demanding requirements than for STAR-PXL (MIMOSA-28)
 - ⇒ evolution toward more advanced technology: TowerJazz $0.18 \mu\text{m}$
- 2 approaches:
 - MISTRAL: extension of MIMOSA-28 (STAR-PXL) – validated & robust
 - ALPIDE: extrapolation from hybrid pixels – higher perfo. but less robust & understood



	σ_{sp}	$t_{r.o.}$	Dose	Fluency	T_{op}	Power	Active area
STAR-PXL	$< 4 \mu\text{m}$	$< 200 \mu\text{s}$	150 kRad	$3 \cdot 10^{12} \text{ n}_{eq}/\text{cm}^2$	30-35°C	160 mW/cm ²	0.15 m ²
ITS-in	$\lesssim 5 \mu\text{m}$	$\lesssim 30 \mu\text{s}$	2.7 MRad	$1.7 \cdot 10^{13} \text{ n}_{eq}/\text{cm}^2$	30°C	$< 300 \text{ mW}/\text{cm}^2$	0.17 m ²
ITS-out	$\lesssim 10 \mu\text{m}$	$\lesssim 30 \mu\text{s}$	15 kRad	$4 \cdot 10^{11} \text{ n}_{eq}/\text{cm}^2$	30°C	$< 100 \text{ mW}/\text{cm}^2$	$\sim 10 \text{ m}^2$

Measured Spatial Resolution

- Several parametres govern the spatial resolution :

- pixel pitch
- epitaxial layer thickness and resistivity
- sensing node geometry & electrical properties
- signal encoding resolution

$\Rightarrow \sigma_{sp}$ fct of pitch \oplus SNR \oplus charge sharing \oplus ADCu, ...

- Impact of **pixel pitch** (analog output) :

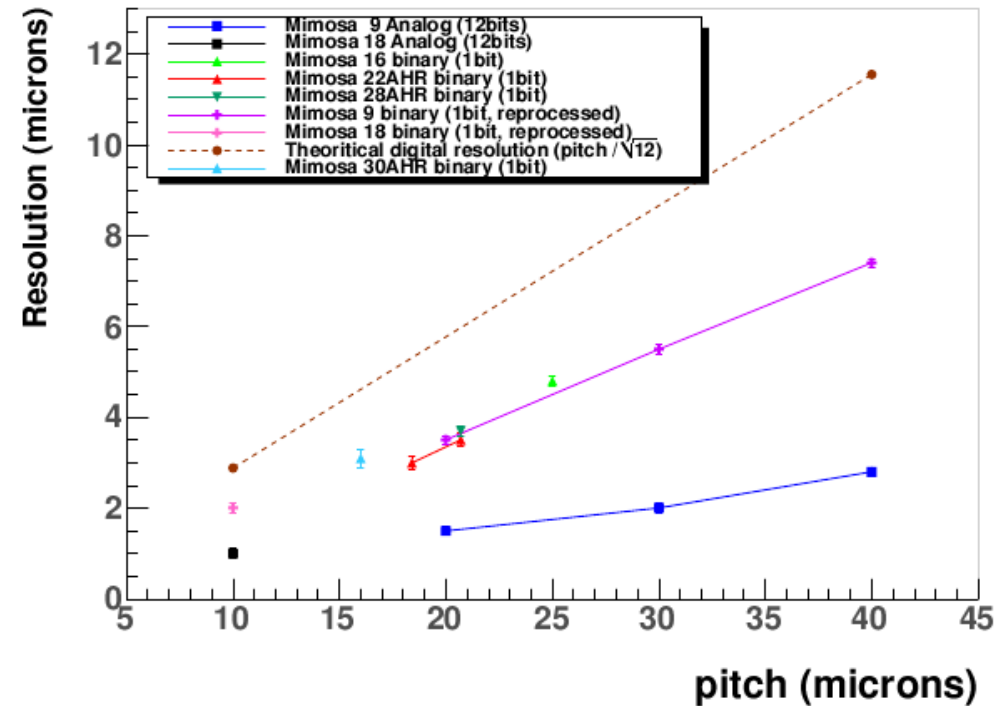
$\sigma_{sp} \sim 1 \mu m$ (10 μm pitch) $\rightarrow \lesssim 3 \mu m$ (40 μm pitch)

- Impact of **charge encoding resolution** :

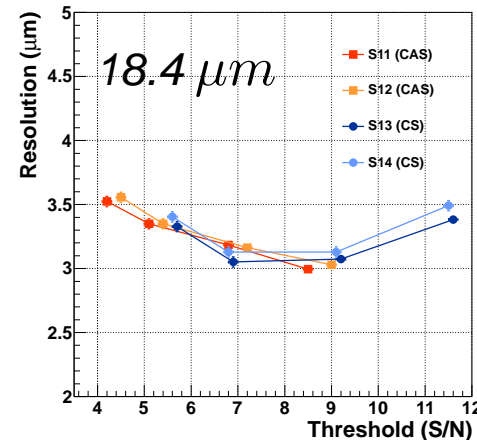
\triangleright ex. of 20 μm pitch $\Rightarrow \sigma_{sp}^{digi} = \text{pitch} / \sqrt{12} \sim 5.7 \mu m$

Nb of bits	12	3-4	1
Data	measured	reprocessed	measured
σ_{sp}	$\lesssim 1.5 \mu m$	$\lesssim 2 \mu m$	$\lesssim 3.5 \mu m$

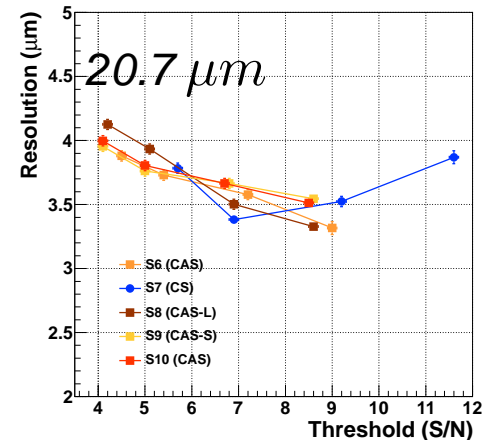
Mimosa resolution vs pitch



Resolution vs Threshold

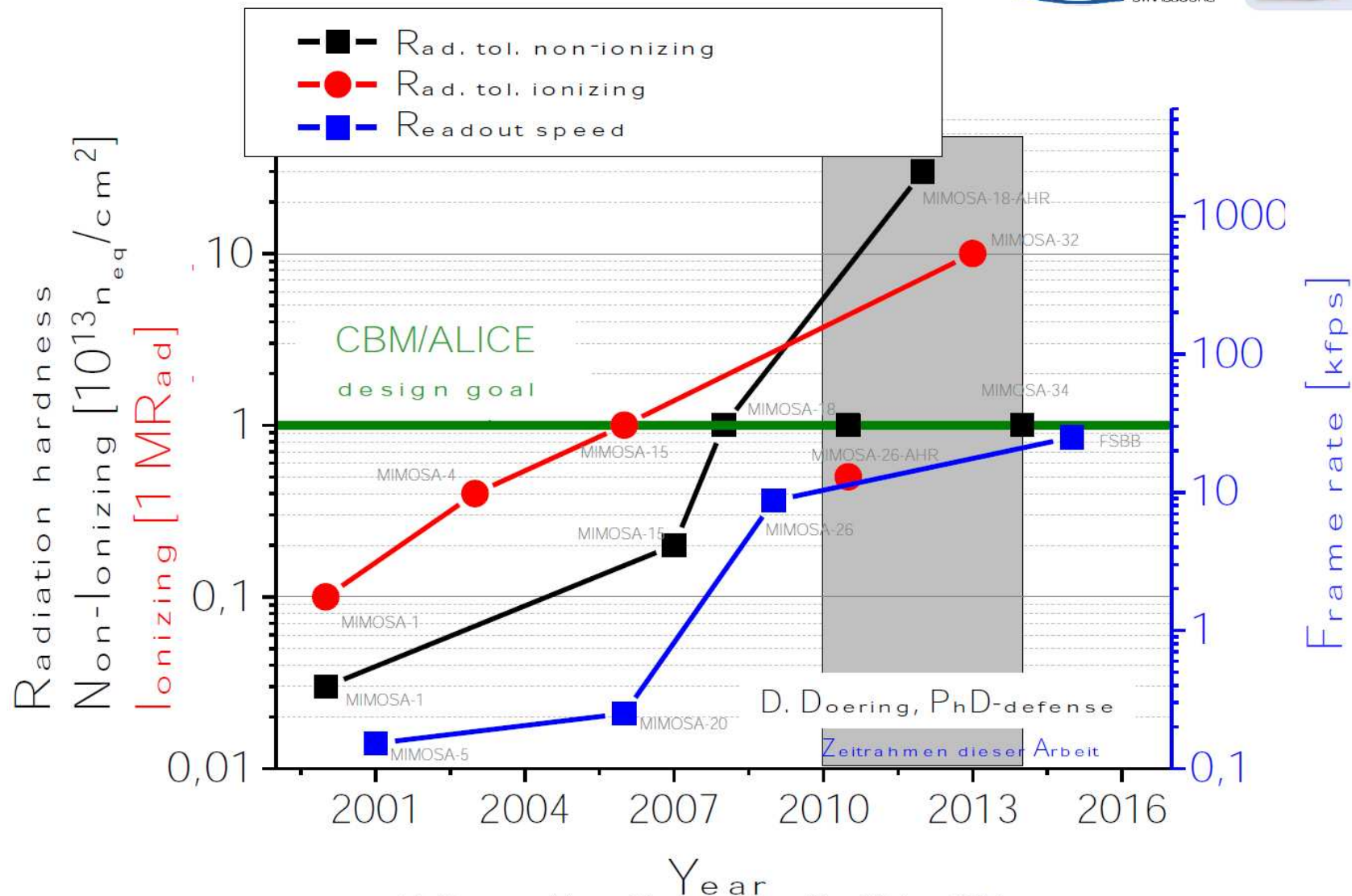


Resolution vs Threshold



Radiation Tolerance

Material budget: $0.05\% X_0$
 Spatial resolution: $\sim 3\text{-}5\ \mu\text{m}$



Speed vs Pixel Dimensions

- Pixel dimensions govern the spatial resolution at the expense of read-out speed

⇒ Trade-off to be found specific to each application

Pixel pitch	$< 10 \mu m$	$\gtrsim 15 \mu m$	$> 20 \mu m$	$\gtrsim 25 \mu m$	$\lesssim 50 \mu m$
Nb(T)	2–3	15	$\gtrsim 50$	$\gtrsim 200$	HV: few 10^2
$\sigma_{sp} [\mu m]$	$\lesssim 1 \times 1$	$< 3 \times 3$	$< 5 \times 5$	$\lesssim 5 \times 5$	$\gtrsim 10 \times 10$
$\Delta t [\mu s]$	10^3	$\lesssim 30/200$	$\gtrsim 10-15$	< 10	10^{-2}
Pre-Amp+Filter	Out	In-Pix	In-Pix	In-Pix	In-Pix
Discrimination	Out	Out	In-Pix	In-Pix	In-Pix
Sparsification	Out	Out	Out	In-Pix	In-Pix
Ex.(chip)	Mimosa-18	ULTIMATE/MISTRAL	ASTRAL	ALPIDE	HV-CMOS
Depleted	No	No	No	Yes	YES
CMOS Process	AMS-0.35	AMS-0.35/Tower-0.18	Tower-0.18	Tower-0.18	AMS-0.35/0.18
Ex.(appli.)	Beam Tele.	STAR-PXL/ALICE-ITS	ALICE-ITS	ALICE-ITS	LHC ?