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# 1-Short Slab design « BGA»



#### ⇒ The constant:

1.0 – Ecal thickness constant <u>A</u>		A	$\int$
4x0,22mm C+ 1x15,3mmC+ 3x0,22mmC- 1x0,1mm epoxy <u>B</u>	+ = <u>16,94mm</u>	В	<b>1</b>
4x0,22mm C + 1x0,1mm epoxy + W1 + 4x0,22mmC + 1x0,1mm epoxy <u>C</u> SLAB 8,53mm + W1 + 0,4mm gap	= <u>1,96mm + W1</u> = <u>8,53 mm + W1</u>	<b>C</b>	<b>1</b>
<u>D</u> SLAB 8,53mm + W2 + 0,4mm gap	= <u>8,53 mm + W2</u>	G	×
<u>E</u> 4x0,22mm C + 1x0,1mm epoxy + W2 + 4x0,22mmC + 1x0,1mm epoxy	= <u>1,96mm + W2</u>	D	Ŷ
<u>F</u> 2x0,22mm C + 1x0,1mm epoxy + 1x3,5mm 1x0,1mm epoxy + 1x2mm C+ 4x0,22mm	$mW+$ $C = \underline{7,02 \ mm}$	E	<b>1</b>
<u>G</u> 16,94mm + N1 x(1,96 mm+W1) + N1 x(8, N2 x(8,53mm+W2) + N2 x(1,96 mm+W2	.53mm+W1) + ) + 7,02 = <u>?</u>		

N1= number of tungsten W1 layer thickness 1 N2= Number of tungsten W2 layer thickness 2



## 1-Short Slab design « BGA»



#### ⇒ The goal:

#### 1.1 – Approach the final design with the short SLAB

Have the same part of the long slab (as much as possible) \_ Cover 0,1mm Provide for the management of all these parts for assembly Shielding Ensure the industrialization of all the parts \_ FEV10 16 chips kapton Mechanical bilge **Mechanical** attachment DIF SMBV4 **Kapton HV** (LAL/LLR) **Carbon U** support (LLR) **Mechanical** plug



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## 3-Sectional view of the BGA slab



#### 3.0 – SLAB THICKNESS



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### $\Rightarrow$ News: 4.0 – Long slab fully in carbon



We are now able to build a H We have already built one long H fully in carbon In 2015, we will produce one long slab with W.



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### *5-Test beam 2015*

#### ⇒ The goal:

5.0 – Next test beam (if electronic test and cosmic test are OK)

- 10 short SLAB with 4 wafers per slab.
- Possible to add 4 short slab but 1 wafer per slab.
- 1 long SLAB with only 4 wafers





## 6-ASU Setup"4 wafers without glue"



#### ⇒ The goal:

- 6.0 Setup option with support of test electric probes for connecting WAFER to FEV
  - Realize an assembly with removable wafer in order to acquire cosmic data. This assembly will test the entire acquisition chain (Wafer-FEV-SMBV4-DIF-GDCC-CCC-PC-Software) before the wafer gluing operation (or not). LLR has built 4 setup like this (laser injection example)



## 6-ASU Setup"4 wafers without glue"



⇒ The goal:

6.1 – Setup option with support of test electric probes for connecting WAFER to FEV to SMBV

• Very good reproducibility of the contacts! (30 min for testing 1 FEV 16 chip and 4 wafer)

Dif

- Very reasonable noise >15 with a gain of 1.2pF
- 1024 achievement Scurves in parallel !



### 7-Slab for Test beam 2015



⇒ The goal:

#### 7.0 – Testing the response of the short SLAB with 4 wafer in particular zone



### 7-Slab for Test beam 2015



10 Covers pressure

 $\Rightarrow$  The goal:

7.1 – Testing the response of the Long SLAB with 1 and perhaps 40 wafers!

- quality of the first wafer signal relative to the last wafer
- Specific Pixel of wafers (corners)
- DAC Testing..... \_
- and surprises!!!





### Conclusion

#### ⇒ Conclusion:

- In 2015, we will produce the next long slab in carbon and another one in Tungsten
- We have found an industrial solution for build the cover of 0,1mm thickness and 0,3 m of length, the next step is 1m of length.
- All the 4 ASU setup are completed, it 's now possible to acquire data without sticking 4 wafers of 1024 pixels.
- It's now possible to test all the function of an ASU in less than <sup>1</sup>/<sub>2</sub> H!
- We will prepare the future test beam of 2015 "if the production of all slab is ok"