



Digitiser report

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What exists, where and what state.

Firmware status

Time over Threshold.

Building Digitisers.

What exists, where and what state.

➤ Legnaro :

- ❖ 14 working, 1 waiting return of Segment module.

➤ Orsay :

- ❖ 1 being used for testing pre-processors.

➤ Daresbury UK :

- ❖ 2 digitisers and one Segment module are being repaired and tested as time allows.

Firmware status

Experiment mode firmware changes for the Segment and Core ADC cards are produced from time to time as new functions are requested.

- Segment changed 15/01/2010.
 - The Preamp Inhibit signal transmission to the Pre-Processing is disabled by default in all six channels. It can be enabled by setting bit7 in the module control register.



Time Over Threshold

- The LLP collaboration has specified a method of transferring data other than continuous ADC values across the link from the Digitiser to the Pre-Processor
- The firmware to collect time over threshold, TOT, data has been developed by Ian Brawn of STFC.
- The firmware has been successfully tested by CSNSM Orsay.





Building Digitisers

- 11 Digitisers are being built at Daresbury. Changes have been made to the electronics to incorporate current modifications.
 - ❖ 2 are for Legnaro.
 - ❖ 1 is for Padova.
 - ❖ 8 are for STFC at Daresbury.
- The manufacture of electronics is complete and testing is in progress.
- Assembly of Digitisers will start in January.
- Delivery is expected in March.